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(12) **United States Design Patent** (10) **Patent No.:** **US D896,217 S**  
**Jaggers et al.** (45) **Date of Patent:** **\*\* Sep. 15, 2020**

(54) **HOUSING OF AN ELECTRONIC DEVICE**

(56) **References Cited**

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(\*\*) Term: **15 Years**

(Continued)

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(21) Appl. No.: **29/723,393**

(57) **CLAIM**

(22) Filed: **Feb. 6, 2020**

The ornamental design for a housing of an electronic device, as shown and described.

**Related U.S. Application Data**

(62) Division of application No. 29/615,083, filed on Aug. 25, 2017, now Pat. No. Des. 878,359, which is a division of application No. 29/548,331, filed on Dec. 11, 2015, now Pat. No. Des. 798,904.

**DESCRIPTION**

(51) **LOC (12) Cl.** ..... **14-02**  
(52) **U.S. Cl.**  
USPC ..... **D14/300**  
(58) **Field of Classification Search**  
USPC ..... D14/300-302, 308, 348, 353-355;  
D5/1-4, 47, 53, 58, 61  
CPC .. G06F 1/203; H05K 7/1488; H05K 7/20736;  
H05K 7/20781; H05K 5/0021; H05K  
5/0052

FIG. 1 is a top perspective view of a housing of an electronic device;  
FIG. 2 is a bottom perspective view of a housing of an electronic device;  
FIG. 3 is a front view of a housing of an electronic device;  
FIG. 4 is a rear view of a housing of an electronic device;  
FIG. 5 is a first side view of a housing of an electronic device;  
FIG. 6 is a second side view of a housing of an electronic device;  
FIG. 7 is a top plan view of a housing of an electronic device; and,

See application file for complete search history.

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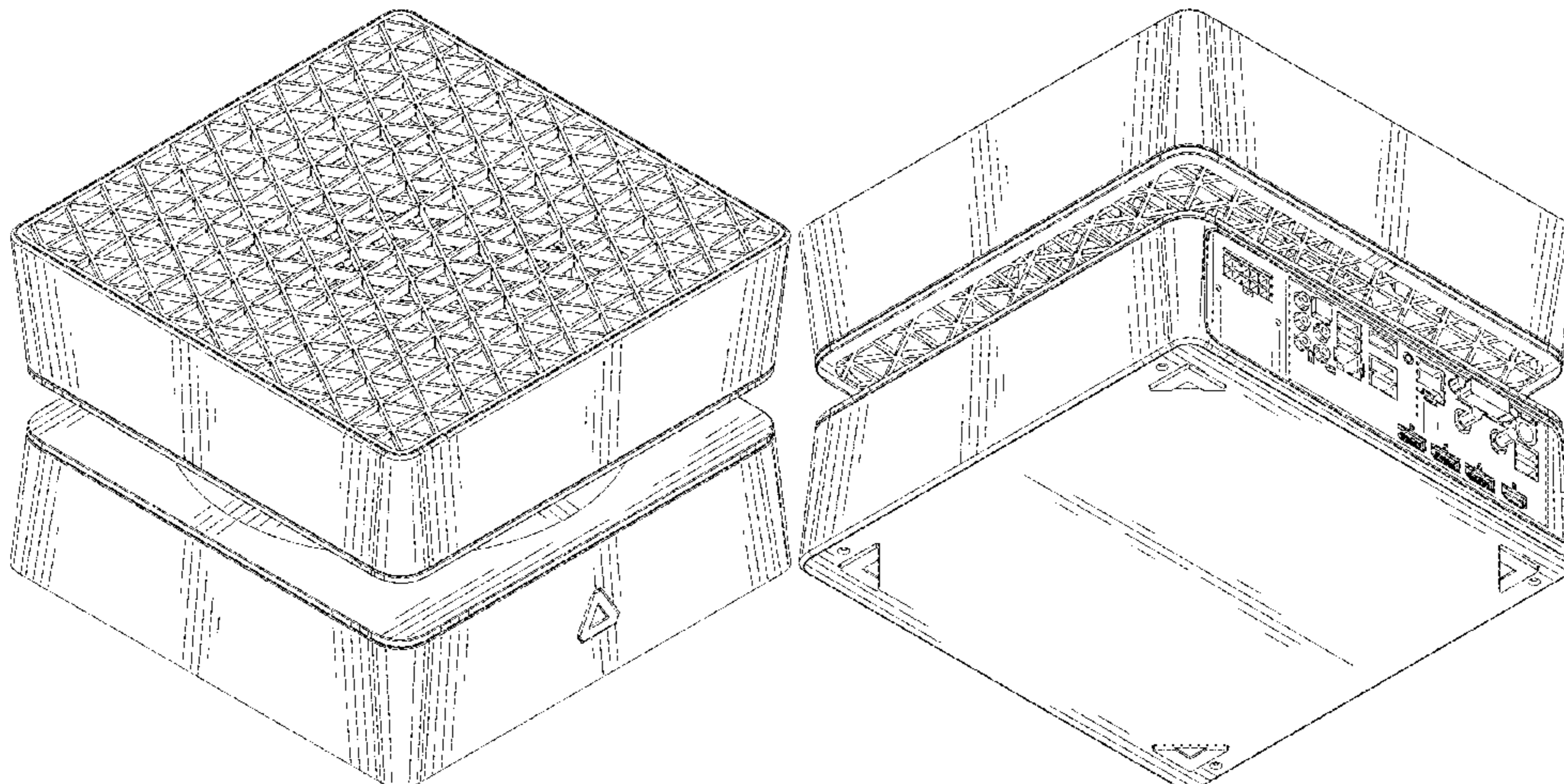


FIG. 8 is a bottom plan view of a housing of an electronic device.

1 Claim, 6 Drawing Sheets

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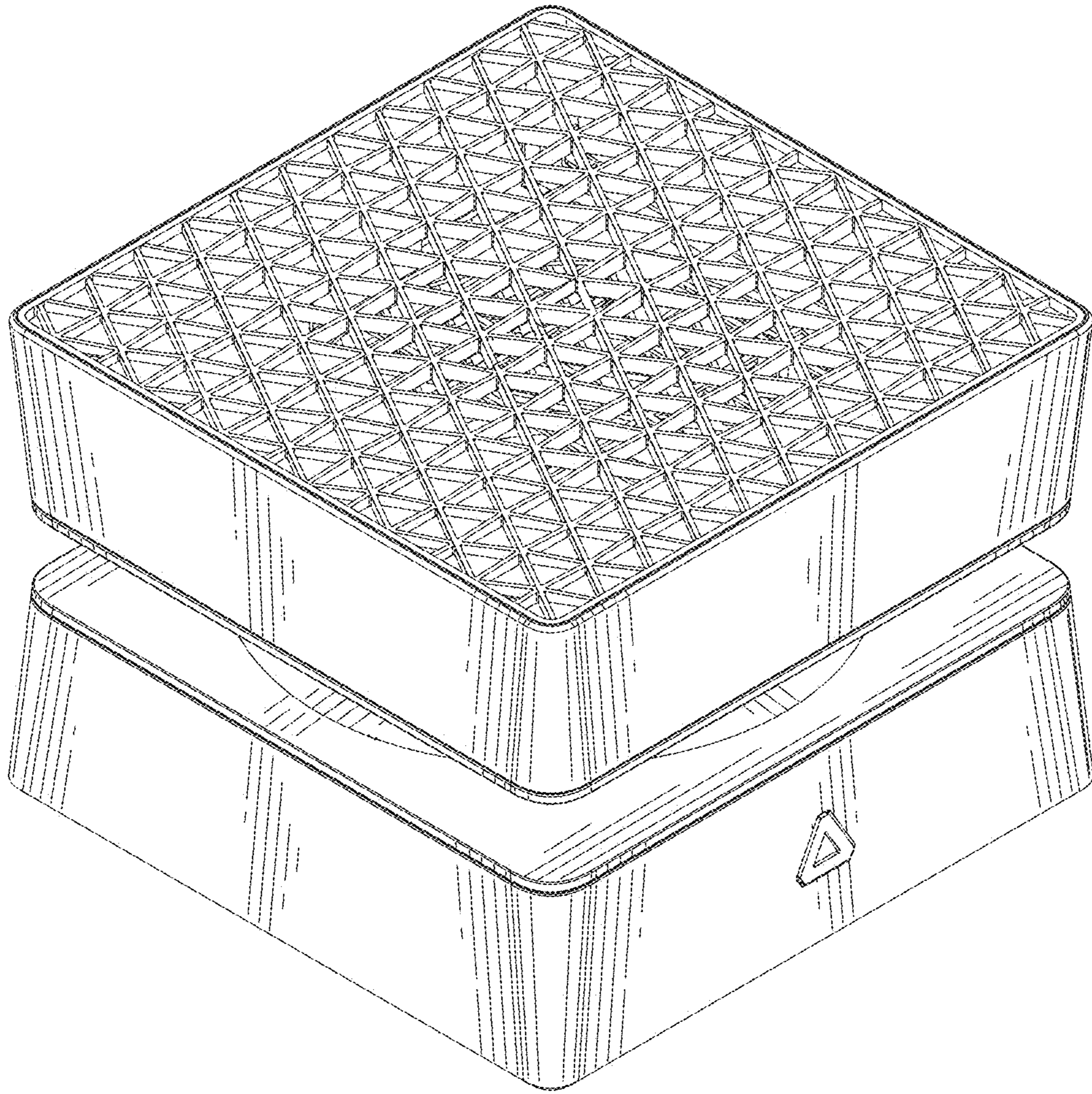


FIG. 1

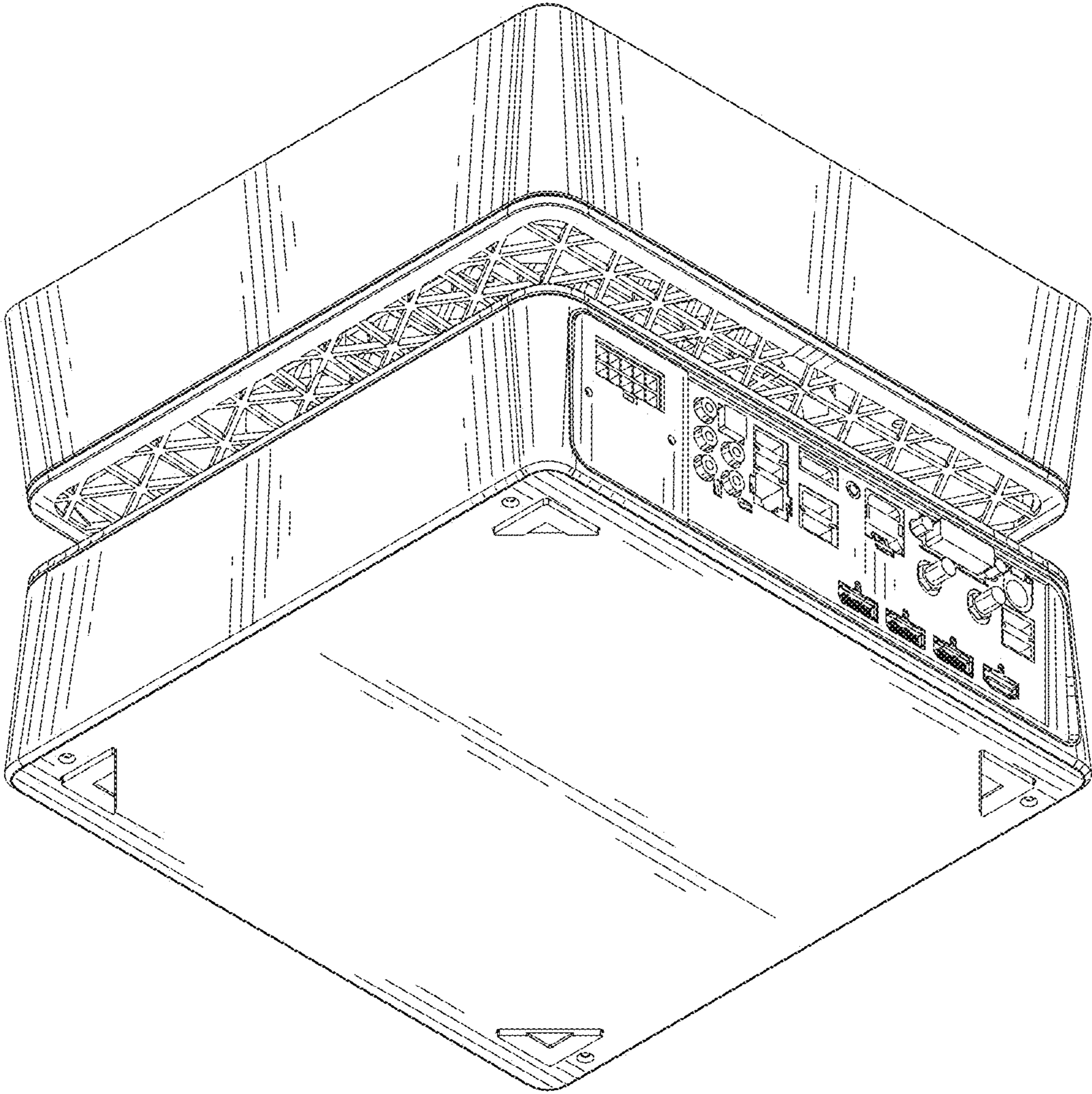


FIG. 2

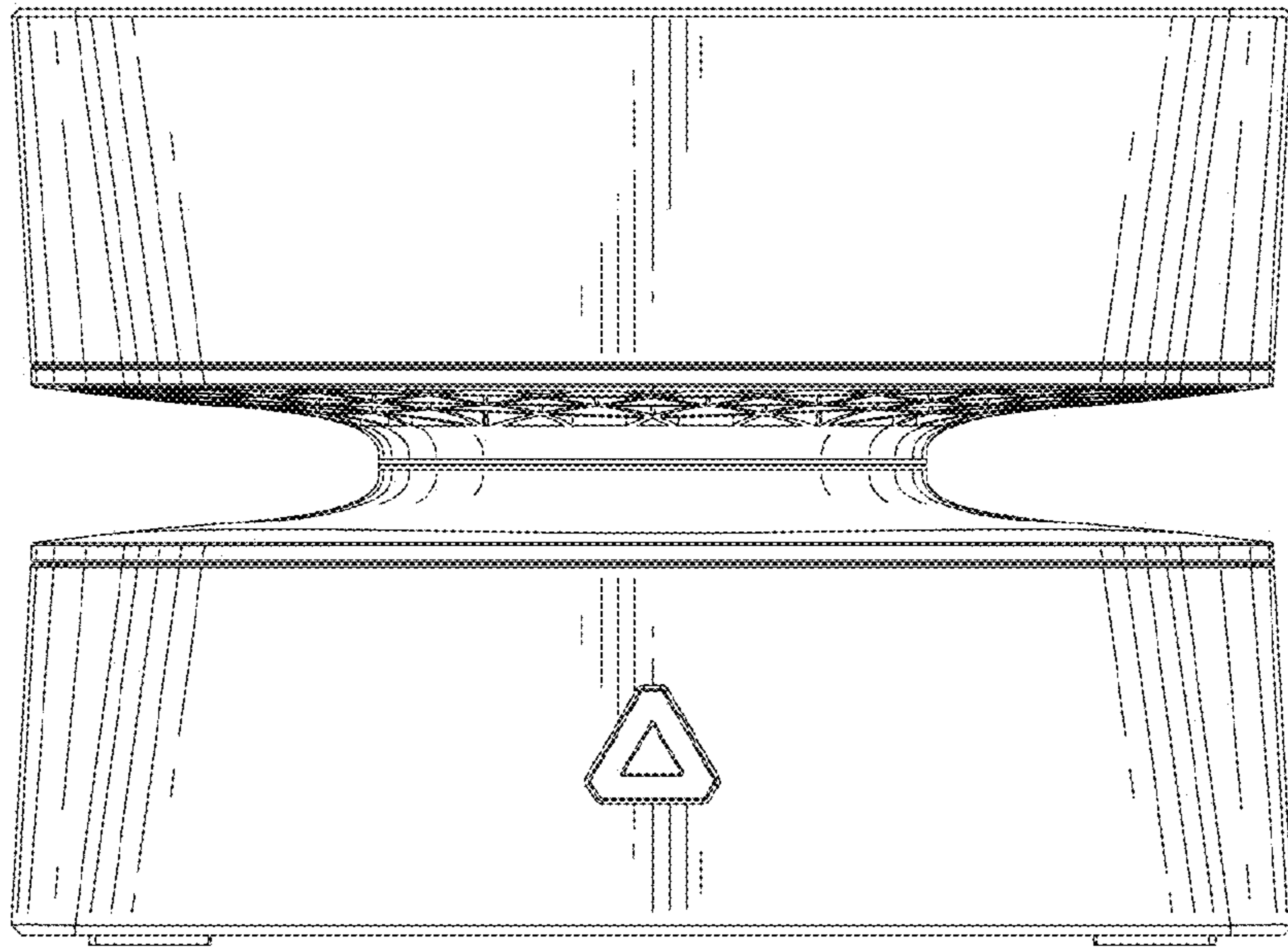


FIG. 3

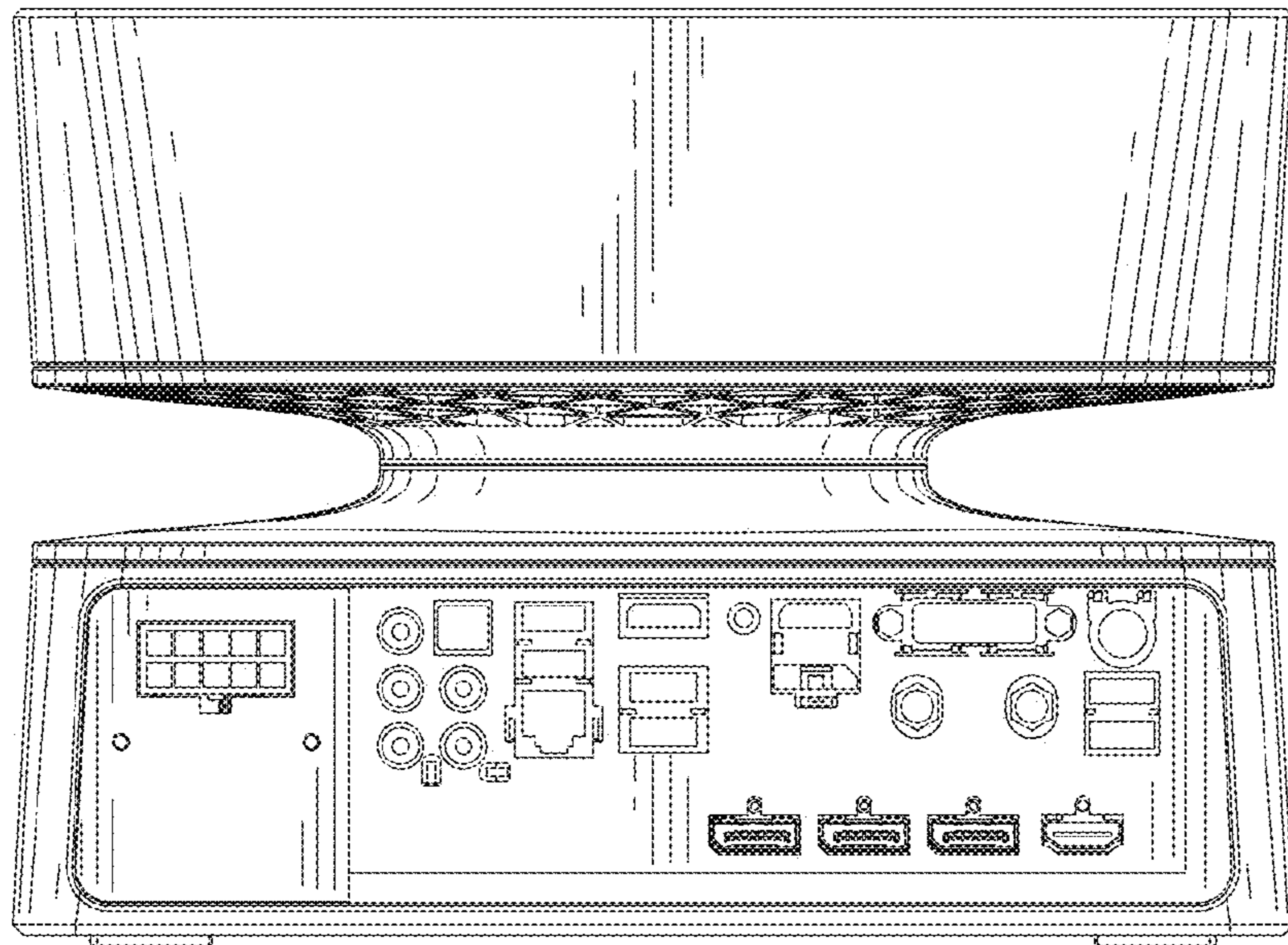


FIG. 4

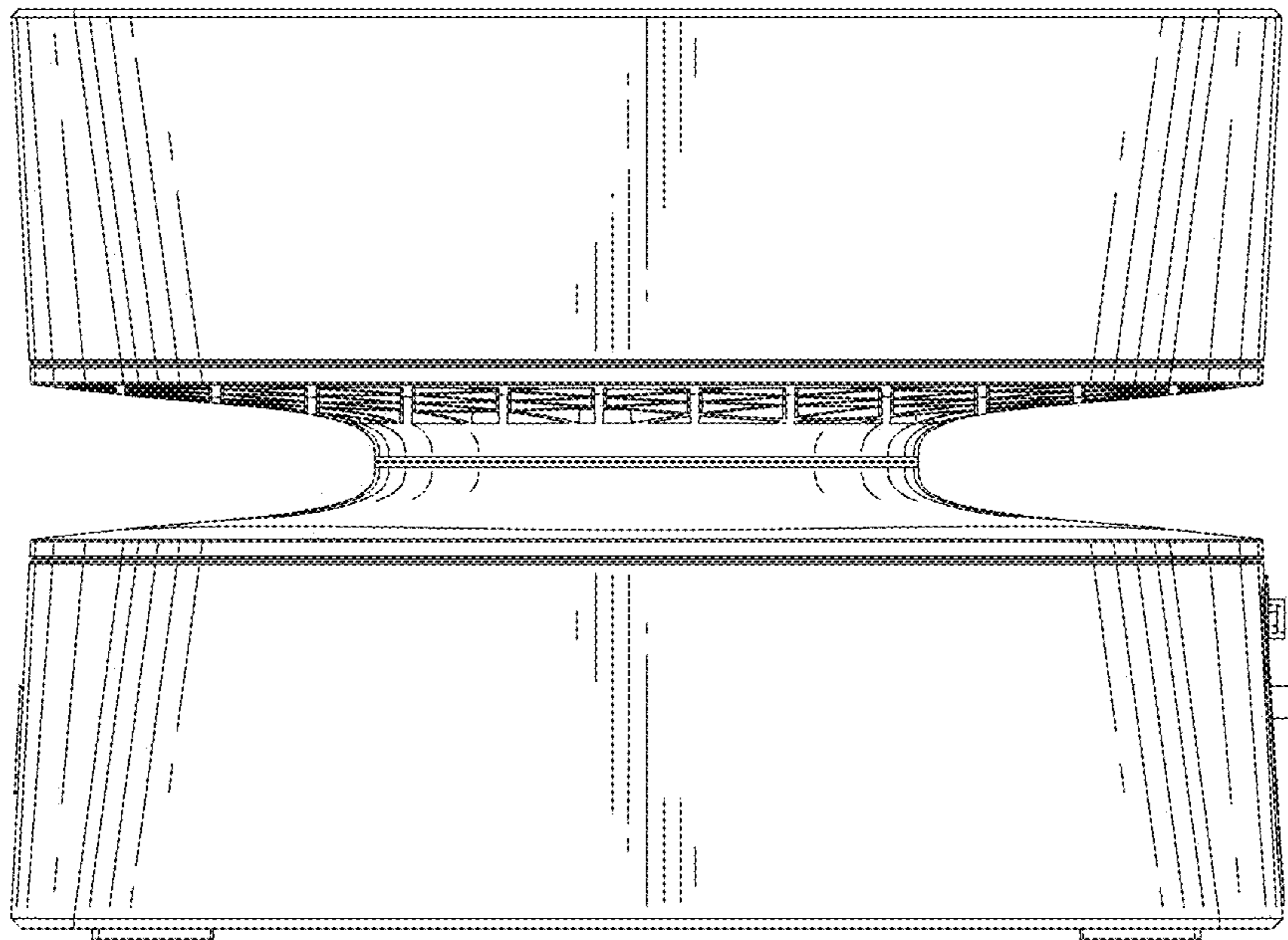


FIG. 5

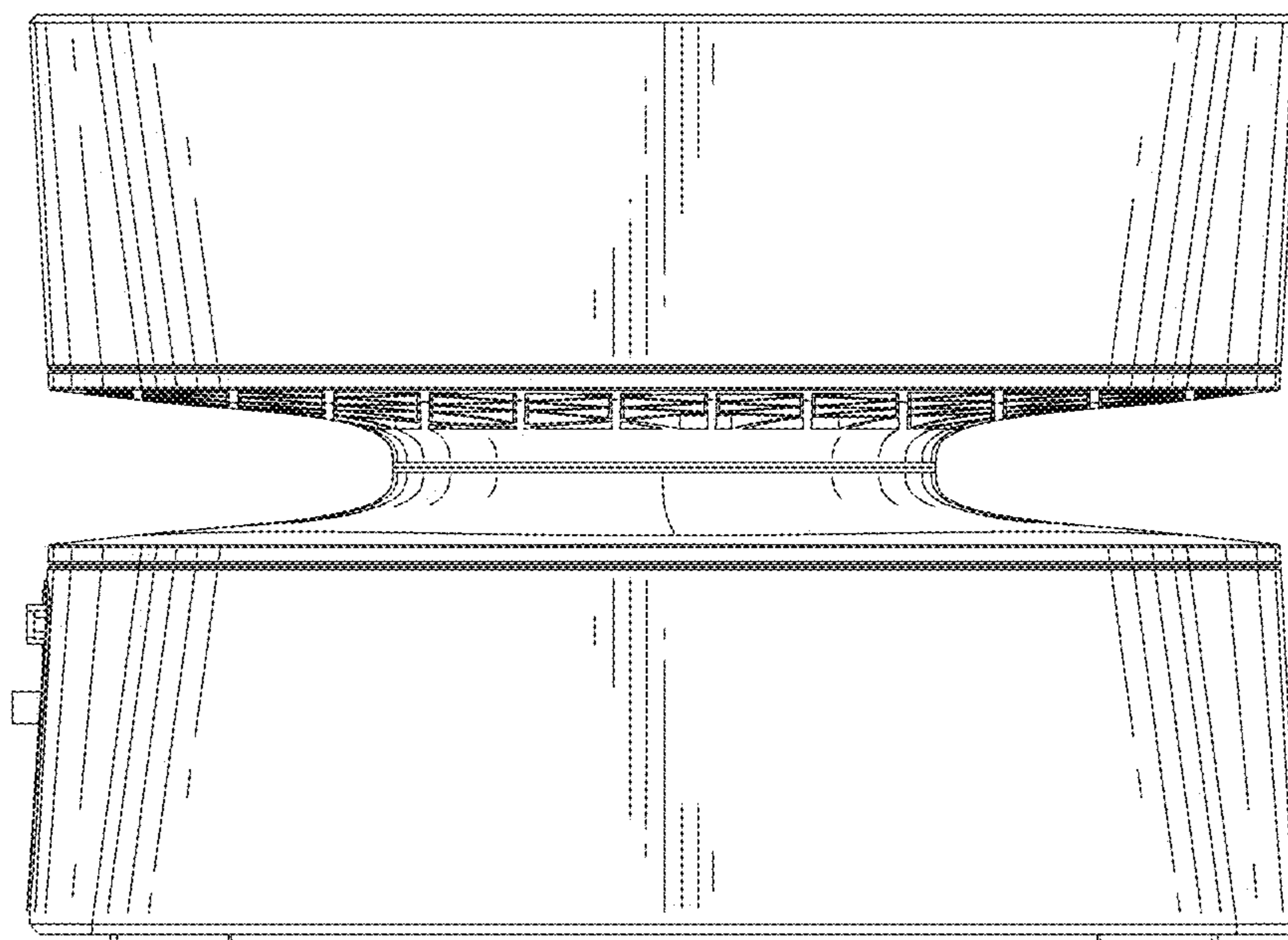


FIG. 6

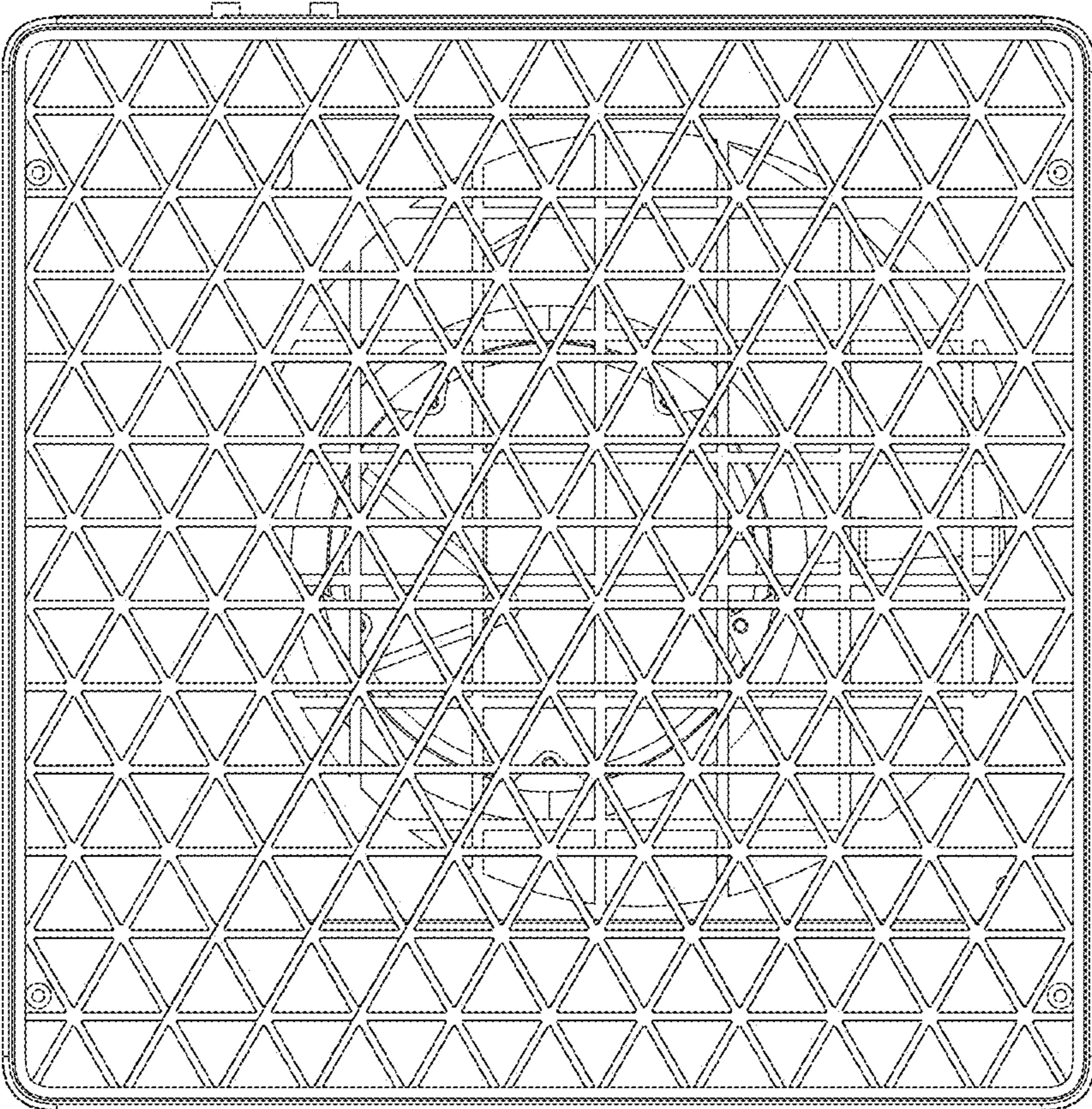


FIG. 7

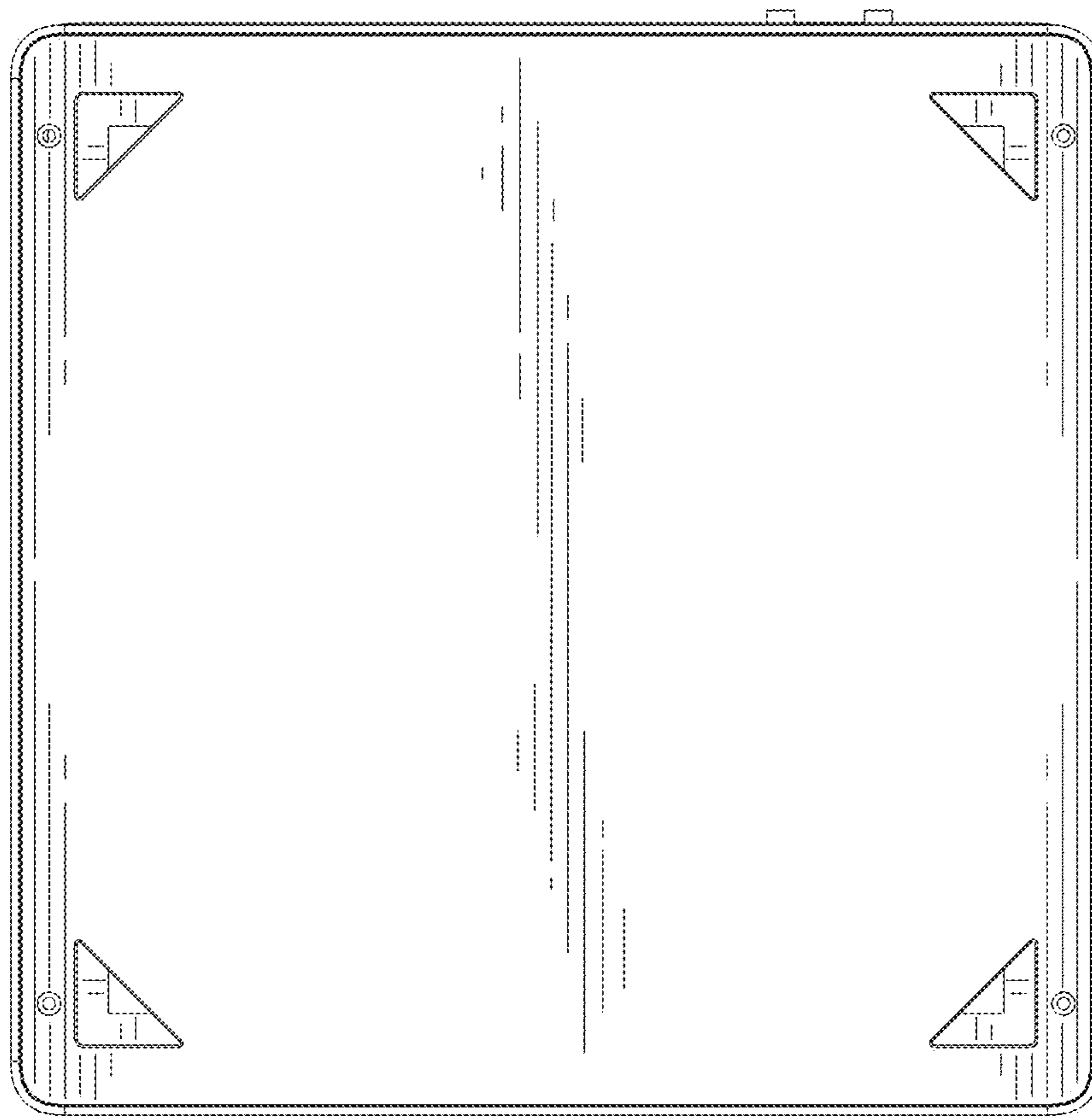


FIG. 8