



US00D895561S

(12) **United States Design Patent** (10) **Patent No.:** **US D895,561 S**
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(54) **PASSIVE MULTIPLEXER**
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(30) **Foreign Application Priority Data**

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(51) **LOC (12) Cl.** **13-03**
(52) **U.S. Cl.**
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(58) **Field of Classification Search**
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D14/479-480, 481-483, 140.1, 140.4,
D14/164, 193; D13/123, 133, 146-147,
D13/152, 154-155, 158, 164, 184, 199
CPC G02B 6/3897; G02B 6/3887; G02B
6/4256-4257; G02B 6/426; G02B 6/4262;
G02B 6/4441; G02B 6/4446; G02B
6/4447; G02B 6/445; G02B 6/4452-4453;
G02B 6/4472; G02B 6/4455; G02B
6/4453; G02B 7/00

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a passive multiplexer, as shown
and described.

DESCRIPTION

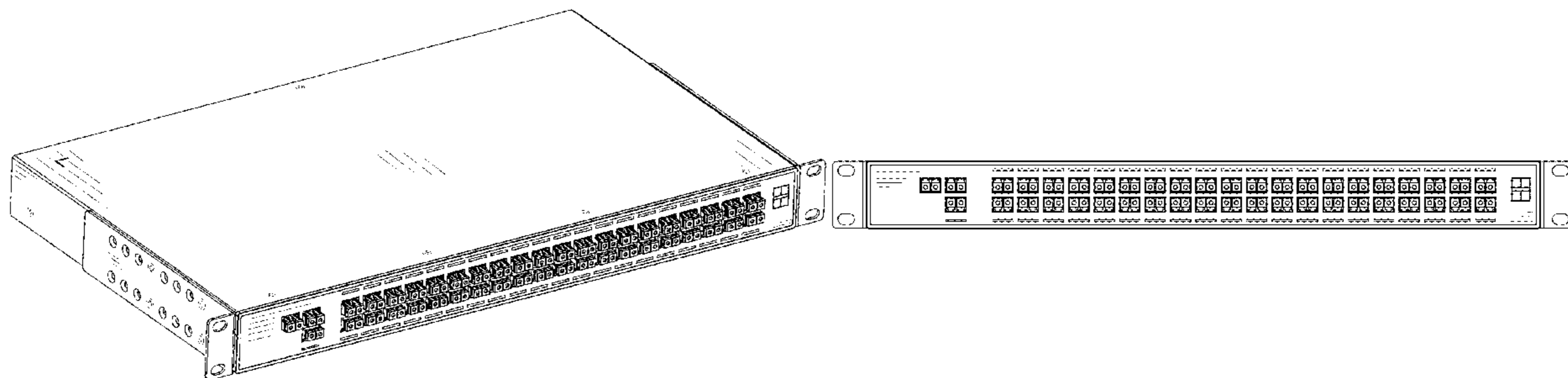
FIG. 1 is a perspective view of a passive multiplexer
showing my new design;
FIG. 2 is a front view thereof;
FIG. 3 is a back view thereof;
FIG. 4 is a left side view thereof;
FIG. 5 is a right side view thereof;
FIG. 6 is a top view thereof; and,
FIG. 7 is a bottom view thereof.
The broken lines shown in the drawings are for purposes of
illustrating portions of the passive multiplexer that form no
part of the claimed design.

1 Claim, 7 Drawing Sheets

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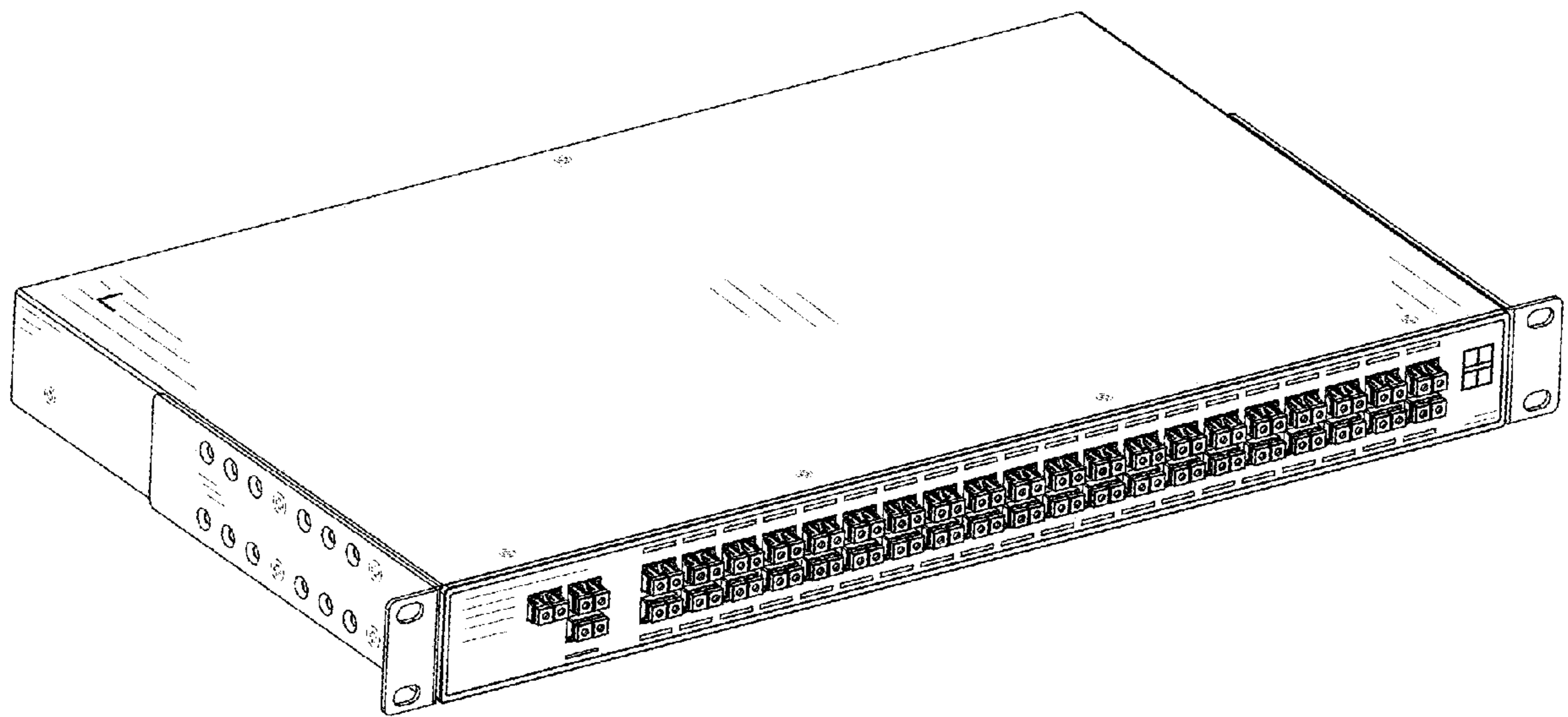


FIG. 1

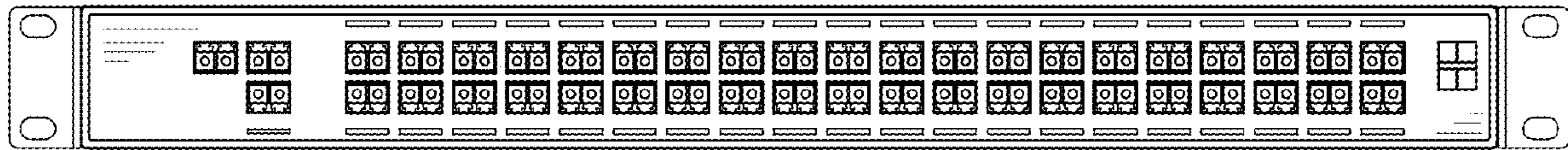


FIG.2

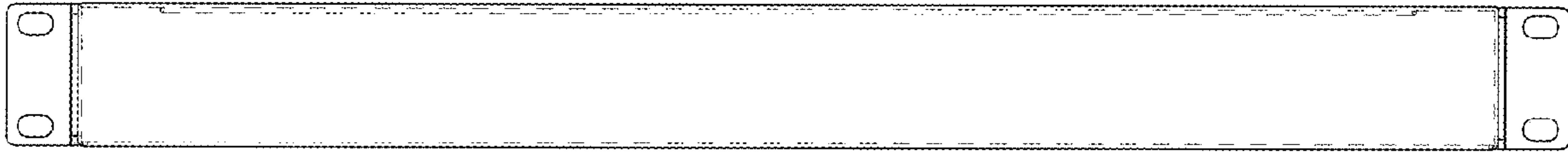


FIG.3

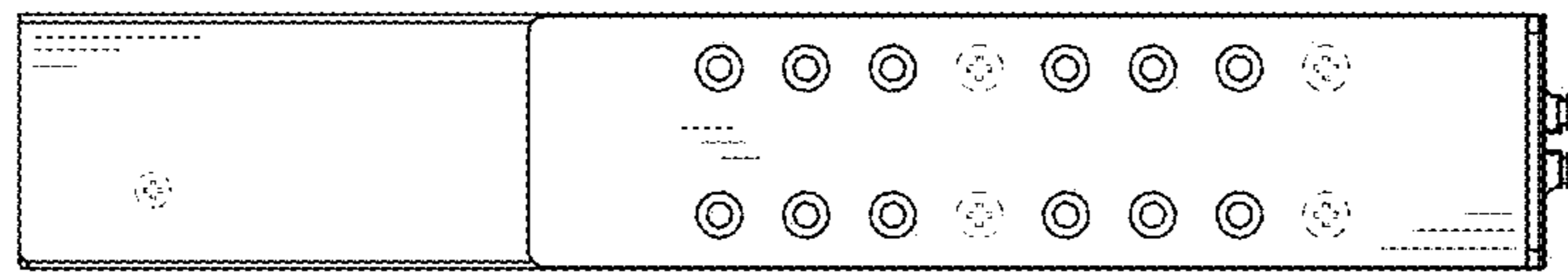


FIG.4

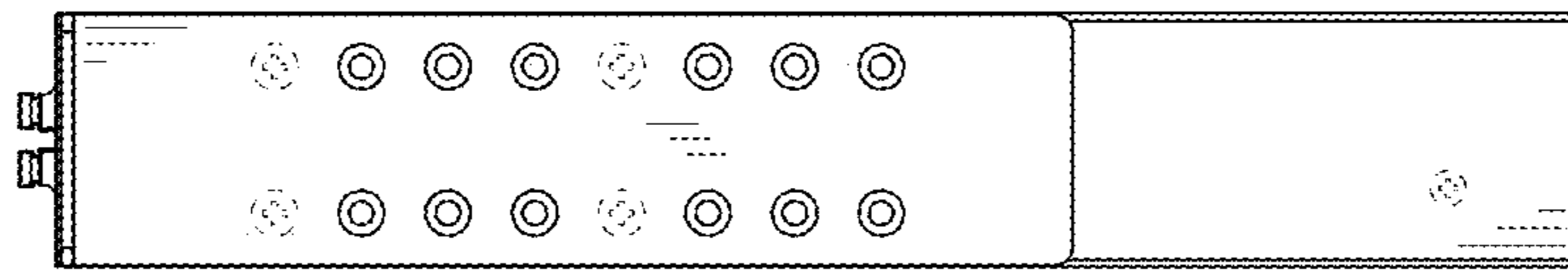


FIG.5

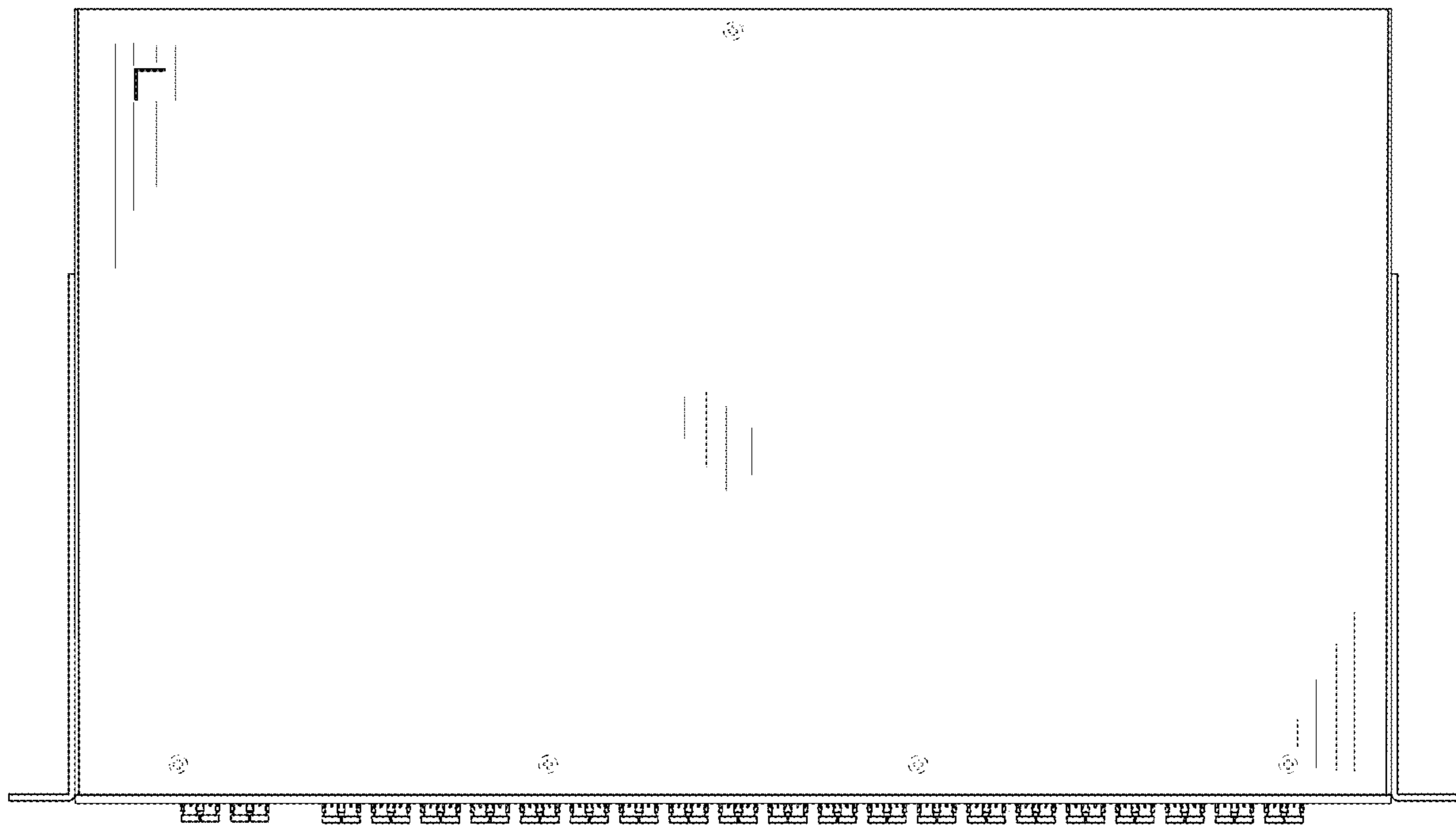


FIG.6



FIG. 7