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(12) **United States Design Patent** (10) **Patent No.:** **US D893,499 S**
Kirkpatrick (45) **Date of Patent:** **** Aug. 18, 2020**

(54) **COOLING STAND FOR AN ELECTRONIC DEVICE**
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(**) Term: **15 Years**

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(52) **U.S. Cl.**
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D12/400, 415-418, 420, 426.1, 114, 223;
D16/242, 245; D10/74; D3/218;
D13/107, 108; D6/553, 567, 672, 682,
D6/310, 362, 708; D19/84, 86, 90, 91;
D23/371
CPC B60R 11/00; B60R 11/02; B60R 11/0241;
B60R 11/0252; B60R 2011/0003; B60R
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A45F 2200/0525; H05K 5/0204; F16M
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See application file for complete search history.

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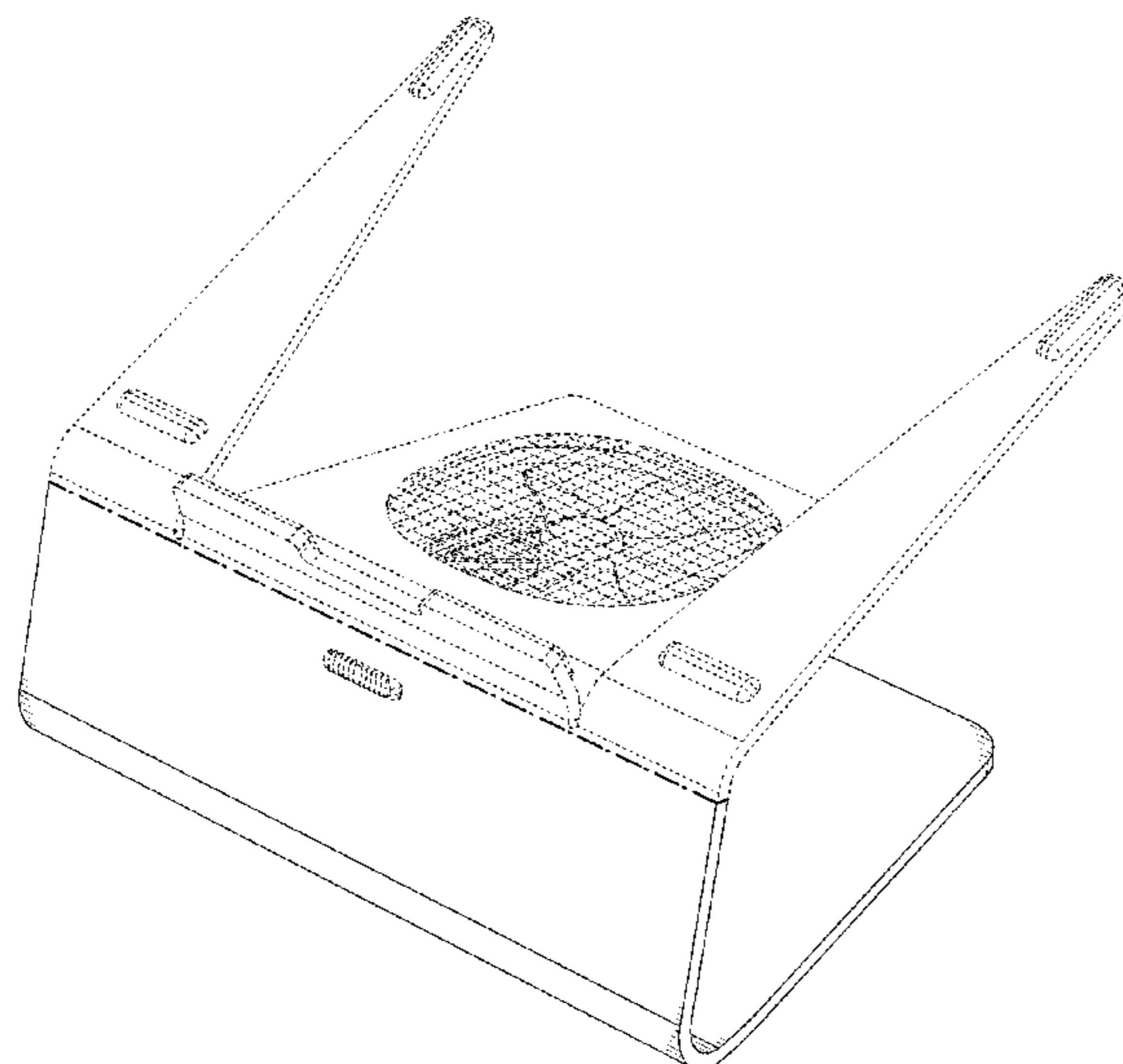
(57) **CLAIM**

The ornamental design for a cooling stand for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a front, side and top perspective view of the cooling stand for an electronic device;
FIG. 2 is a back, side and top perspective view thereof;
FIG. 3 is a front elevational view thereof;
FIG. 4 is a back elevational view thereof;
FIG. 5 is a top plan view thereof; and,
FIG. 6 is a left side elevational view thereof; the right side elevational view is a mirror image of the left side.
The broken lines shown in the drawings represent portions of the cooling stand for an electronic device that form no part of the claimed design or illustrate environment that forms no part of the claimed design.
The dash-dot-dash broken lines define the bounds of the claimed design and form no part thereof.

1 Claim, 6 Drawing Sheets



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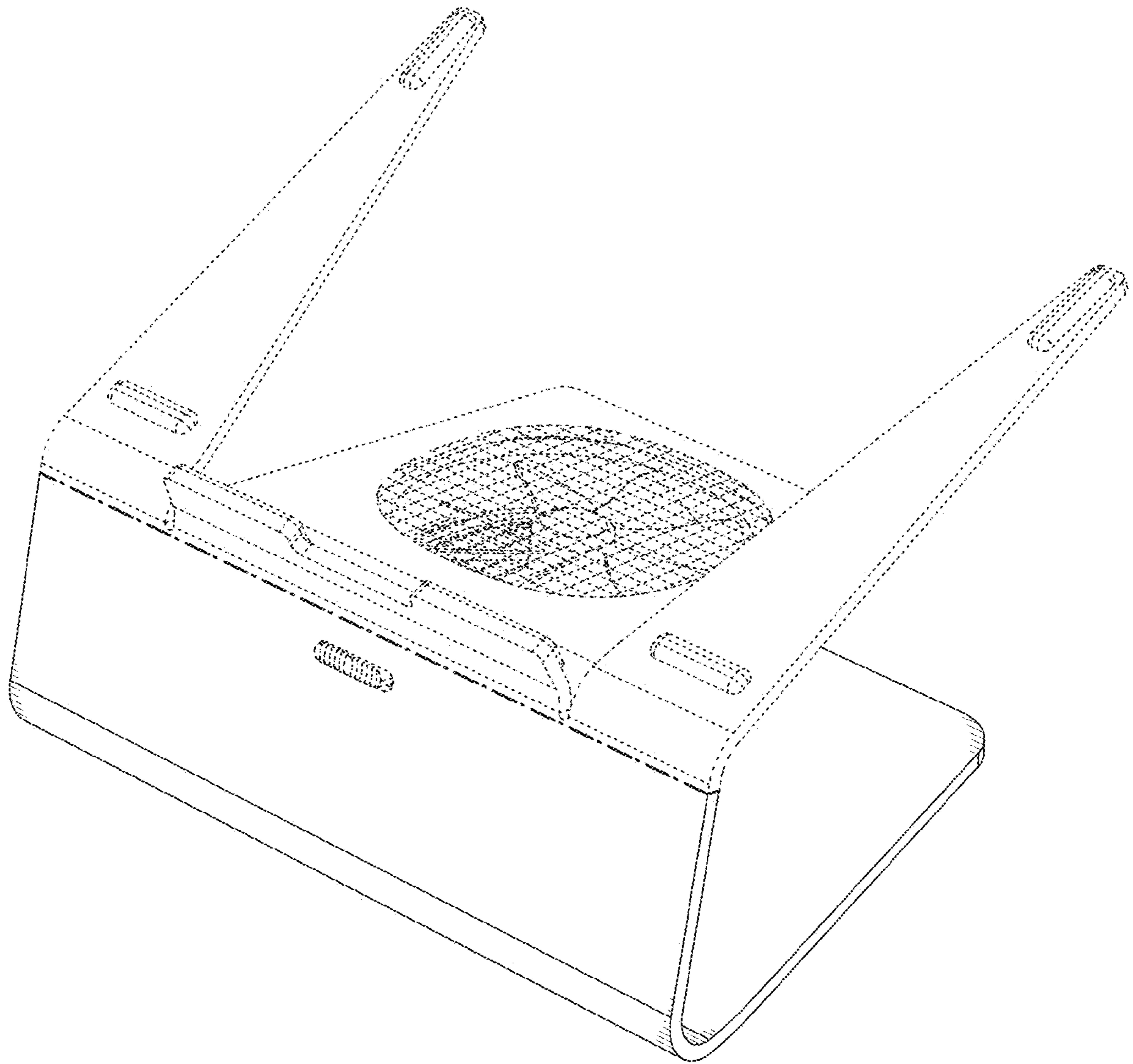


FIG. 1

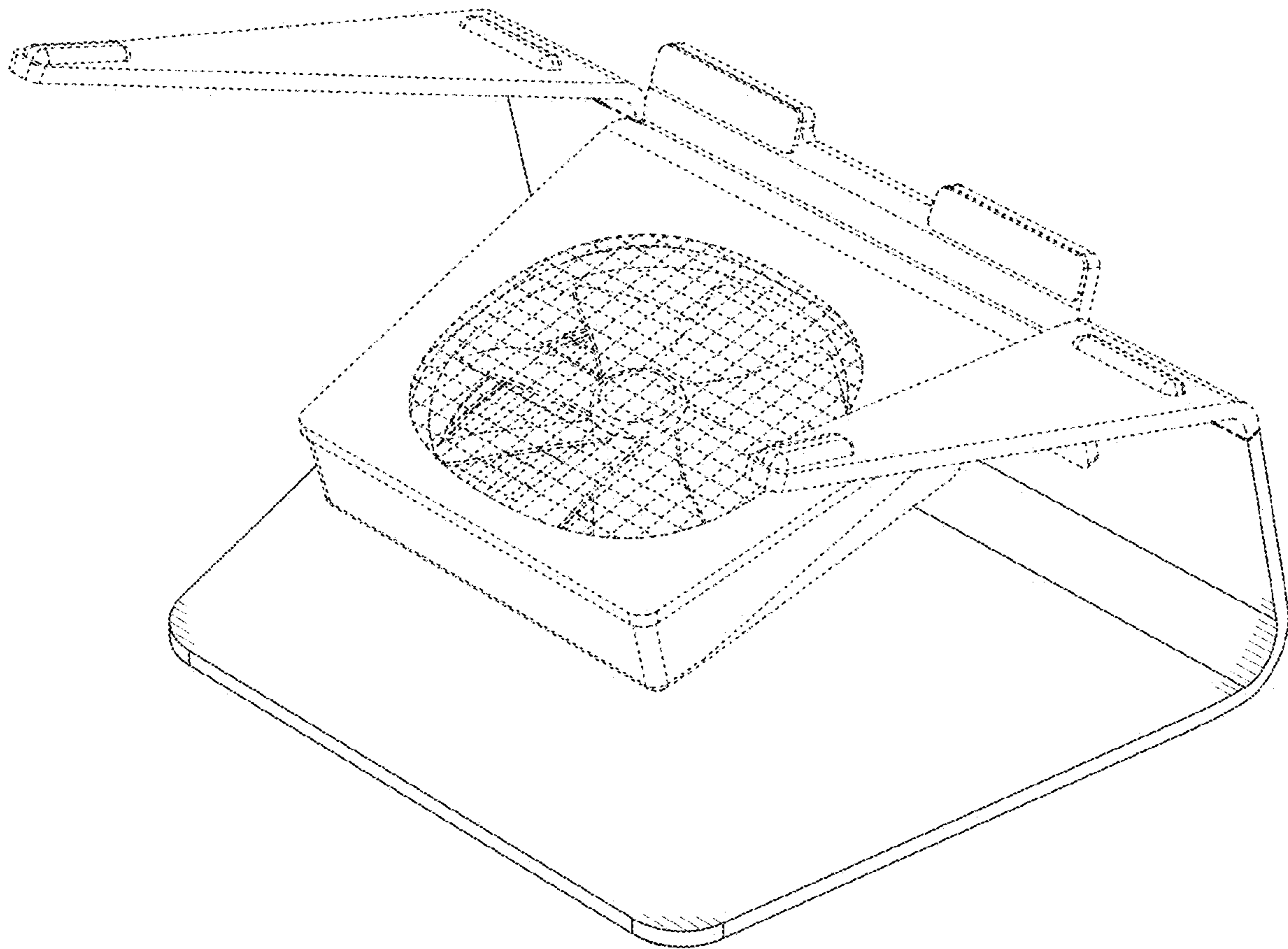


FIG. 2

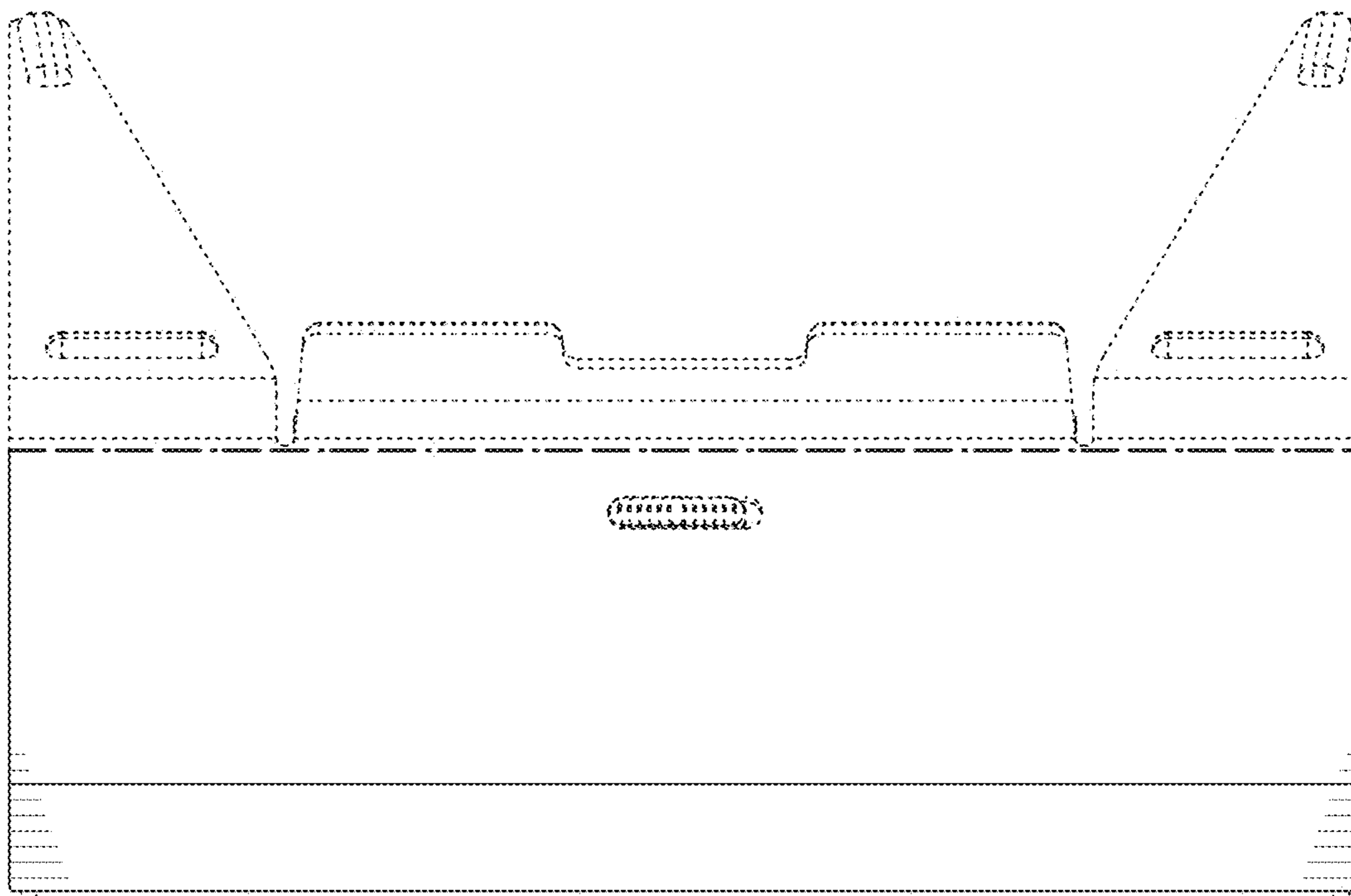


FIG. 3

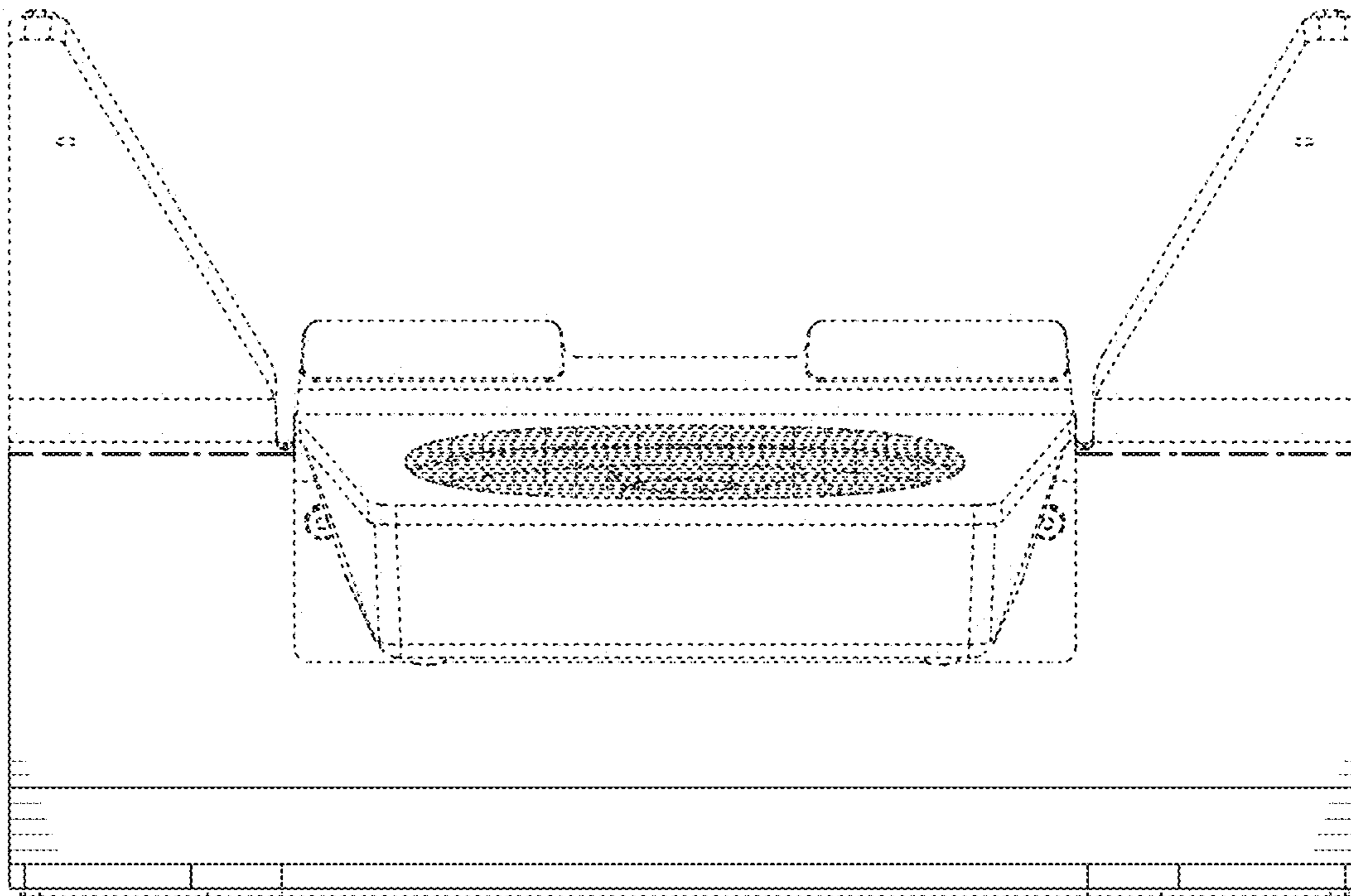


FIG. 4

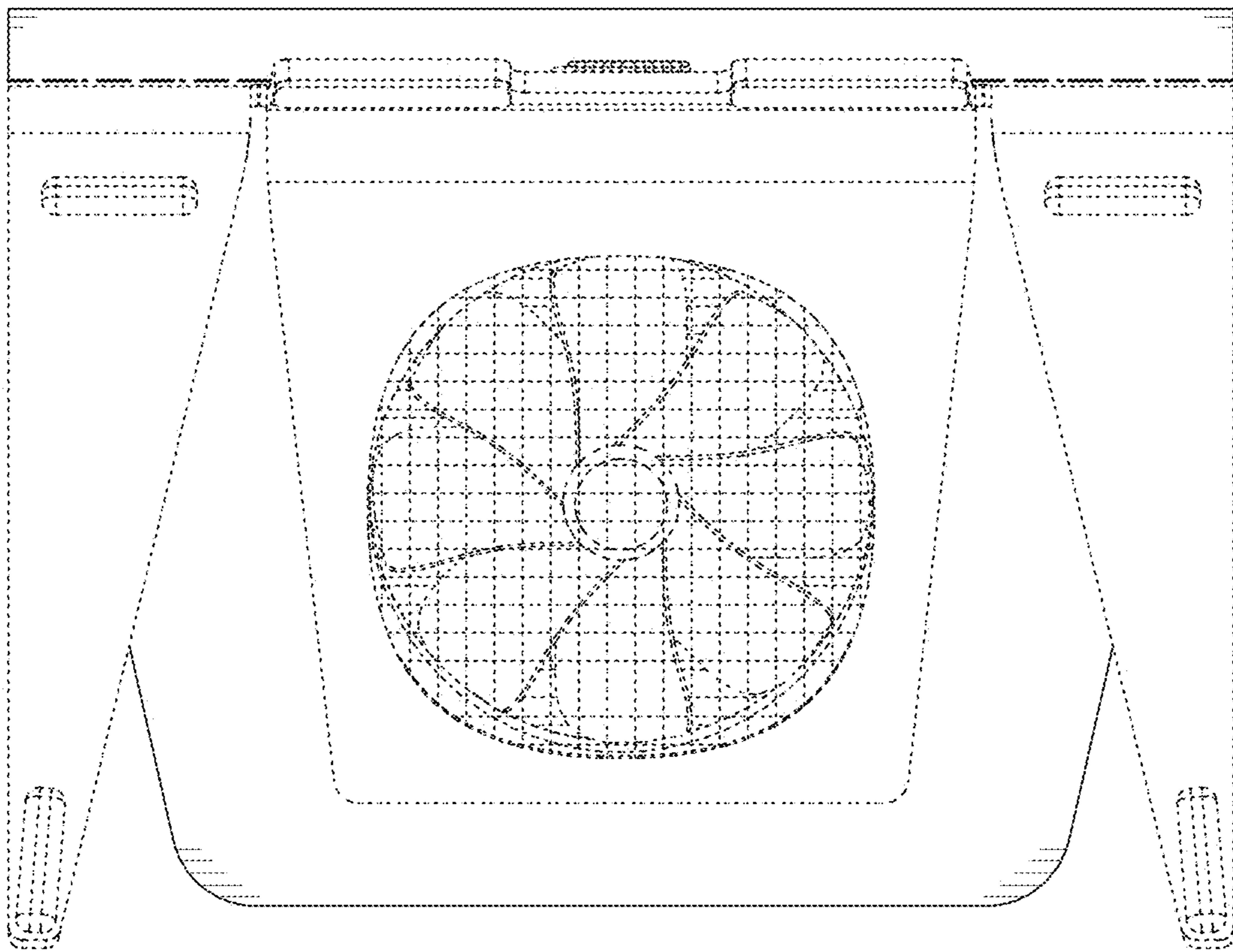


FIG. 5

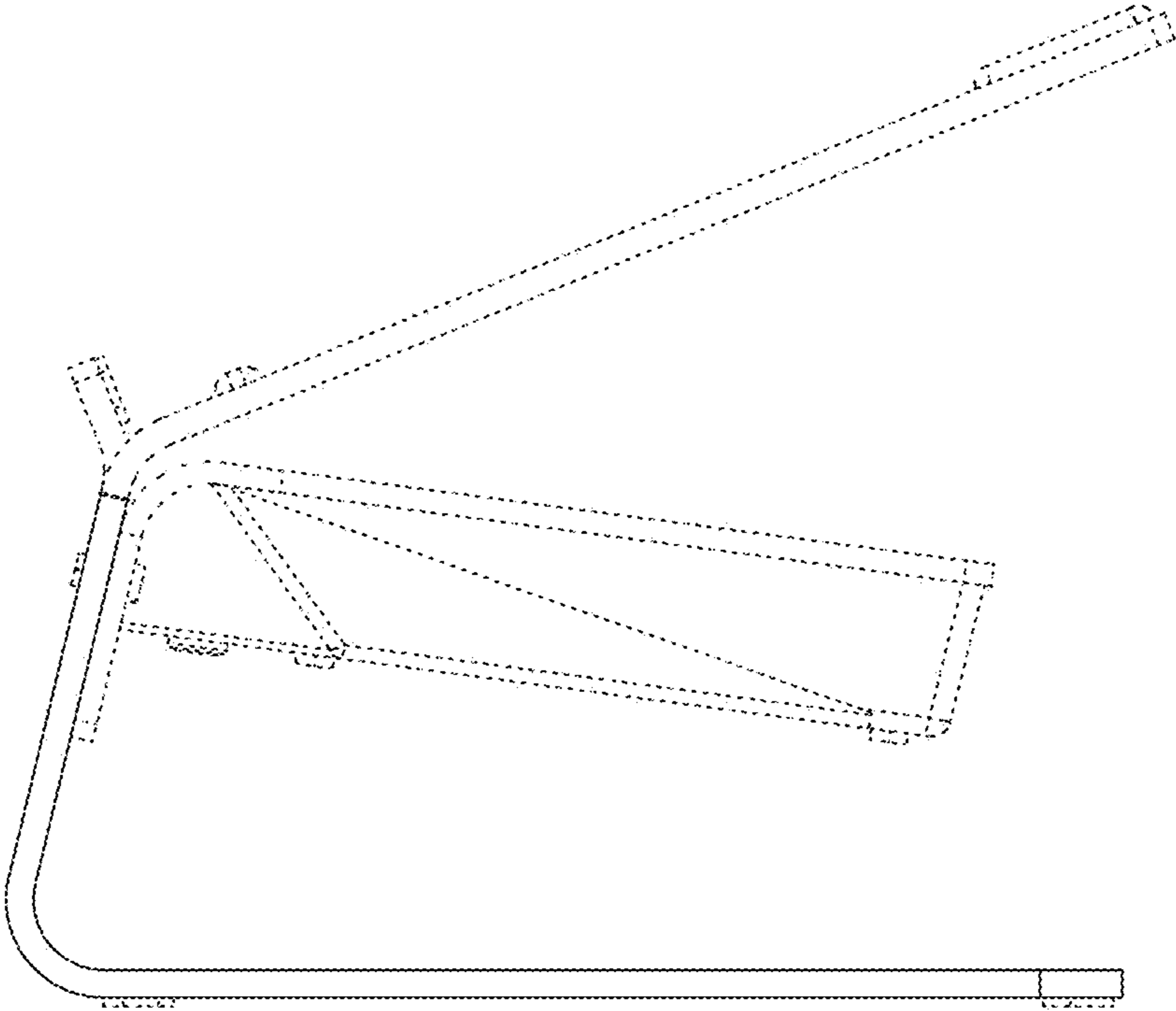


FIG. 6