



US00D893434S

(12) **United States Design Patent**
Ishida

(10) **Patent No.:** **US D893,434 S**

(45) **Date of Patent:** **** Aug. 18, 2020**

(54) **ELECTRIC CONNECTOR**

(71) Applicant: **SMK Corporation**, Tokyo (JP)

(72) Inventor: **Yoshiyasu Ishida**, Saitama (JP)

(73) Assignee: **SMK Corporation**, Tokyo (JP)

(**) Term: **15 Years**

(21) Appl. No.: **29/679,021**

(22) Filed: **Jan. 31, 2019**

(30) **Foreign Application Priority Data**

Aug. 24, 2018 (JP) 2018-018400

(51) **LOC (12) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/147**

(58) **Field of Classification Search**
USPC D13/133, 146, 154, 147, 184, 199
CPC H01R 9/09; H01R 9/091; H01R 9/098;
H01R 9/22; H01R 9/24; H01R 9/2408;
H01R 9/2691; H01R 9/28; H01R 12/716;
H01R 13/426
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D642,535 S *	8/2011	Sato	D13/147
D642,986 S *	8/2011	Nishimura	D13/147
D683,317 S *	5/2013	Nishimura	D13/147
D688,210 S *	8/2013	Mashiyama	D13/147
D692,833 S *	11/2013	Nishimura	D13/147
D695,691 S *	12/2013	Kobuchi	D13/147
D696,201 S *	12/2013	Takenaga	D13/147
D748,583 S *	2/2016	Kobuchi	D13/147
D759,600 S *	6/2016	Takemoto	D13/147
D774,463 S *	12/2016	Omodachi	D13/147
D774,464 S *	12/2016	Takenaga	D13/147
D793,342 S *	8/2017	Ashibu	D13/147
D811,344 S *	2/2018	Ozeki	D13/147
D816,043 S *	4/2018	Ashibu	D13/147

D824,339 S *	7/2018	Yamaguchi	D13/147
D826,166 S *	8/2018	Yayoshi	D13/147
D866,477 S *	11/2019	Takenaga	D13/147

(Continued)

FOREIGN PATENT DOCUMENTS

CN 305306888 * 8/1919

OTHER PUBLICATIONS

Multi-RF Board-to-Board Connector, date N/A, [online], [site visited Jun. 11, 2020], Available from Internet URL: <https://www.hirose.com/product/series/BM46?lang=en> (Year: n/a).*

Primary Examiner — Karen E Kearney

Assistant Examiner — Michael Chong

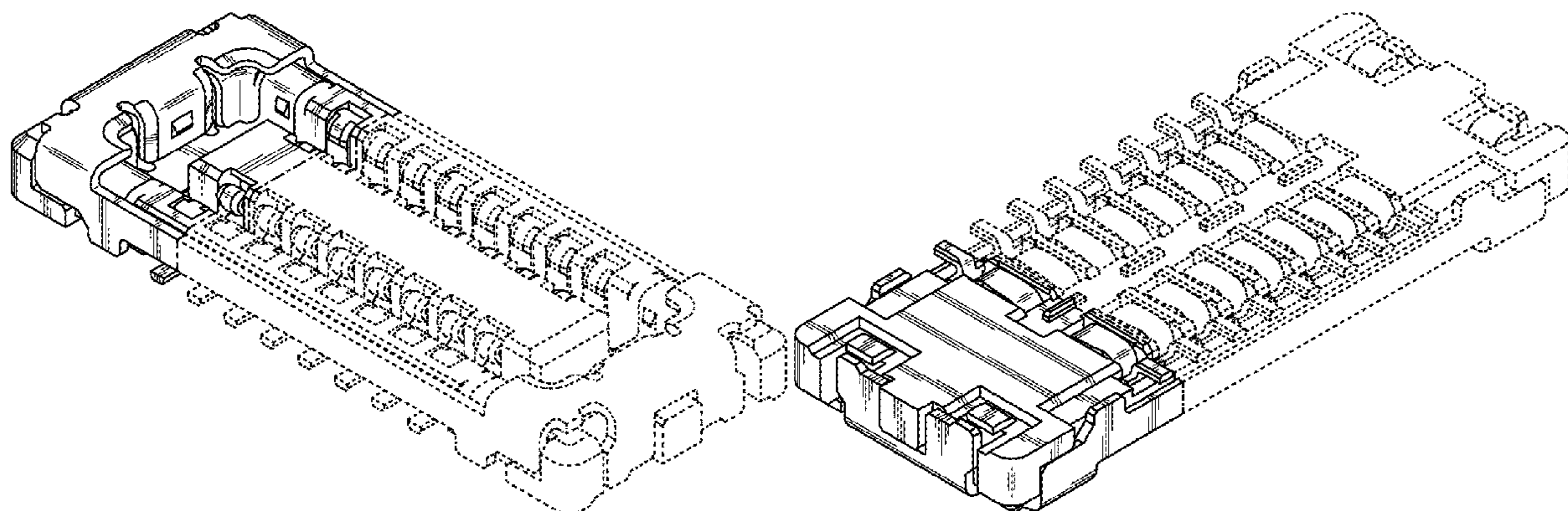
(57) **CLAIM**

The ornamental design for an electric connector, as shown and described.

DESCRIPTION

FIG. 1 is a front elevation view of an electric connector, showing our new design;
FIG. 2 is a top plan view thereof, that is symmetric with a bottom plan view;
FIG. 3 is a right side view thereof;
FIG. 4 is a left side view thereof;
FIG. 5 is a rear elevation view thereof;
FIG. 6 is a 1st perspective view thereof; and,
FIG. 7 is a 2nd perspective view thereof.
The dash-dash broken line portions of the figure drawings are included to show portions of the article that from no part of the claimed design.
The dash-dot broken line portions of the figure drawings are included to show boundaries that from no part of the claimed design.
The shadings in the figures show contour and not surface ornamentation.

1 Claim, 7 Drawing Sheets



(56)

References Cited

U.S. PATENT DOCUMENTS

D877,704 S * 3/2020 Takenaga D13/147
D885,346 S * 5/2020 Ishida D13/147
D885,347 S * 5/2020 Ishida D13/147

* cited by examiner

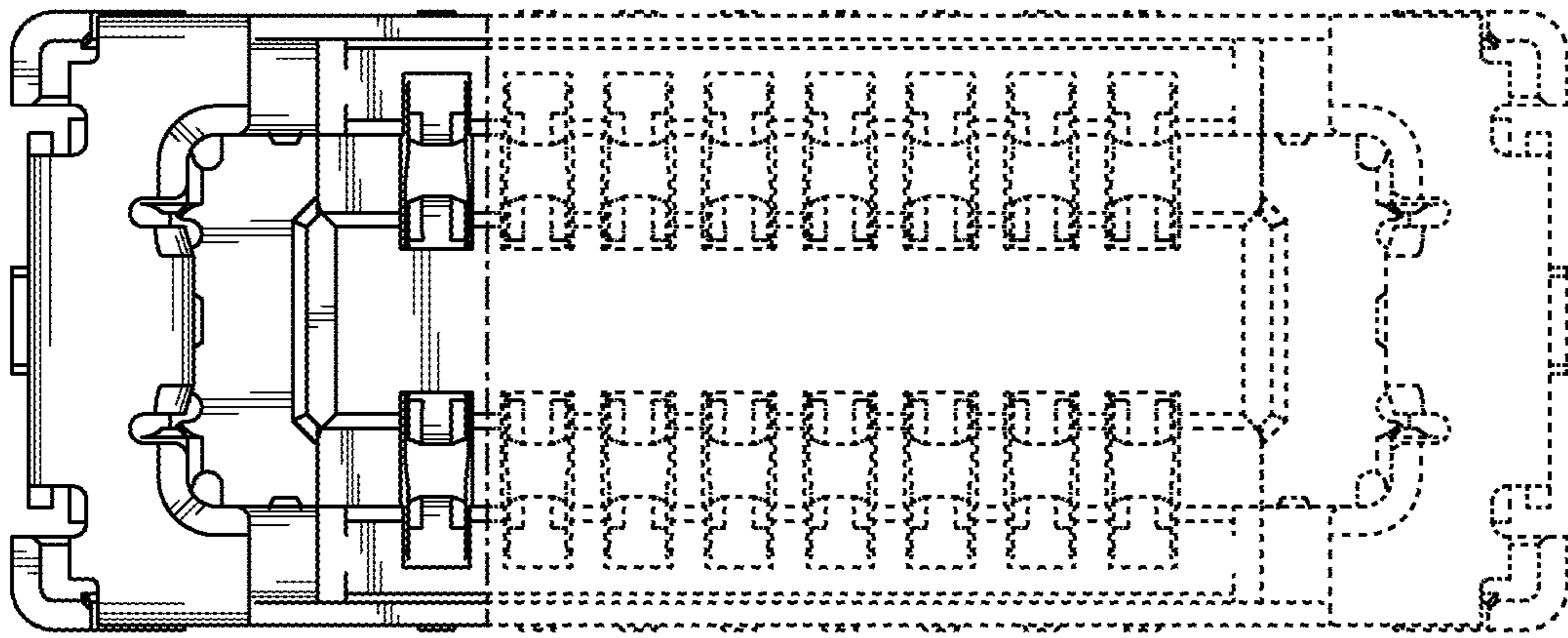


Fig. 1

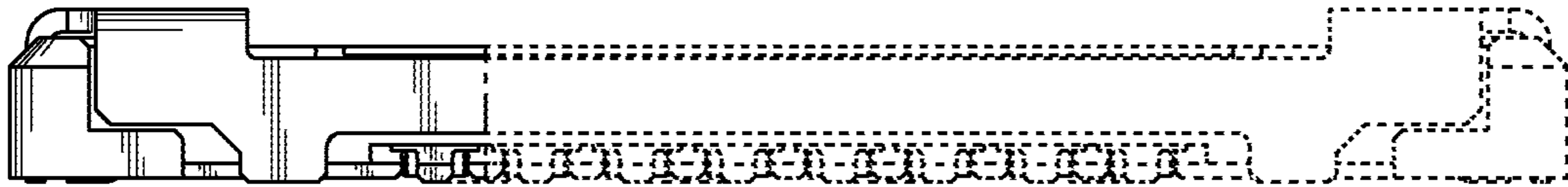


Fig. 2

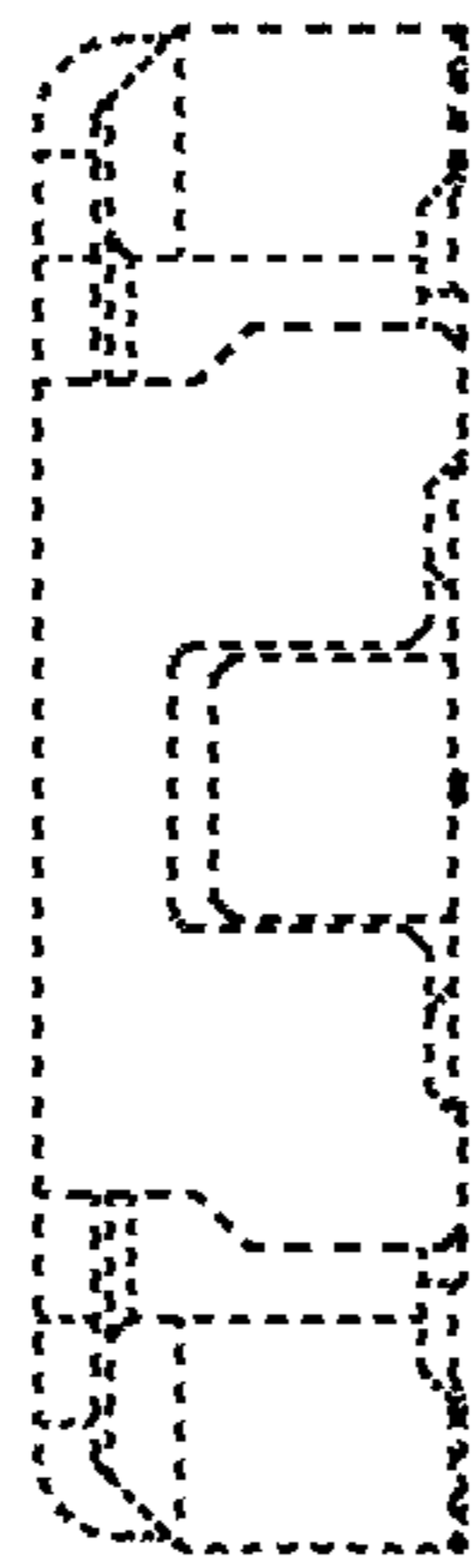


Fig. 3

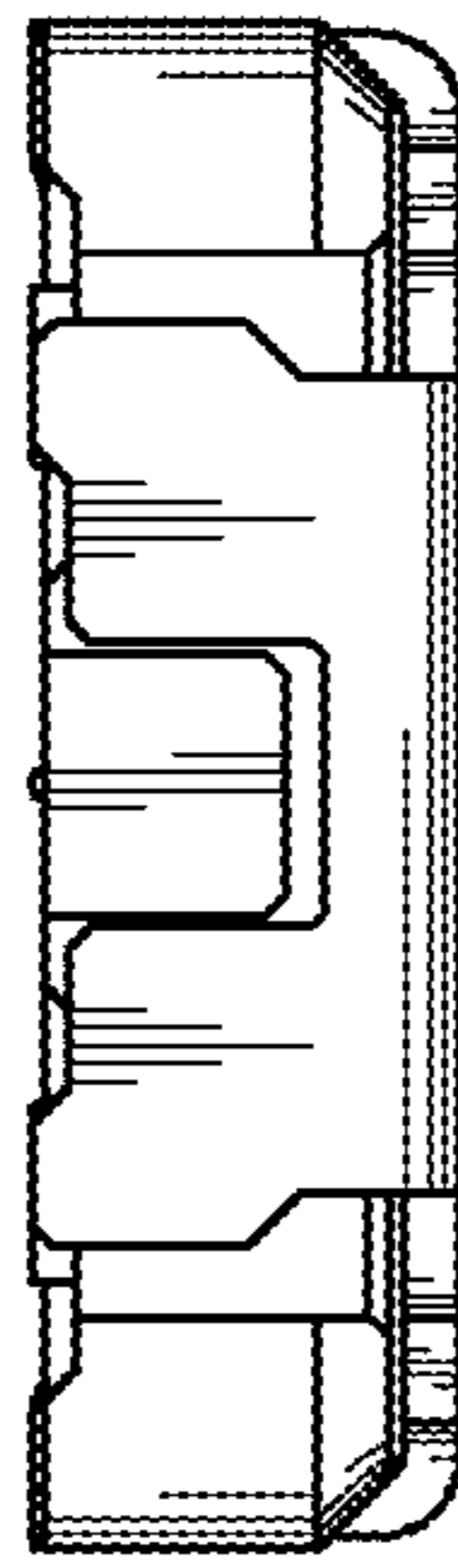


Fig. 4

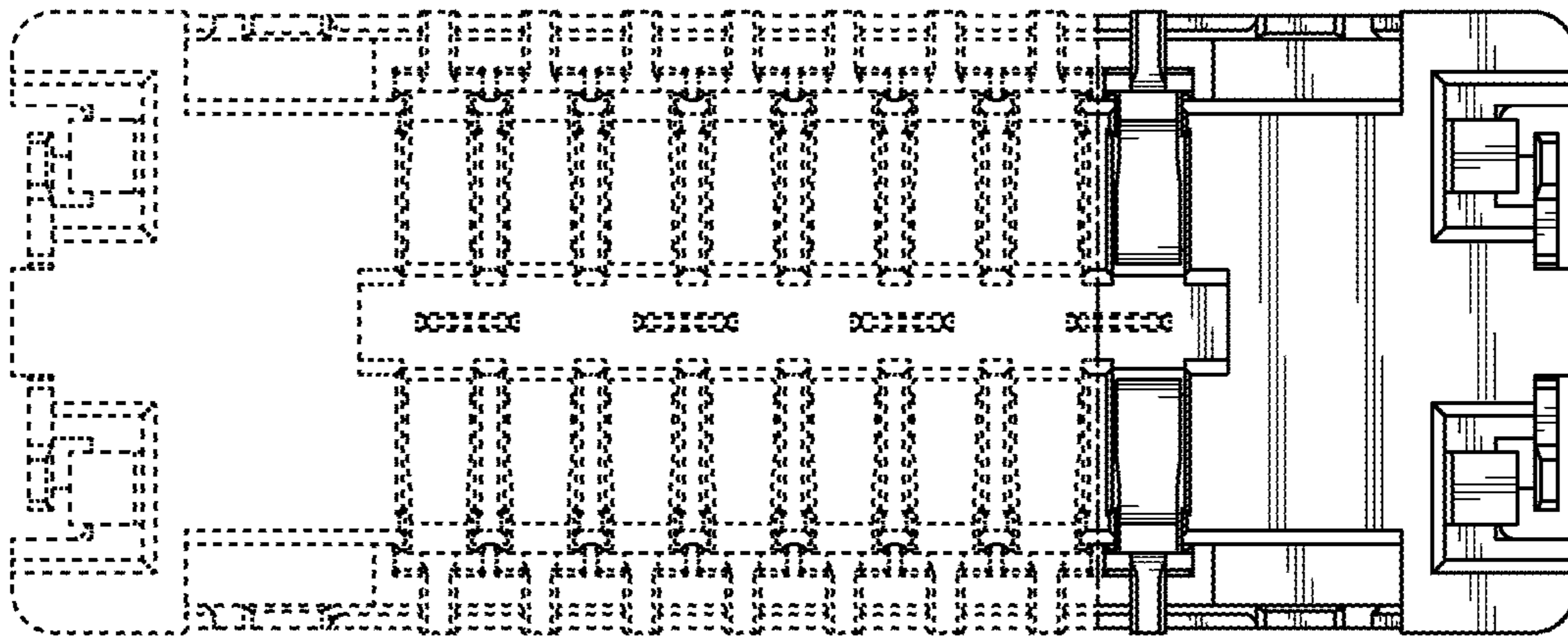


Fig. 5

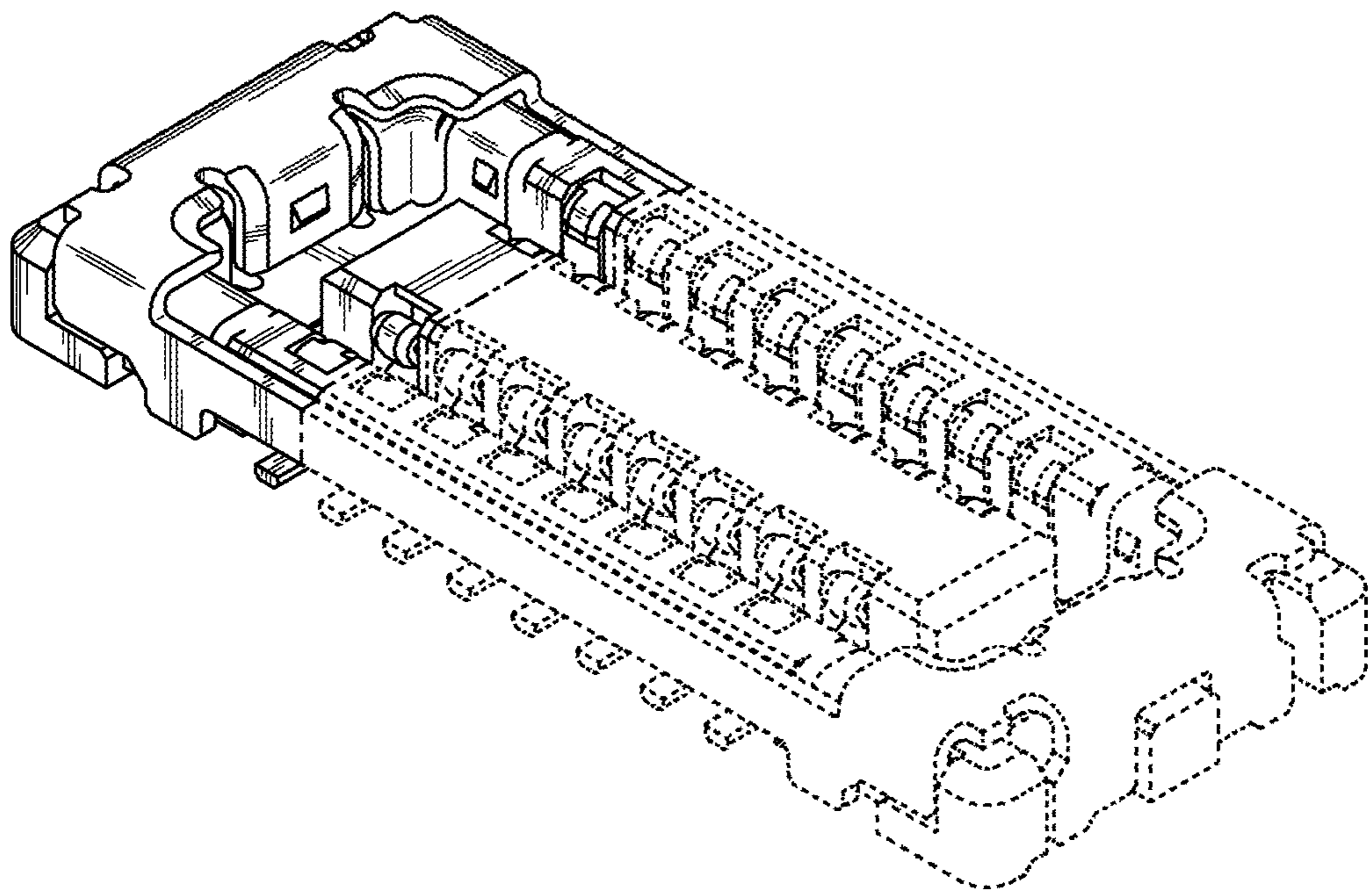


Fig. 6

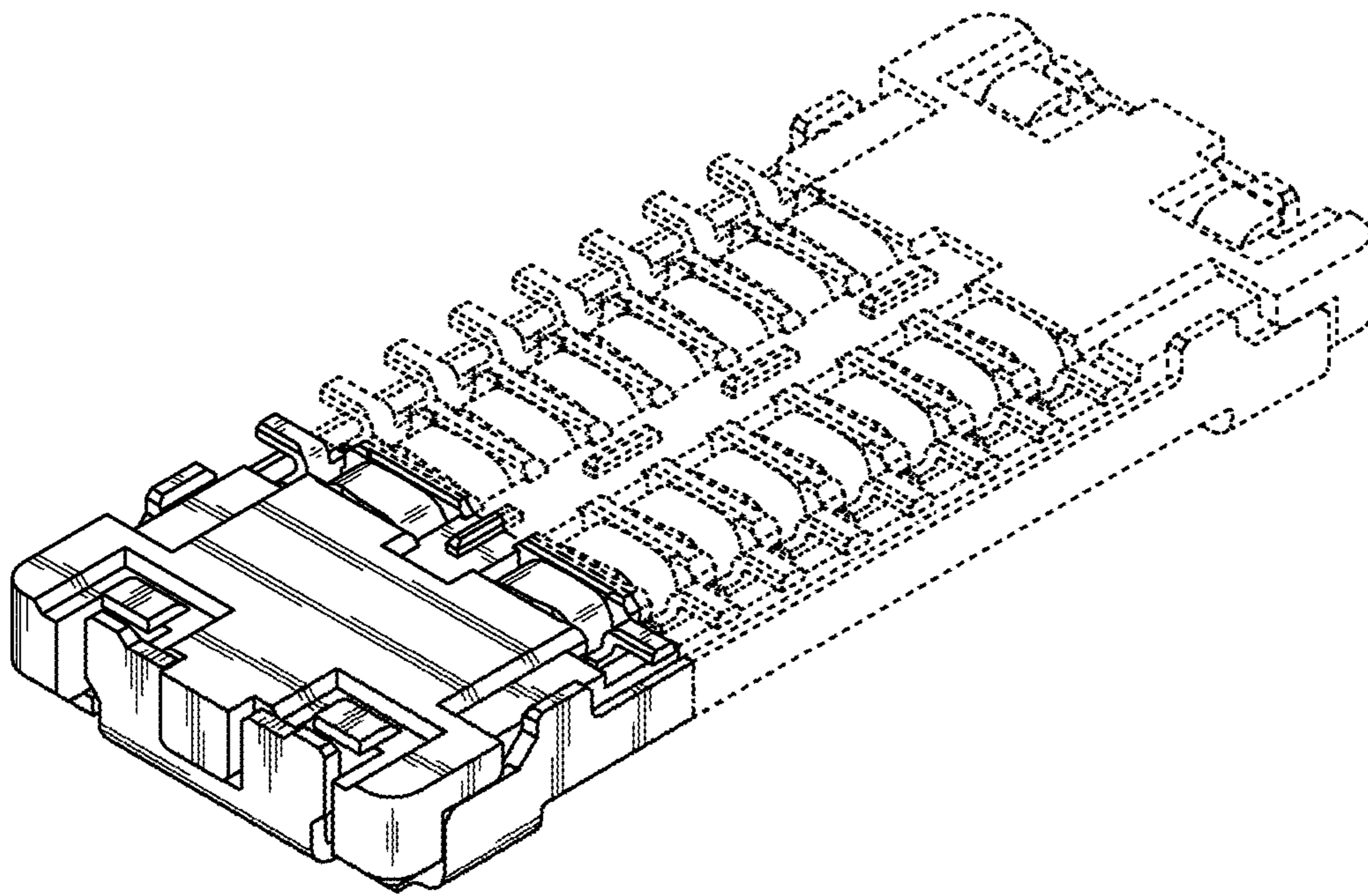


Fig. 7