

US00D890769S

(12) **United States Design Patent** (10) **Patent No.:** **US D890,769 S**  
**Bolotin et al.** (45) **Date of Patent:** **\*\* Jul. 21, 2020**

(54) **ELECTRONIC DEVICE**  
(71) Applicant: **ClevX, LLC**, Kirkland, WA (US)  
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**Robert Hubler**, San Jose, CA (US)  
(73) Assignee: **ClevX, LLC**, Kirkland, WA (US)

D624,553 S 9/2010 Mamone  
D624,920 S 10/2010 Sheppard et al.  
D642,585 S \* 8/2011 Lan ..... D14/480.5  
D670,297 S \* 11/2012 Huang ..... D14/480.5  
D670,712 S \* 11/2012 Bolotin ..... D14/480.5  
D671,123 S 11/2012 Ni et al.  
D711,389 S \* 8/2014 Sun ..... D14/480.5  
D711,891 S 8/2014 Emami et al.

(Continued)

(\*\*) Term: **15 Years**

**OTHER PUBLICATIONS**

(21) Appl. No.: **29/717,024**

U.S. Appl. No. 29/648,875, filed May 24, 2018, Electronic Device,  
now U.S. Pat. No. D. 873,274

(22) Filed: **Dec. 13, 2019**

(Continued)

**Related U.S. Application Data**

(62) Division of application No. 29/648,875, filed on May  
24, 2018, now Pat. No. Des. 873,274.

*Primary Examiner* — Angela J Lee

(51) **LOC (12) Cl.** ..... **14-03**

(74) *Attorney, Agent, or Firm* — Schwegman Lundberg &  
Woessner, P.A.

(52) **U.S. Cl.**

(57) **CLAIM**

USPC ..... **D14/480.5**

The ornamental design for an electronic device, as shown  
and described.

(58) **Field of Classification Search**

USPC ..... D14/480.1–480.7, 435.1, 411, 433, 436,  
D14/484.1, 137, 155, 203.1–203.8, 240,  
D14/242; D13/133, 154

**DESCRIPTION**

CPC ..... H05K 5/0278; G06K 19/07732  
See application file for complete search history.

FIG. 1 is a front view of an electronic device (e.g., a secure  
electronic data storage device or other electronic storage  
device) with a covering sleeve, showing our new design.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

D520,996 S 5/2006 Deng  
D524,817 S 7/2006 Conley et al.  
D542,787 S \* 5/2007 Siu ..... D14/480.5  
D542,788 S \* 5/2007 Gutmann ..... D14/480.5  
D556,752 S 12/2007 Otsuka et al.  
D579,934 S 11/2008 Okamoto et al.  
D583,816 S 12/2008 Guccione et al.  
D587,719 S 3/2009 Tate et al.  
D607,886 S 1/2010 Bolotin et al.  
D610,134 S 2/2010 Goetzen  
D610,156 S \* 2/2010 Mudrick ..... D14/480.5

FIG. 2 is a rear view thereof.

FIG. 3 is a left view thereof.

FIG. 4 is a right view thereof.

FIG. 5 is a top view thereof.

FIG. 6 is a bottom view thereof.

FIG. 7 is a top-front-right perspective view thereof.

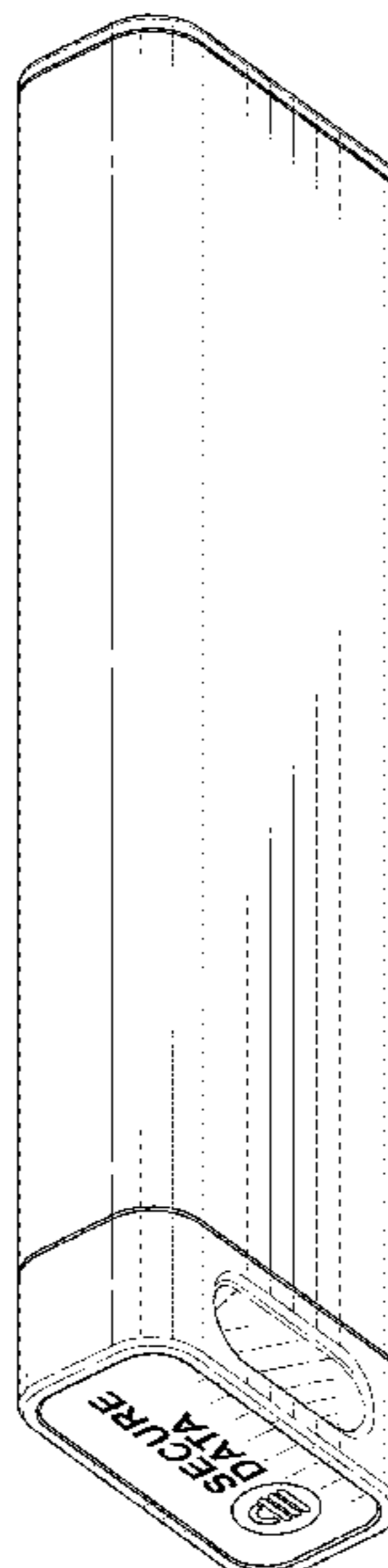
FIG. 8 is a bottom-front-left perspective view thereof.

FIG. 9 is a bottom-rear-left perspective view thereof; and,

FIG. 10 is a top-rear-right perspective view thereof.

The shade lines show surface contour and not surface  
ornamentation.

**1 Claim, 7 Drawing Sheets**



(56)

References Cited

U.S. PATENT DOCUMENTS

D774,514 S	12/2016	Turksu et al.	
D784,357 S *	4/2017	Mombauer .....	D14/480.5
D800,138 S	10/2017	Brown et al.	
D800,139 S	10/2017	Brown et al.	
D806,089 S *	12/2017	Shim .....	D14/480.5
D827,649 S	9/2018	Shim	
D834,580 S	11/2018	Wang	
D845,964 S *	4/2019	Kim .....	D14/480.5
D873,274 S *	1/2020	Bolotin .....	D14/480.5
D874,470 S *	2/2020	Bolotin .....	D14/480.5
2006/0038023 A1 *	2/2006	Brewer .....	H05K 5/0278 235/492

OTHER PUBLICATIONS

U.S. Appl. No. 29/656,335, filed Jul. 11, 2018, Electronic Device,  
now U.S. Pat. No. D. 874,470.

U.S. Appl. No. 29/717,025, filed Dec. 13, 2019, Electronic Device.  
“U.S. Appl. No. 29/648,875, Restriction Requirement dated Jun. 6, 2019”, 5 pgs.  
“U.S. Appl. No. 29/656,335, Restriction Requirement dated Jun. 6, 2019”, 5 pgs.  
“U.S. Appl. No. 29/648,875, filed Jul. 31, 2019 to Restriction Requirement dated Jun. 16, 2019”, 5 pgs.  
“U.S. Appl. No. 29/656,335, filed Jul. 31, 2019 to Restriction Requirement dated Jun. 6, 2019”, 5 pgs.  
“U.S. Appl. No. 29/648,875, Notice of Allowance dated Oct. 10, 2019”, 7 pgs.  
“U.S. Appl. No. 29/656,335, Notice of Allowance dated Oct. 2, 2019”, 7 pgs.  
“U.S. Appl. No. 29/648,875, Corrected Notice of Allowability dated Nov. 6, 2019”, 2 pgs.  
“U.S. Appl. No. 29/656,335, Corrected Notice of Allowability dated Dec. 20, 2019”, 2 pgs.  
“U.S. Appl. No. 29/717,025, Ex Parte Quayle Action dated Apr. 10, 2020”, 5 pgs.

\* cited by examiner

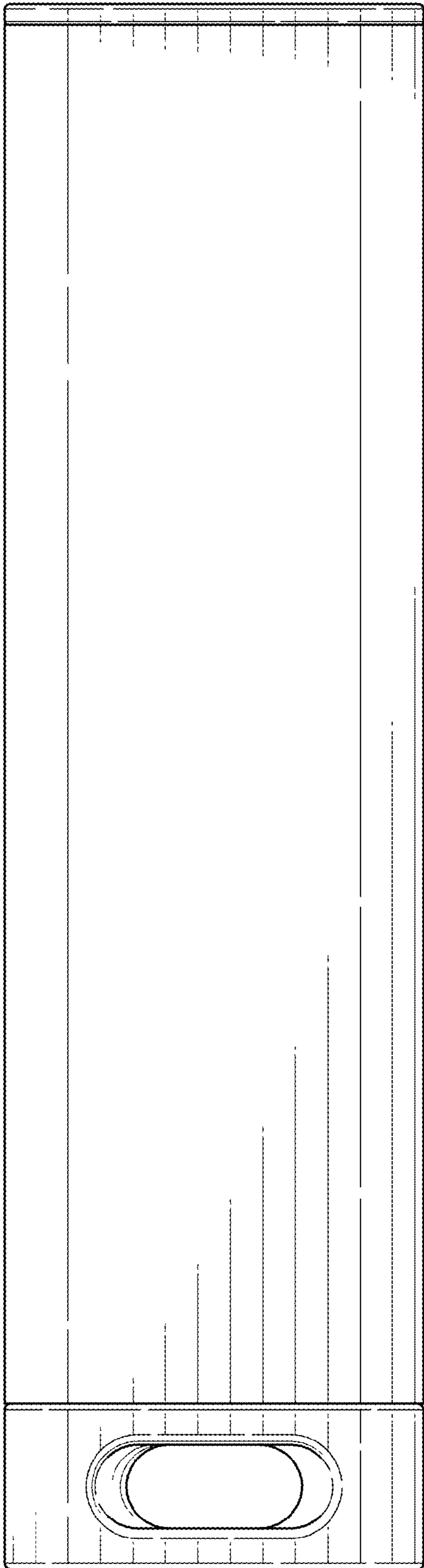


FIG. 1

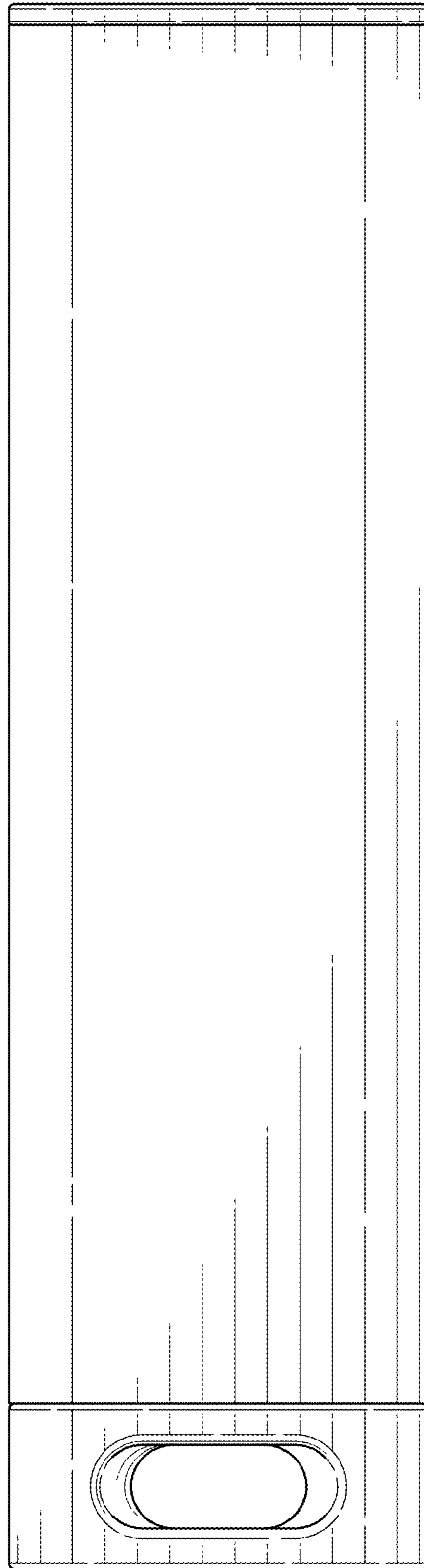


FIG. 2

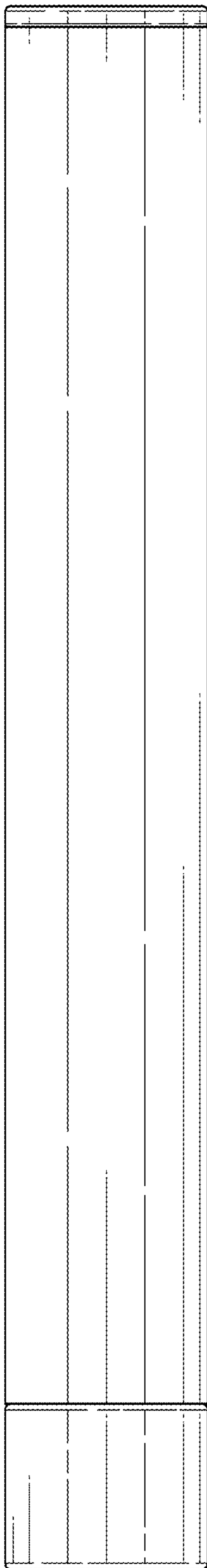


FIG. 3

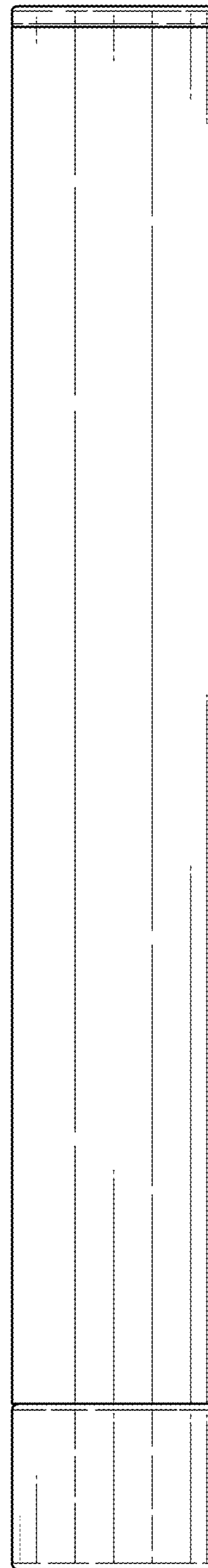
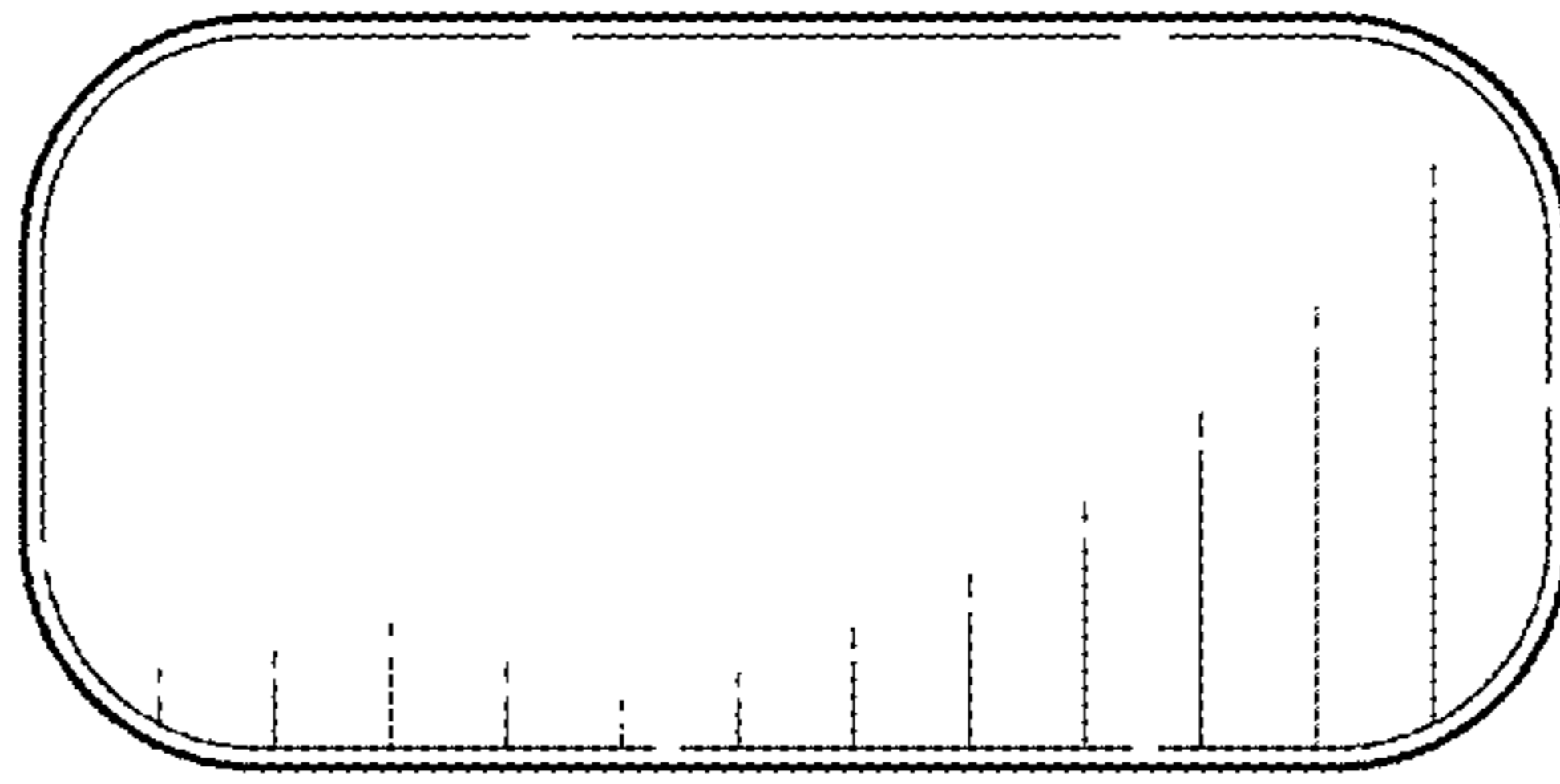


FIG. 4



**FIG. 5**



**FIG. 6**

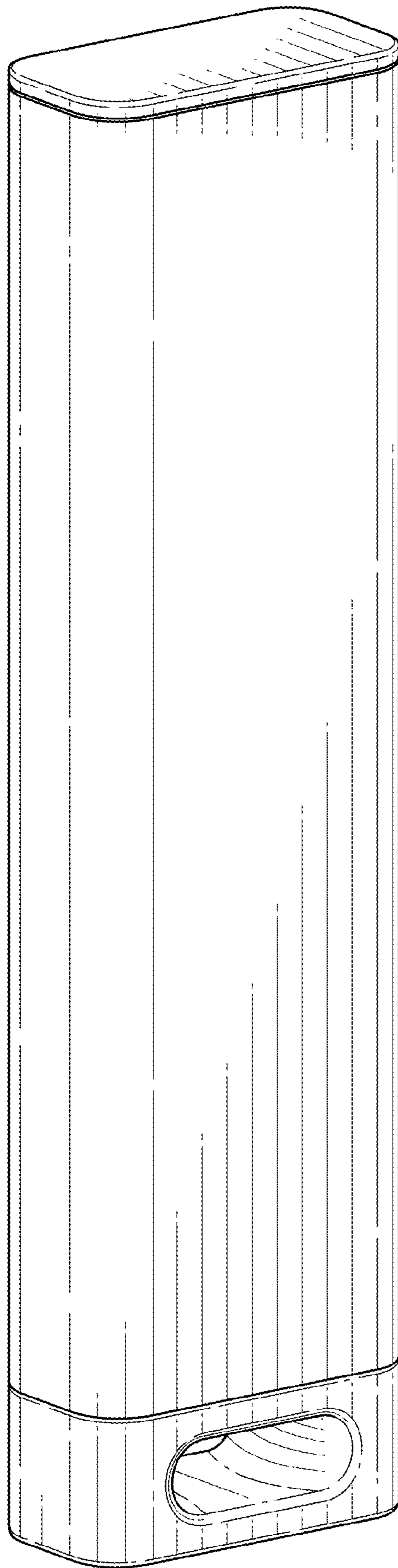


FIG. 7

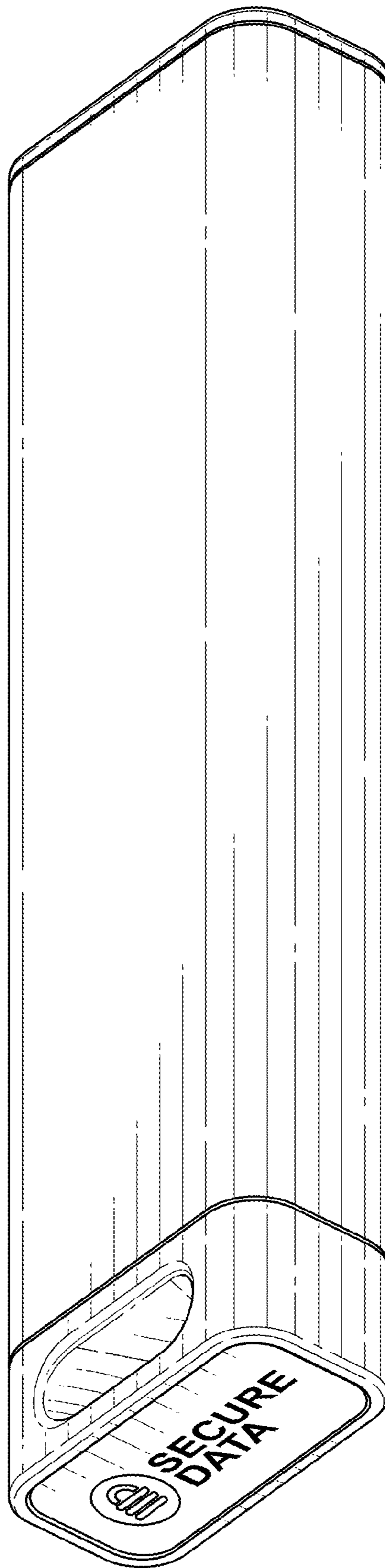


FIG. 8

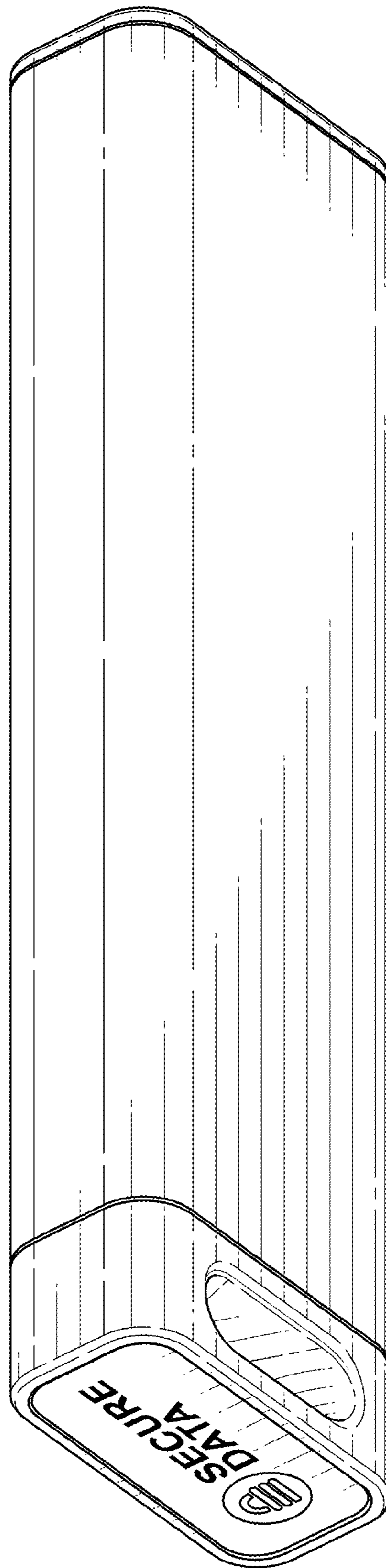


FIG. 9



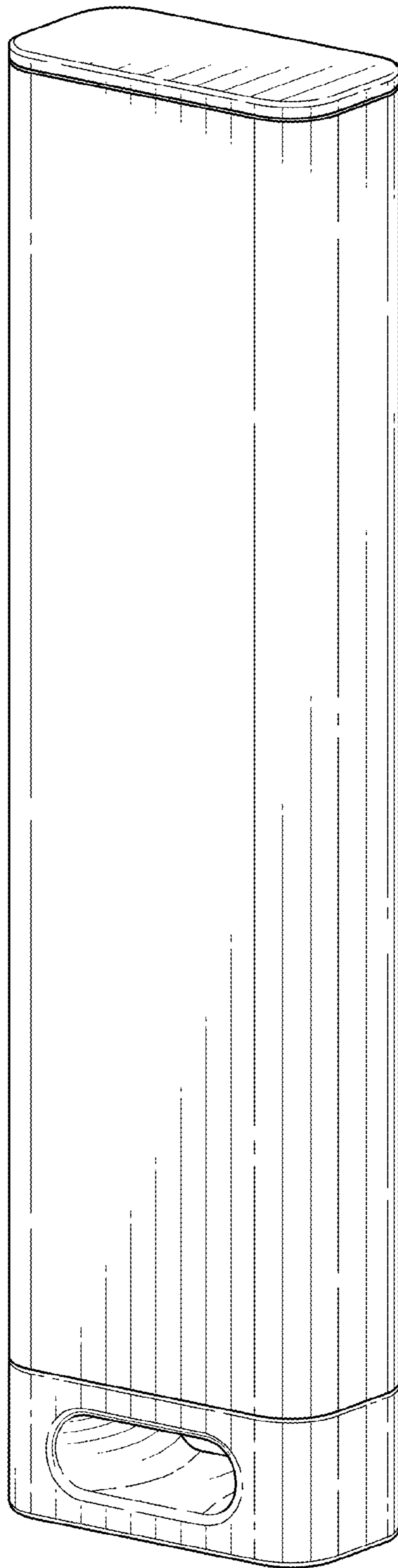


FIG. 10

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : D890,769 S  
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INVENTOR(S) : Bolotin et al.

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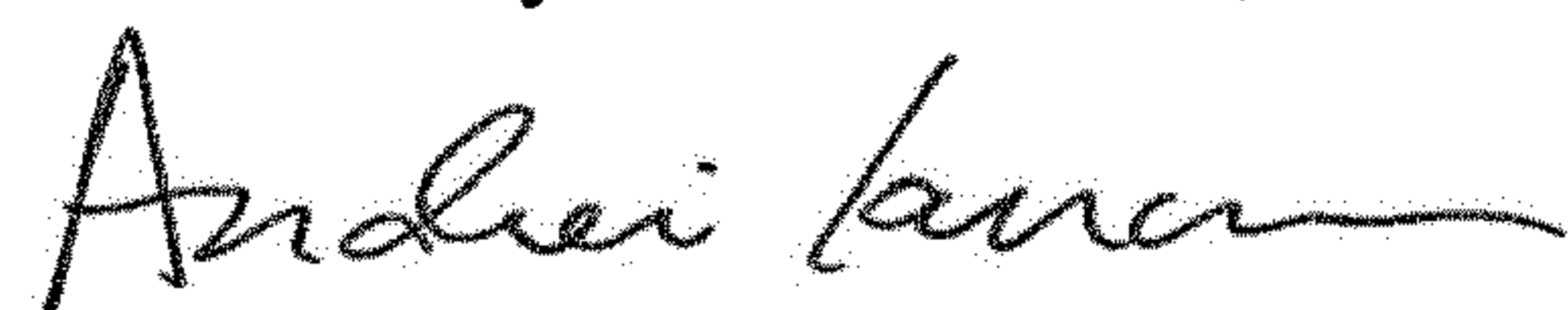
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page

On page 2, in Column 2, under "Other Publications", Line 7, delete "Jun. 16," and insert --Jun. 6,--  
therefor

On page 2, in Column 2, under "Other Publications", Line 10, delete "Oct. 10," and insert --Oct. 1,--  
therefor

Signed and Sealed this  
Tenth Day of November, 2020



Andrei Iancu  
*Director of the United States Patent and Trademark Office*