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(12) **United States Design Patent**  
**Kim et al.**

(10) **Patent No.:** **US D886,073 S**  
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(54) **FLEXIBLE PRINTED CIRCUIT BOARD**

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(73) Assignee: **GIGALANE CO., LTD.**, Hwaseong-si, Gyeonggi-Do (KR)

(\*\*) Term: **15 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (12) Cl.** ..... **13-03**

(52) **U.S. Cl.**  
USPC ..... **D13/182**

(58) **Field of Classification Search**

USPC ..... D24/187, 168; D10/52, 81; D11/3; D13/182

CPC ..... H05K 1/028; H05K 1/118; H05K 1/0221; H05K 1/0237; H05K 1/0242; H05K 1/025; H05K 1/0266; H05K 1/0271; H05K 1/0393; H05K 1/111; H05K 1/14; H05K 1/189; H05K 2201/09327; H05K 2201/09618; H05K 2201/09663; H05K 2201/09909;

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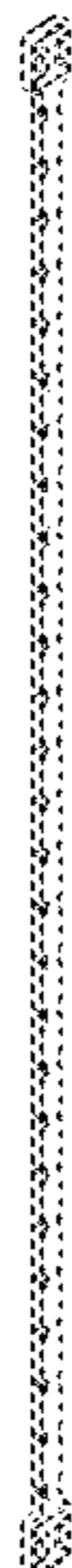
(57) **CLAIM**

The ornamental design for a flexible printed circuit board, as shown and described.

**DESCRIPTION**

**1.1** is a front perspective view of a flexible printed circuit board showing our new design;  
**1.2** is a front elevation view thereof;  
**1.3** is a rear elevation view thereof;  
**1.4** is a left side elevation view thereof;  
**1.5** is a right side elevation view thereof;  
**1.6** is a top plan view thereof;  
**1.7** is a bottom plan view thereof;  
**1.8** is a rear perspective view thereof;  
**1.9** is an enlarged perspective view of a portion of **1.1**;  
**1.10** is a front elevation view thereof;  
**1.11** is a rear elevation view thereof; and  
**1.12** is a left side elevation view thereof.

(Continued)



The dot dash broken lines depict the boundary of the area of enlargement and form no part of the claimed design.

**1 Claim, 12 Drawing Sheets**

(58) **Field of Classification Search**

CPC ..... H05K 2201/2009; H05K 2203/048; H05K 2203/082; H05K 3/28; H05K 3/363; A61B 2562/046; A61B 5/04001; A61B 5/04085; A61B 5/0531; A61B 5/4893

See application file for complete search history.

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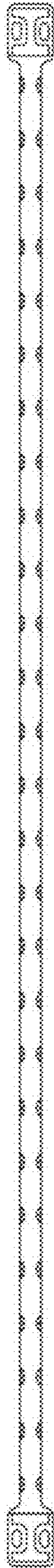
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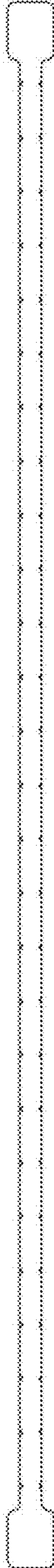
1.1



1.2



1.3



1.4



1.5



**1.6**

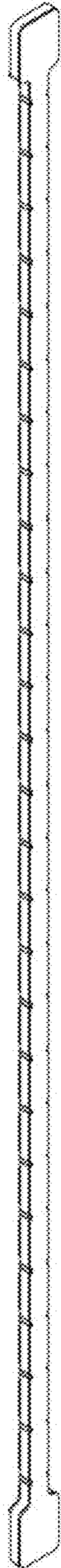




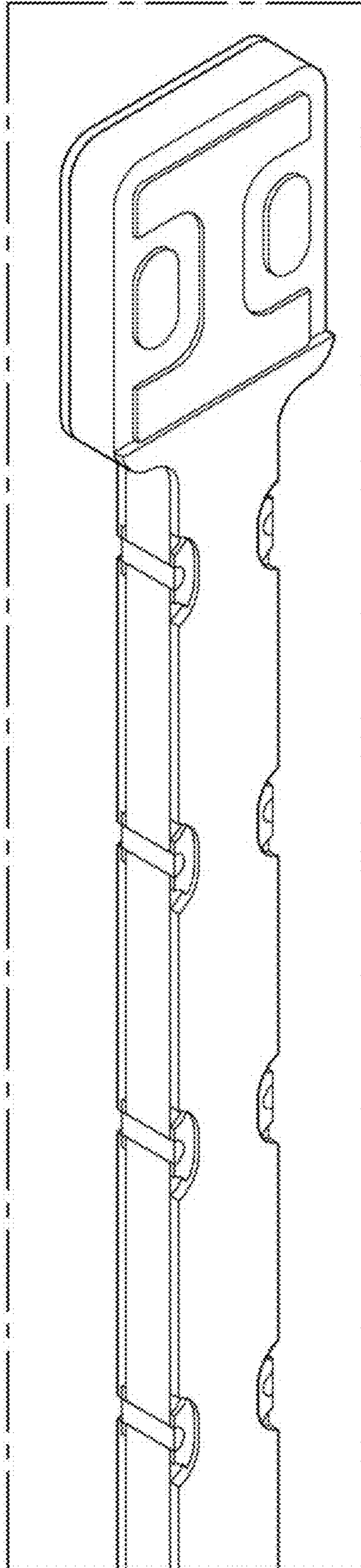
1.7



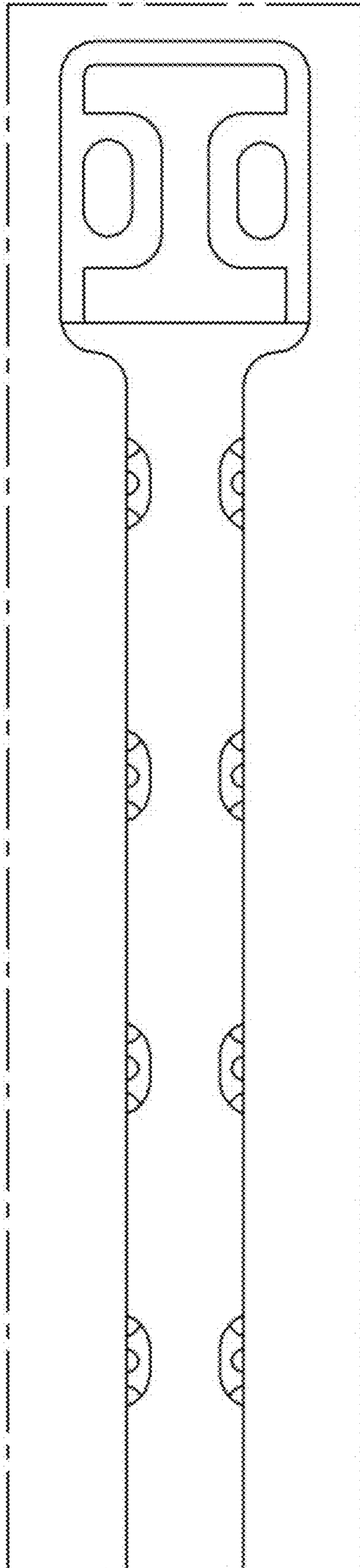
1.8



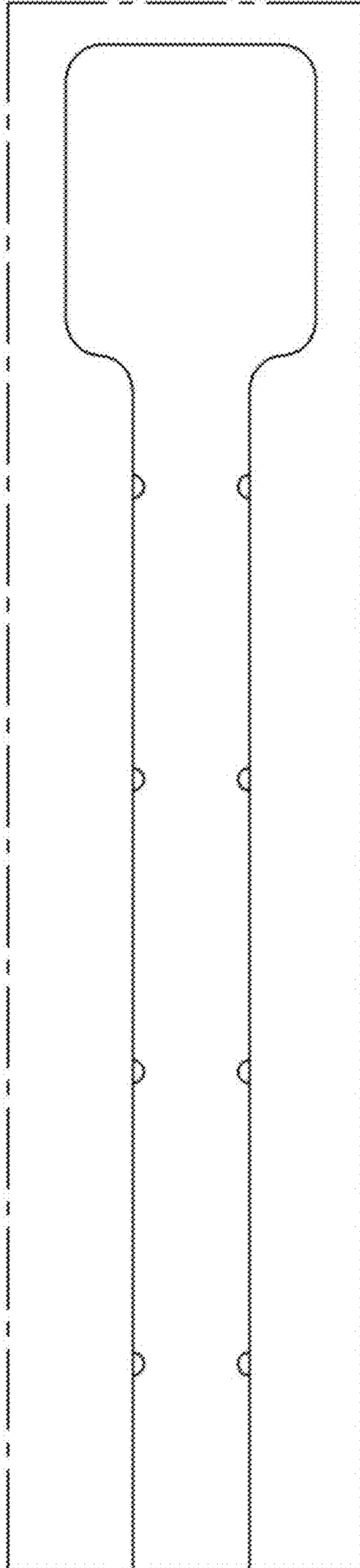
1.9



1.10



1.11



1.12

