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(12) **United States Design Patent**
MacDonald et al.

(10) **Patent No.:** **US D886,072 S**
(45) **Date of Patent:** **** Jun. 2, 2020**

- (54) **CIRCUIT CUBE**
- (71) Applicant: **Tenka Inc.**, Willits, CA (US)
- (72) Inventors: **Nathaniel W. MacDonald**, Mill Valley, CA (US); **John F Schuster**, San Francisco, CA (US)
- (73) Assignee: **Tenka Inc.**, Willits, CA (US)
- (**) Term: **15 Years**
- (21) Appl. No.: **29/702,958**
- (22) Filed: **Aug. 22, 2019**

Related U.S. Application Data

(62) Division of application No. 29/609,479, filed on Jun. 30, 2017, now Pat. No. Des. 864,131.

(51) **LOC (12) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
 USPC D13/182; 174/68.1, 250, 253, 255, 256;
 318/567, 568.1; 361/600, 601, 718, 719,
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 361/807; 439/55, 65, 68, 76.1, 93, 95
 CPC H05K 3/00; H05K 3/30; H05K 3/301;
 H05K 3/303; H05K 3/34; H05K 3/3405;
 H05K 3/341; H05K 3/36; H05K 3/361;
 H05K 3/363; H05K 3/40; H05K 7/14;
 H05K 7/1422; H05K 7/00; H05K 1/18;
 H05K 1/02; H05K 1/181; H05K 1/182;
 H05K 1/183; H05K 1/184; H05K 1/189;
 H05K 1/00

See application file for complete search history.

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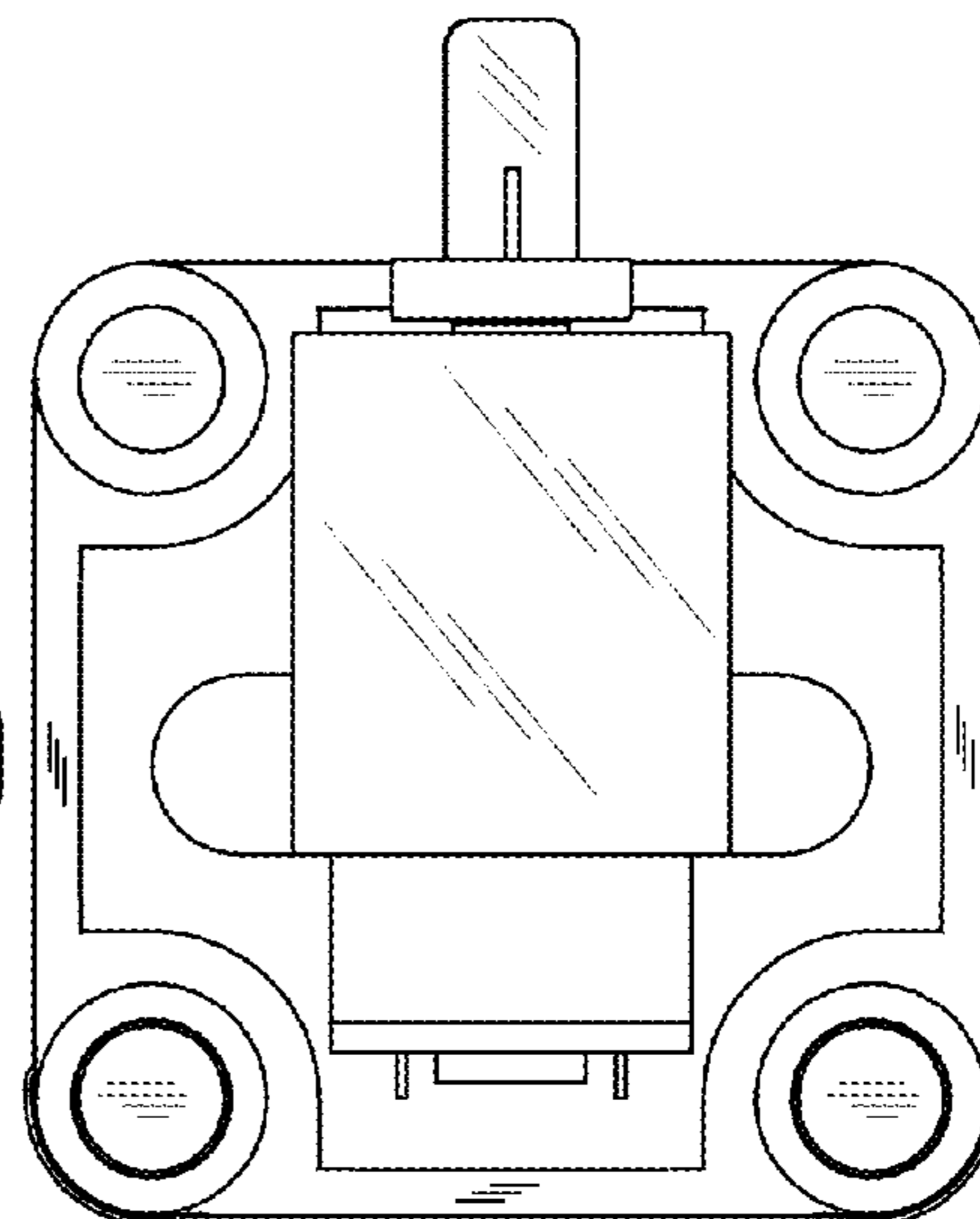
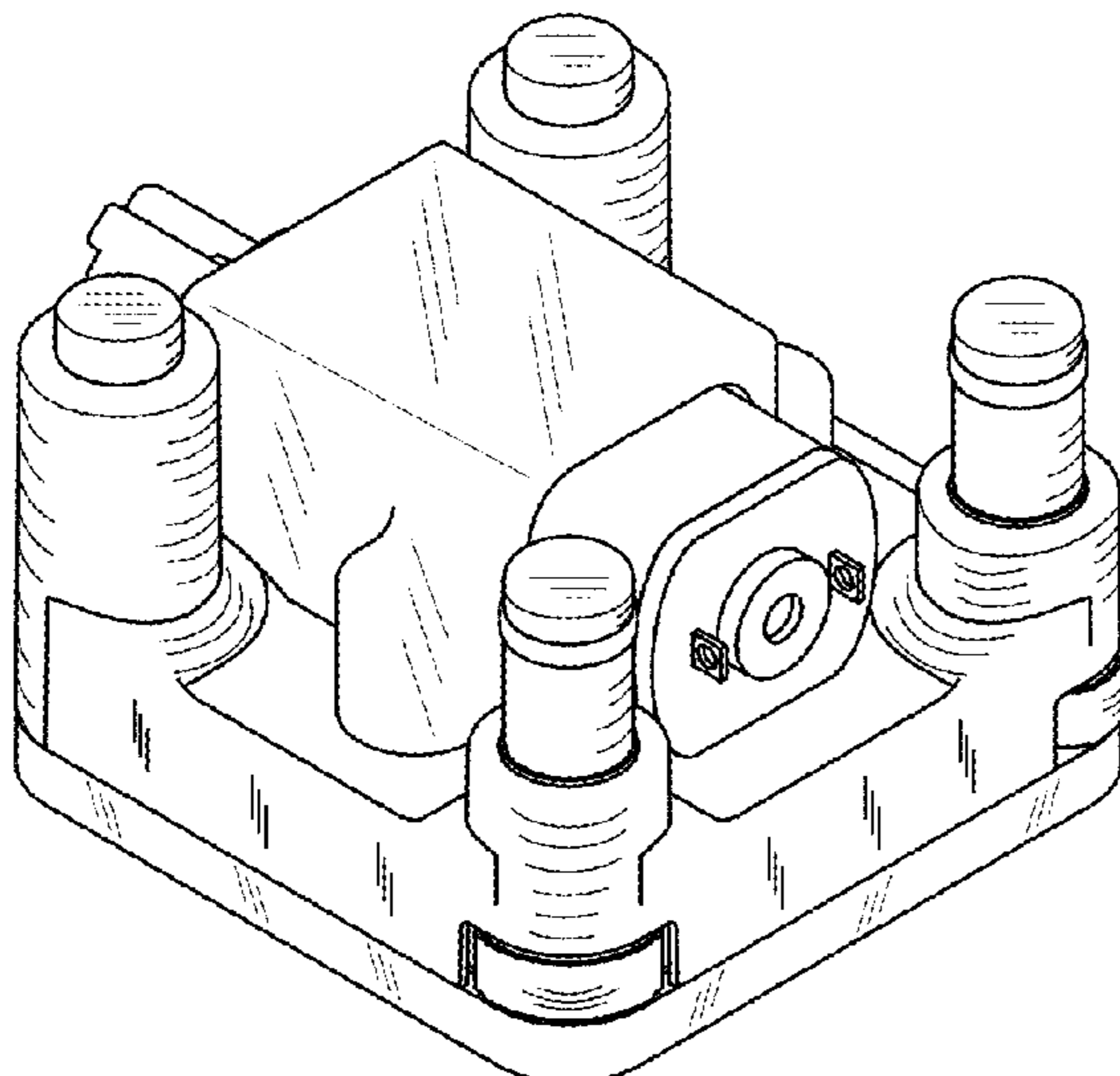
(57) **CLAIM**

The ornamental design for a circuit cube, as shown and described.

DESCRIPTION

FIG. 1 is a top perspective view of a circuit cube, showing our new design;
 FIG. 2 is a top view thereof;
 FIG. 3 is a bottom view thereof;
 FIG. 4 is a front view thereof;
 FIG. 5 is a rear view thereof;
 FIG. 6 is a left view thereof; and,
 FIG. 7 is a right view thereof.
 The broken lines shown in the drawings represent portions of the circuit cube that form no part of the claimed design.

1 Claim, 3 Drawing Sheets



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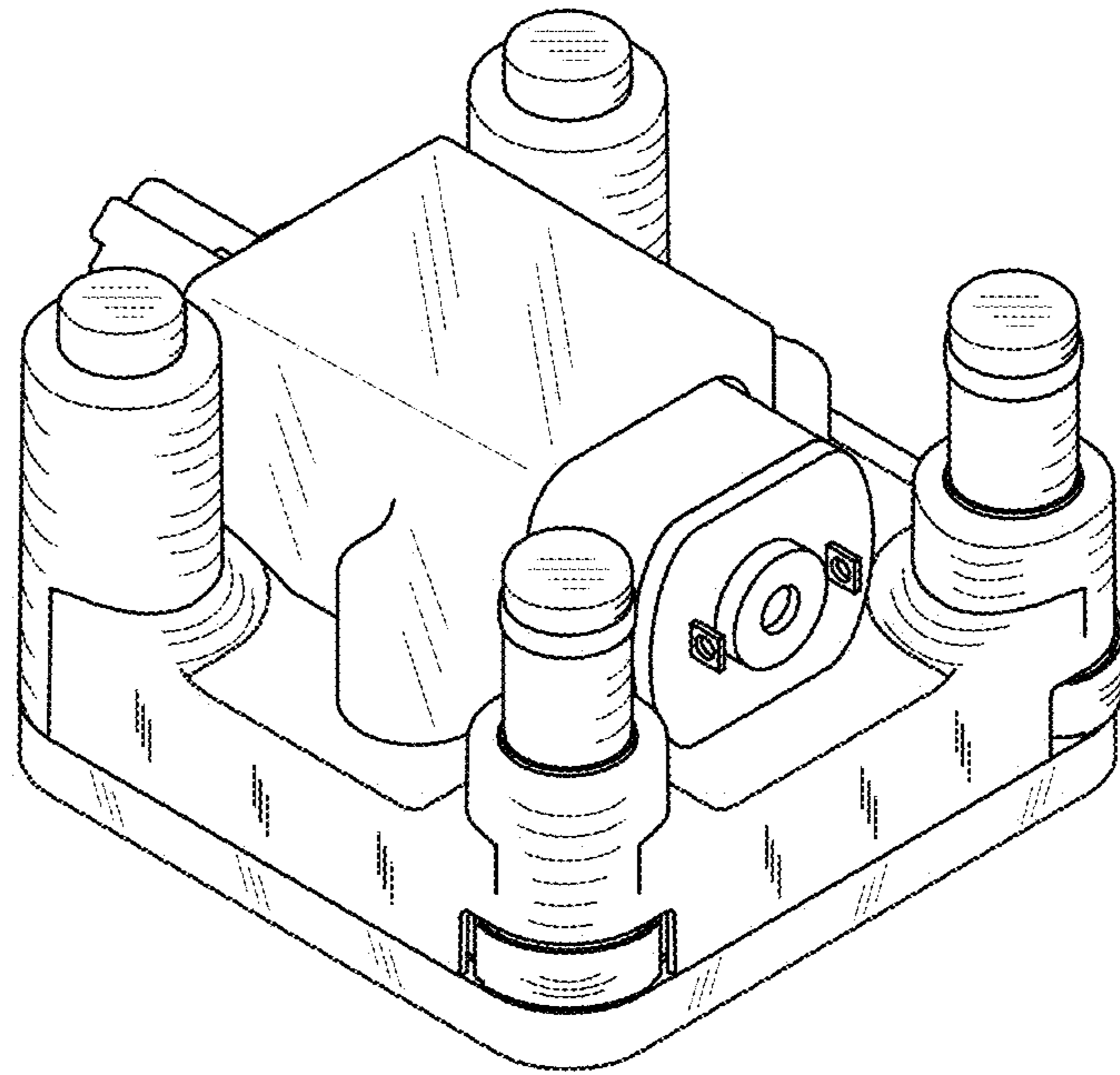


FIG. 1

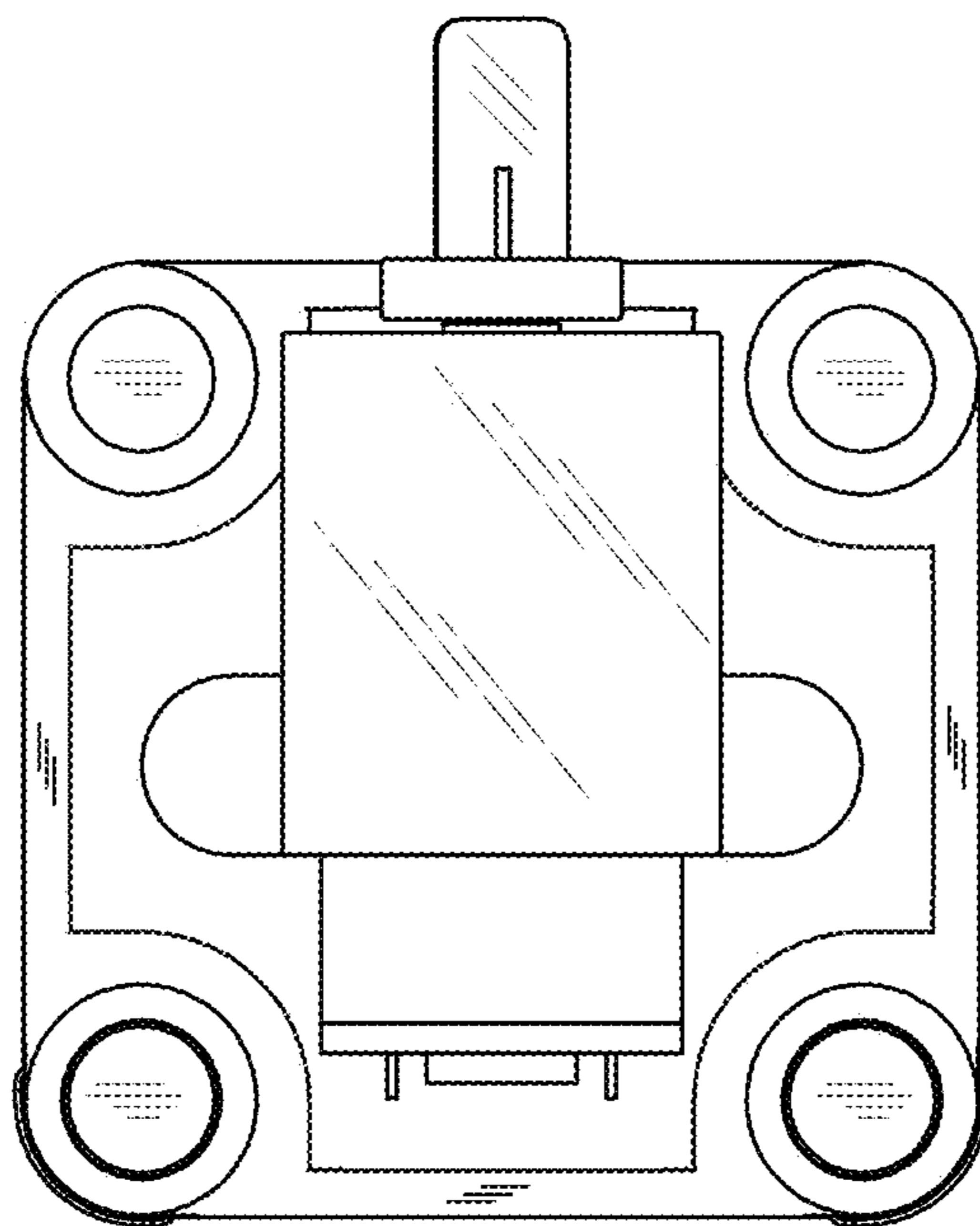


FIG. 2

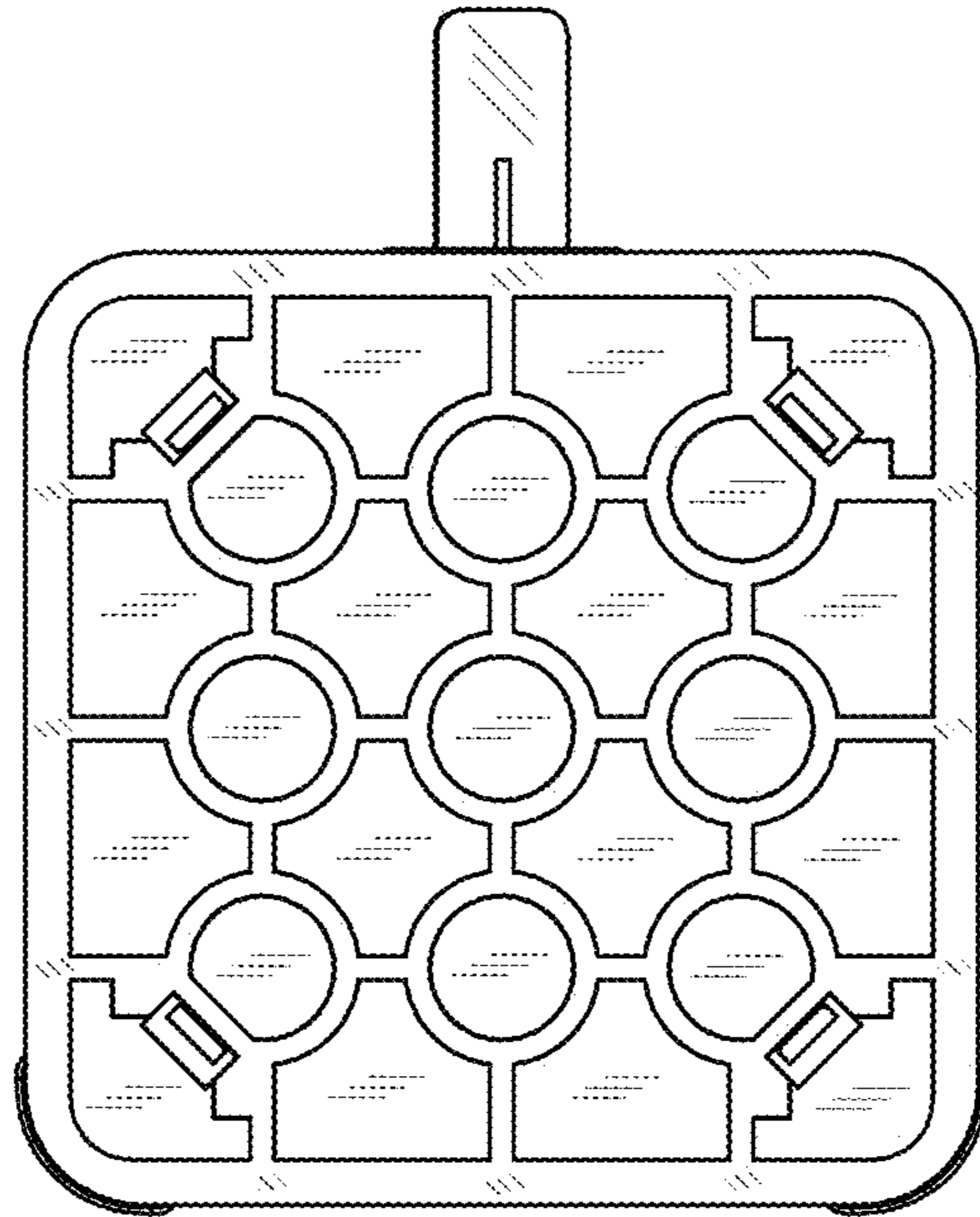


FIG. 3

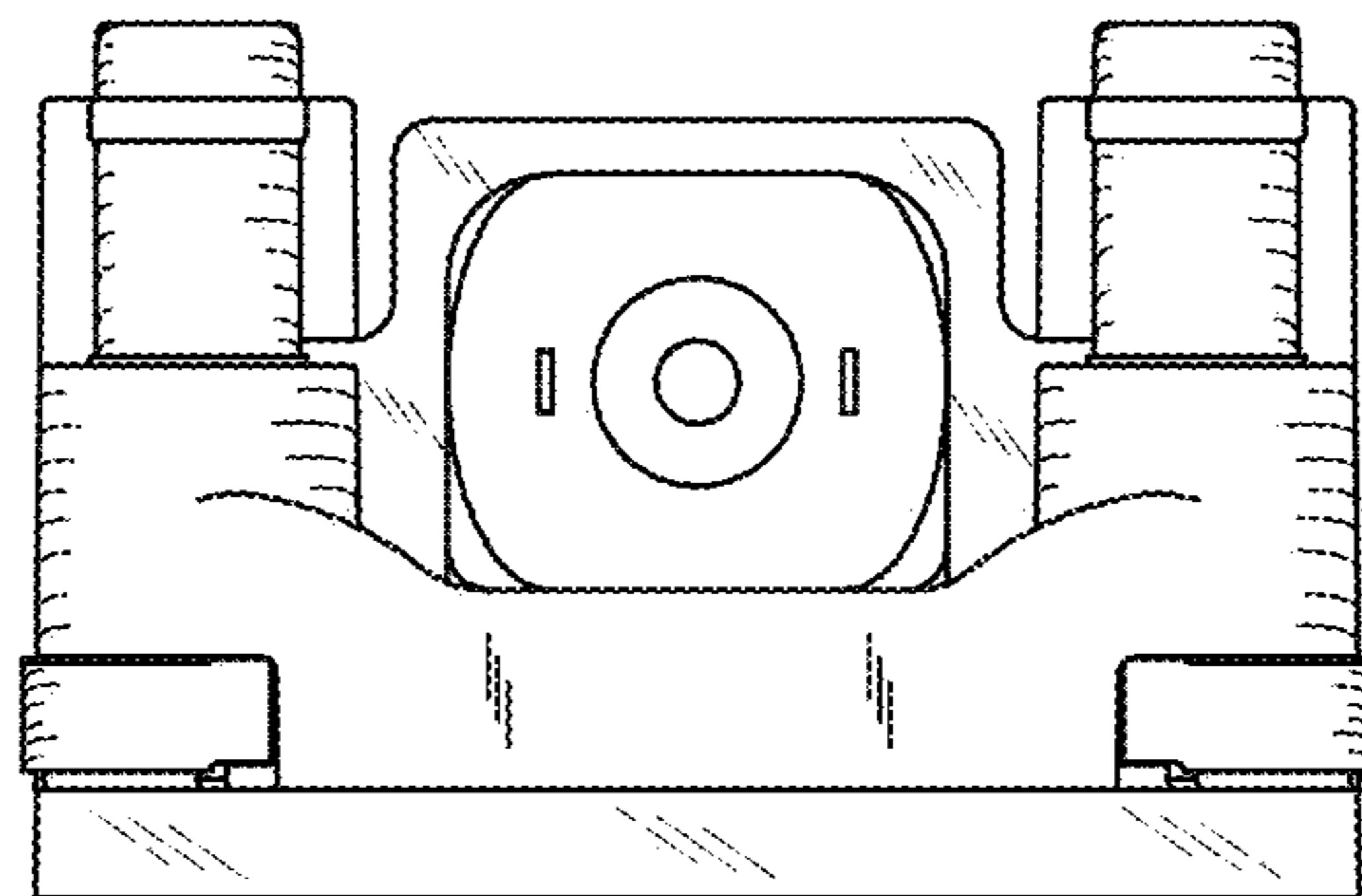


FIG. 4

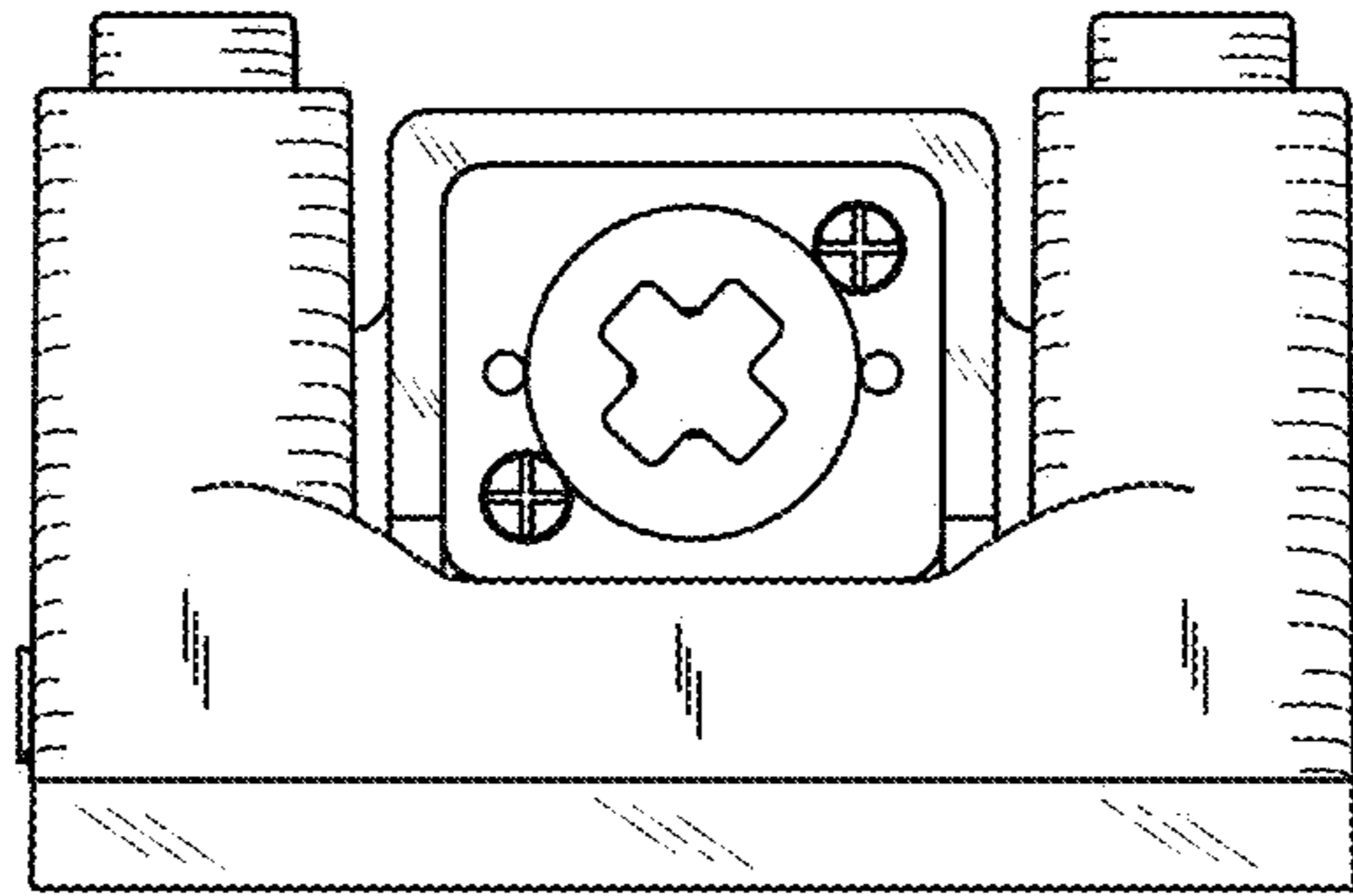


FIG. 5

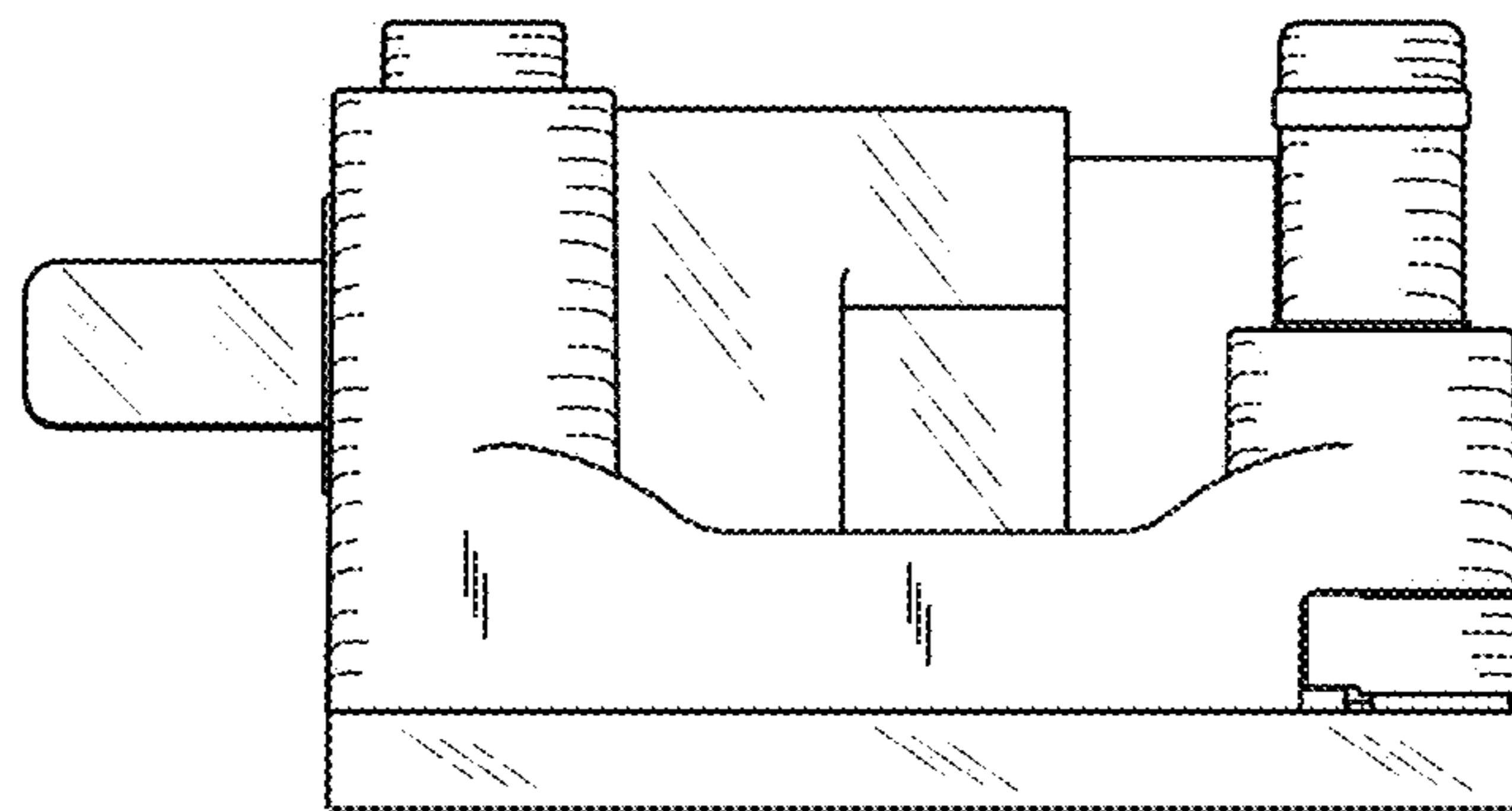


FIG. 6

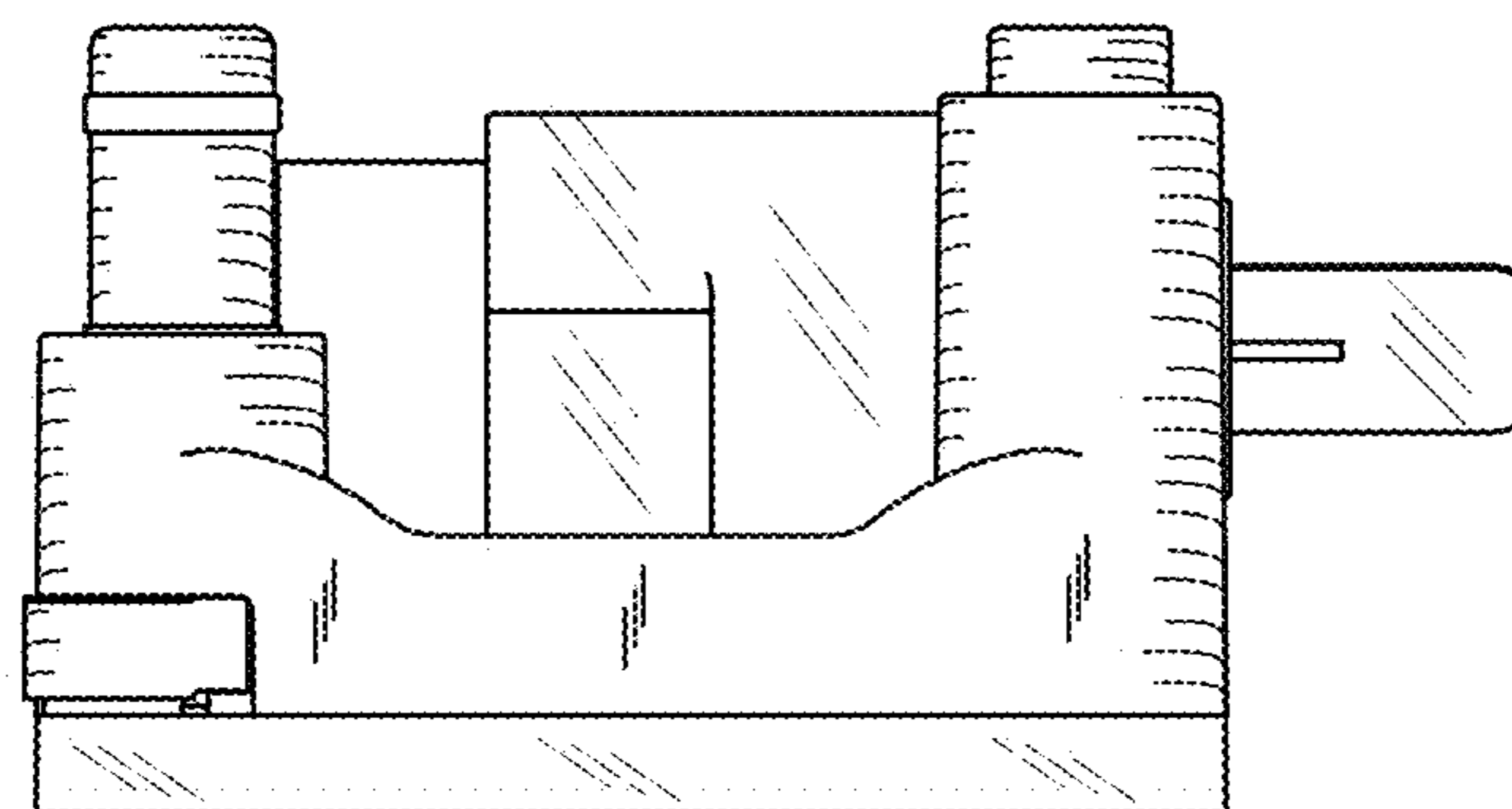


FIG. 7

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : D886,072 S
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DATED : June 2, 2020
INVENTOR(S) : Nathaniel W. MacDonald and John F. Schuster

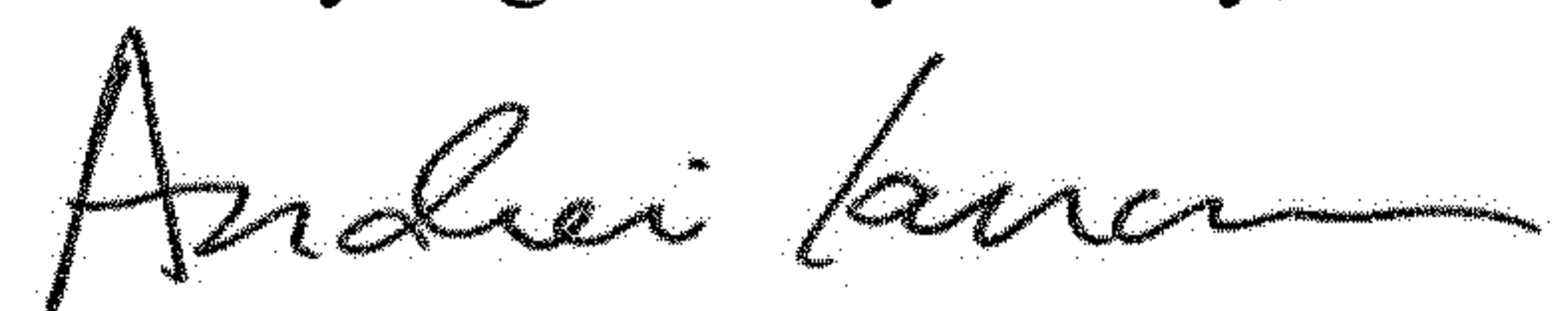
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page
In DESCRIPTION

delete "The broken lines shown in the drawings represent portions of the circuit cube that form no part of the claimed design."

Signed and Sealed this
Twenty-eighth Day of July, 2020



Andrei Iancu
Director of the United States Patent and Trademark Office