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(12)

United States Design Patent
Moore

(10)

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(54)

SLAT WALL TILE

4,506,486 A * 3/1985 Culpepper, Jr. E04D 1/28
52/529

(71)

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4,580,383 A * 4/1986 Pittman E04D 1/08
52/520

(72)

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D333,085 S * 2/1993 Thomsen D8/349
D446,107 S * 8/2001 Carter D8/346
D487,222 S * 3/2004 Balaban D8/354

(Continued)

(73)

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FOREIGN PATENT DOCUMENTS

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DE 102008014038 9/2009
IN 201721000526 1/2017

(21)

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OTHER PUBLICATIONS

(22)

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Retrieved from the internet: <https://github.com/Griffon26/scrumbordtracker> Nov. 25, 2016.

(Continued)

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(63)

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(51)

LOC (12) Cl. **19-06**

(52)

U.S. Cl.
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(58)

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D20/39, 40, 41, 42; D25/140, 141;
D10/61; D6/310, 311, 643, 683; D8/354
CPC B43L 1/00; B43L 1/008; B43L 1/08; B43L
1/04; B43L 5/00; B43L 5/02; G09F 1/12;
G09F 15/00; G09F 15/0006; G09F
15/0068; G09F 15/0012; G09F
2007/1834; G09F 2007/1856
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

1,993,086 A * 3/1935 Chaffee E04D 1/02
52/542

2,188,454 A * 1/1940 Braddock E04D 3/362
52/531

3,703,795 A * 11/1972 Mattes E04F 13/0864
52/521

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(57) **CLAIM**

The ornamental design for a slat wall tile, as shown and described.

DESCRIPTION

FIG. 1 shows a perspective view of the slat wall tile in accordance with the new design.
FIG. 2 shows a front view of FIG. 1.
FIG. 3 shows a rear view of FIG. 1.
FIG. 4 shows a side view of FIG. 1.
FIG. 5 shows an opposite side view from that shown in FIG. 4 of FIG. 1.
FIG. 6 shows a top view of FIG. 1; and,
FIG. 7 shows a bottom view of FIG. 1.
The broken lines shown in the drawings are included to show features that form no part of the claimed design.

1 Claim, 5 Drawing Sheets

(56)

References Cited

U.S. PATENT DOCUMENTS

7,331,150 B2 * 2/2008 Martinique E04D 1/20 52/520

7,891,617 B2 * 2/2011 Tisbo F16B 12/34 248/220.22

8,122,649 B2 * 2/2012 Thomas E04D 1/16 52/100

D687,889 S * 8/2013 Rippinger D19/113

8,677,709 B2 * 3/2014 DiLonardo E04F 13/0837 52/518

9,135,604 B2 9/2015 Ritter et al.

9,239,719 B1 1/2016 Feinstein et al.

D778,105 S * 2/2017 Coffey D7/388

9,605,432 B1 * 3/2017 Robbins E04D 1/12

D811,200 S * 2/2018 Gupta D8/354

D820,904 S * 6/2018 Fiden D17/99

10,167,639 B2 * 1/2019 Smith E04F 13/21

D857,245 S * 8/2019 Smith D25/140

D861,195 S * 9/2019 Wilkie D25/141

2003/0120525 A1 6/2003 Feser et al.

2003/0136067 A1 * 7/2003 Talbacka E04B 2/7457 52/281

2009/0266953 A1 * 10/2009 Goldstein A47F 5/0869 248/214

2014/0020319 A1 * 1/2014 Marchese E04F 13/0837 52/302.3

2015/0347125 A1 12/2015 High et al.

OTHER PUBLICATIONS

Retrieved from the internet: https://www.researchgate.net/publication/30144920_The_dBoard_A_Digital_Scrum_Board_for_Distributed_Software_Development (via 'Download' button); Nov. 30, 2015 Nov. 30, 2015.

Retrived from the internet: <https://confluence.spartez.com/display/ScrumPrint/Setting+up+the+physical+board> Apr. 17, 2018.

* cited by examiner

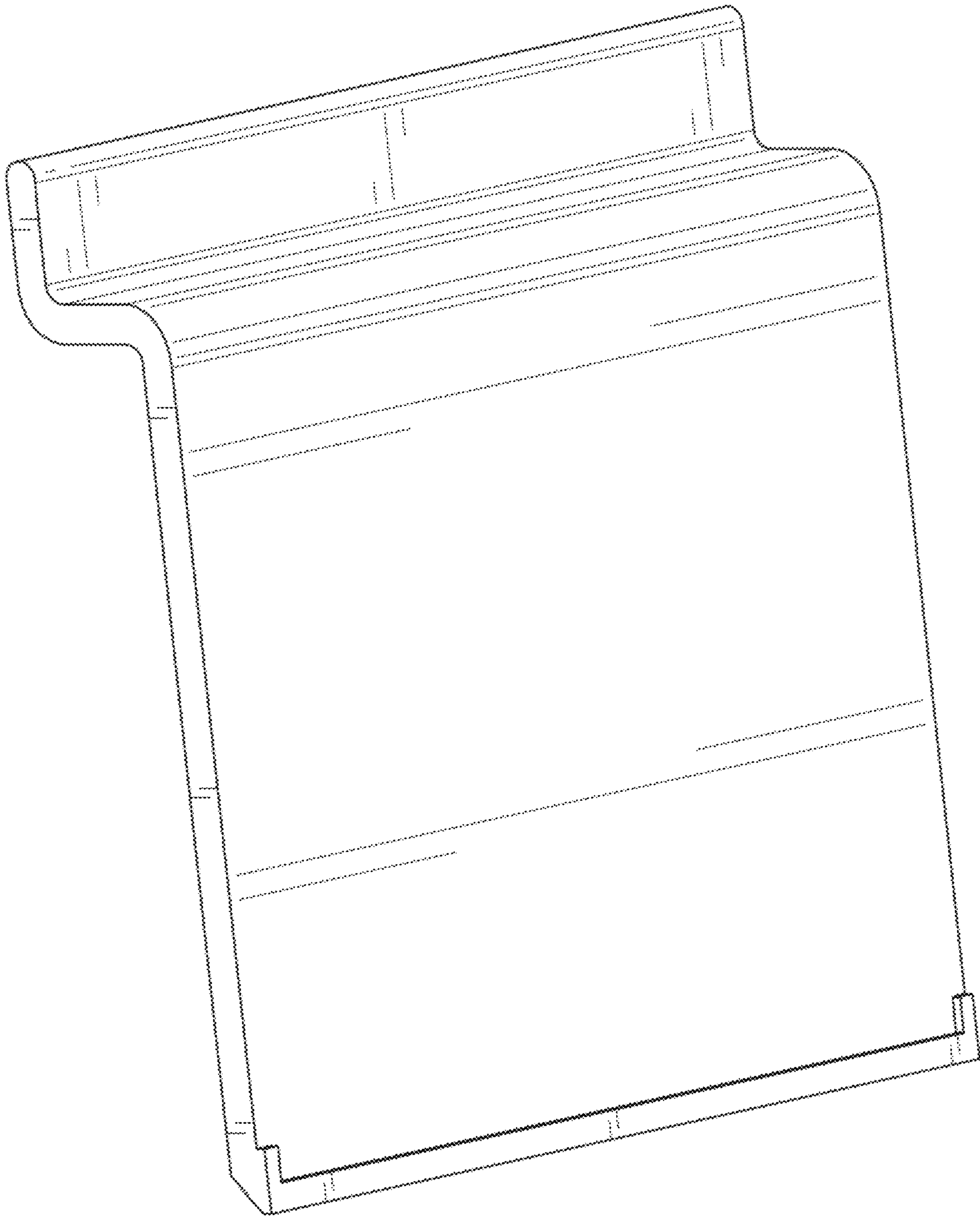


FIG. 1

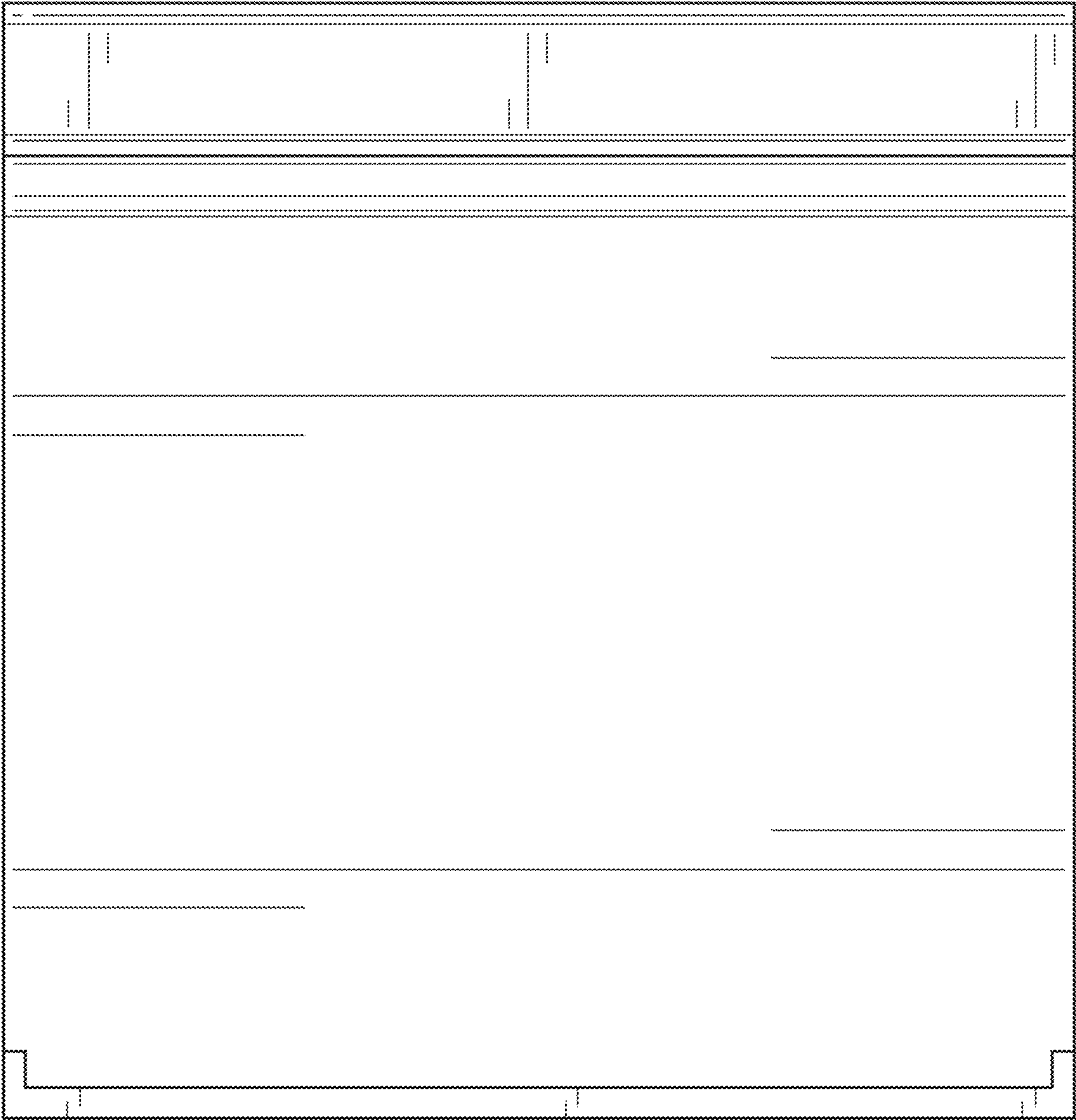


FIG. 2

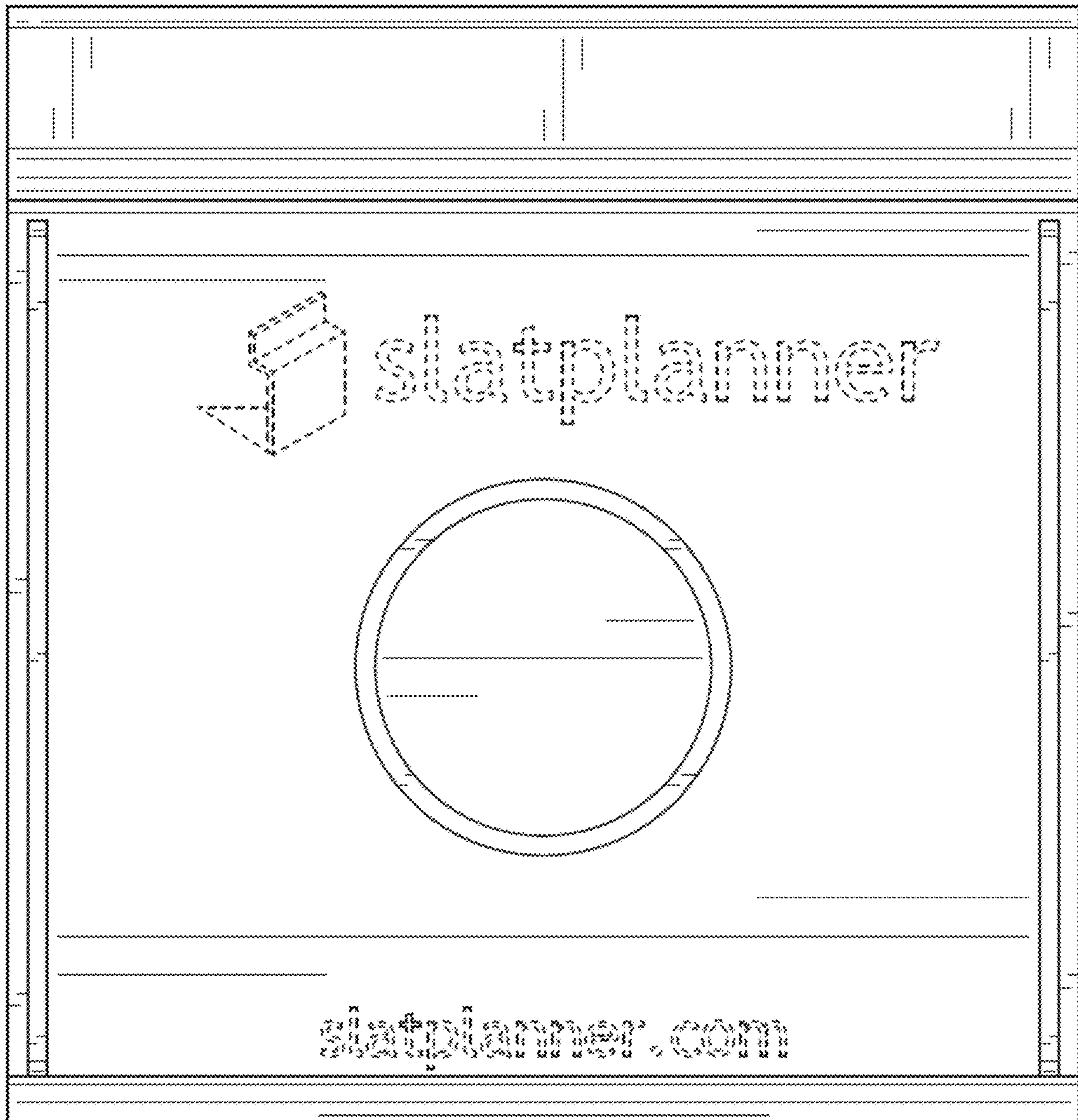


FIG. 3

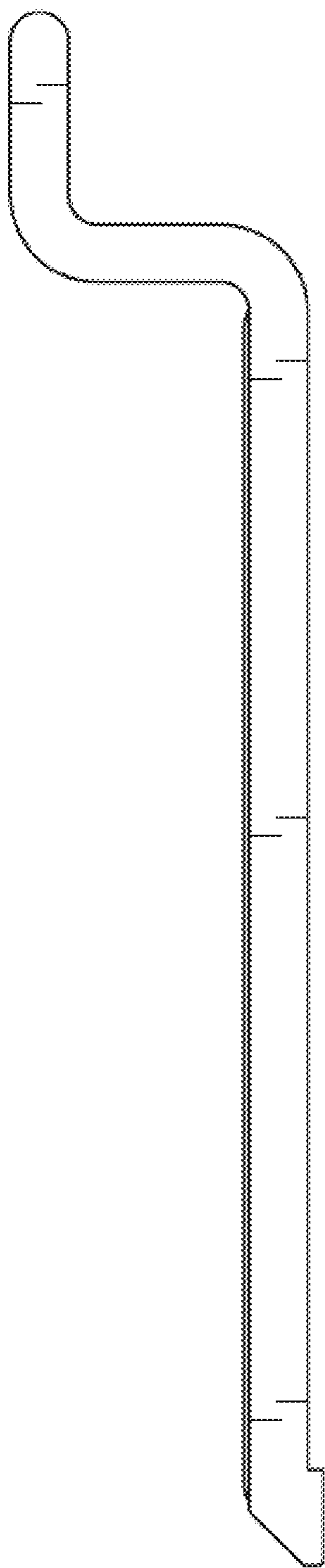


FIG. 4

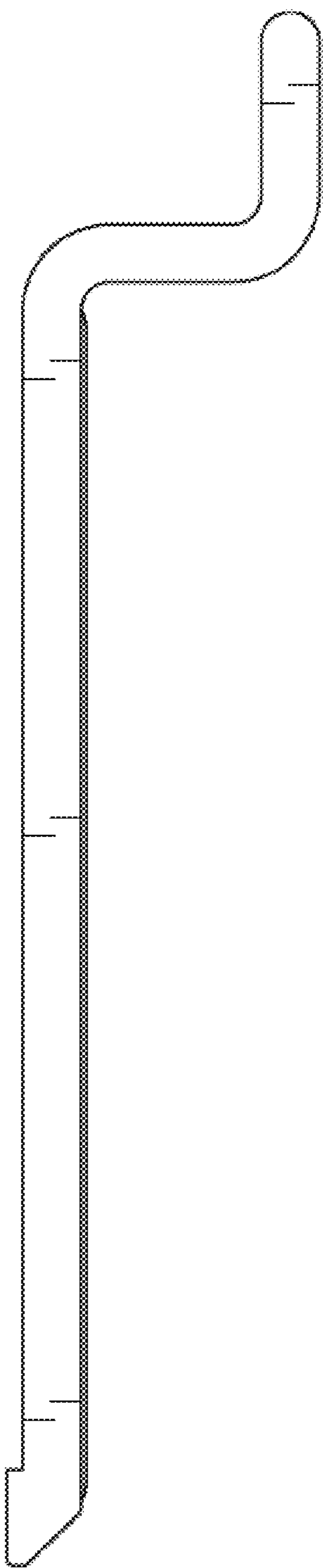


FIG. 5

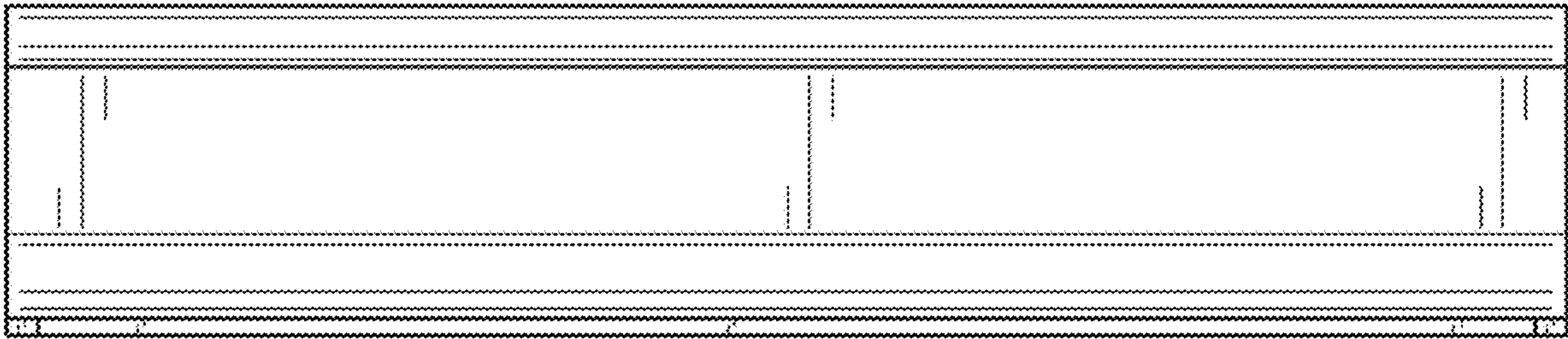


FIG. 6

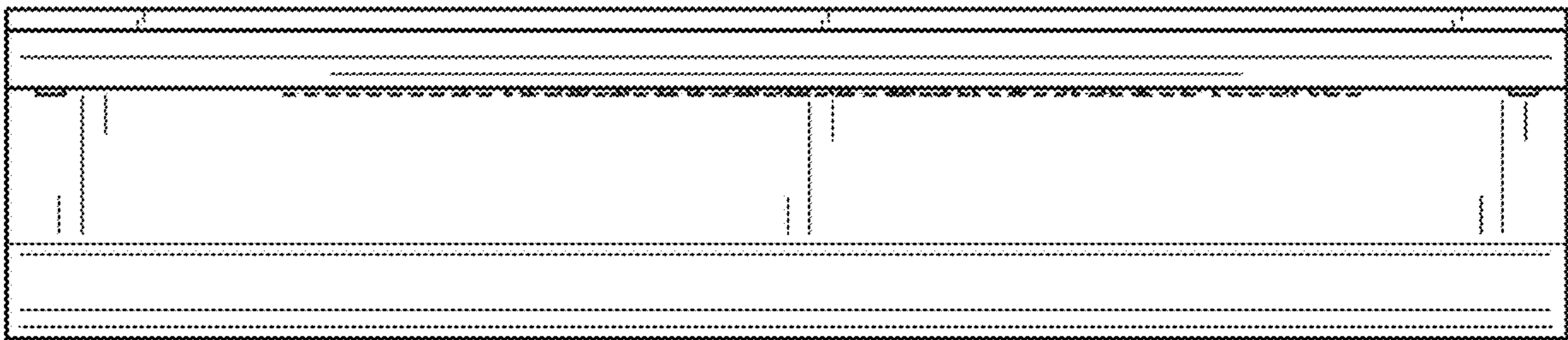


FIG. 7