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(12) **United States Design Patent** (10) **Patent No.:** **US D879,730 S**
Kang et al. (45) **Date of Patent:** **** Mar. 31, 2020**

(54) **CIRCUIT BOARD**(71) Applicant: **SiFive, Inc.**, San Mateo, CA (US)(72) Inventors: **Jack Kang**, San Mateo, CA (US);
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Mulhausen, McLean, VA (US)(73) Assignee: **SiFive, Inc.**, San Mateo, CA (US)(**) Term: **15 Years**(21) Appl. No.: **29/673,813**(22) Filed: **Dec. 18, 2018**(51) LOC (12) Cl. **13-03**

(52) U.S. Cl.

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361/808, 809; 174/250, 253, 255;
363/144, 147; D2/610, 614-623;
D29/113, 117.1, 118CPC H05K 3/30; H05K 3/301; H05K 3/284;
H05K 1/18; H05K 1/181; H05K 1/182;
H05K 1/183; H05K 1/185; H05K 1/00;
H05K 7/1417; H05K 7/02; H05K 7/12;
H05K 7/06; H01L 2924/14

See application file for complete search history.

(56)

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(57) **CLAIM**

The ornamental design for a circuit board, as shown and described.

DESCRIPTION

FIG. 1 is a front, lower, left perspective view of a circuit board showing our new design;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a right view thereof;

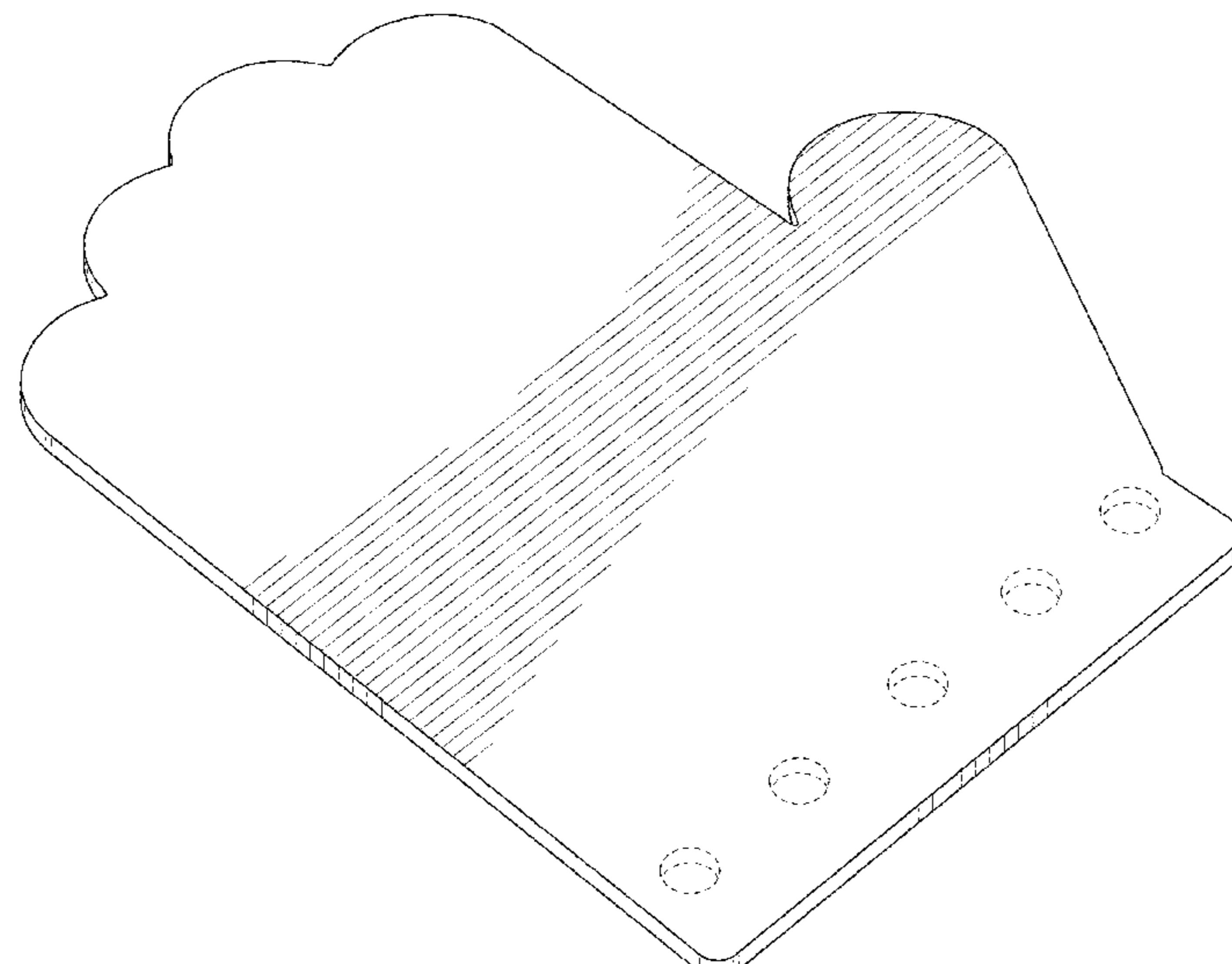
FIG. 5 is a left view thereof;

FIG. 6 is a top view thereof; and

FIG. 7 is a bottom view thereof.

The broken lines in the drawings illustrate unclaimed features of the circuit board and form no part of the claimed design.

1 Claim, 5 Drawing Sheets



(56)

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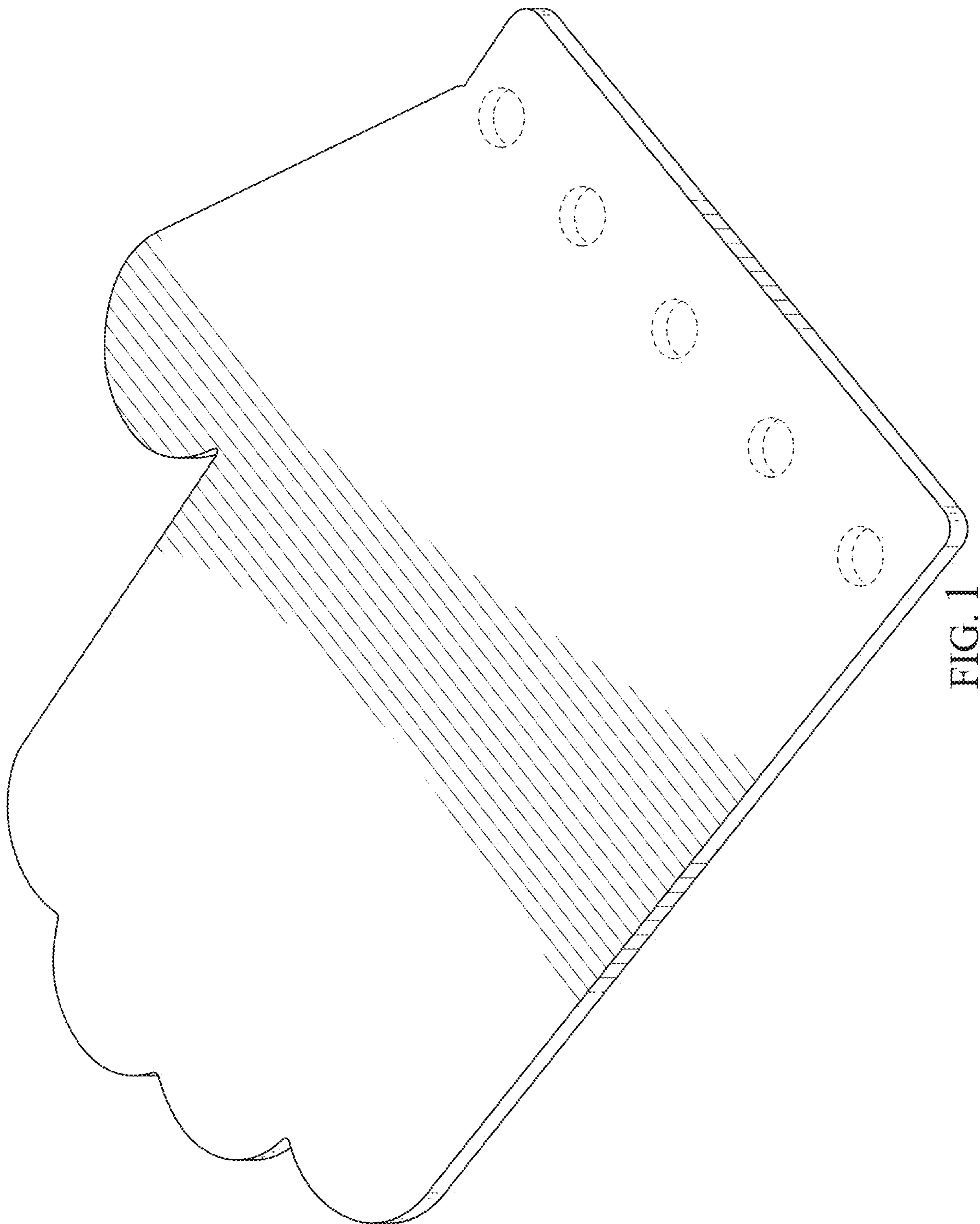
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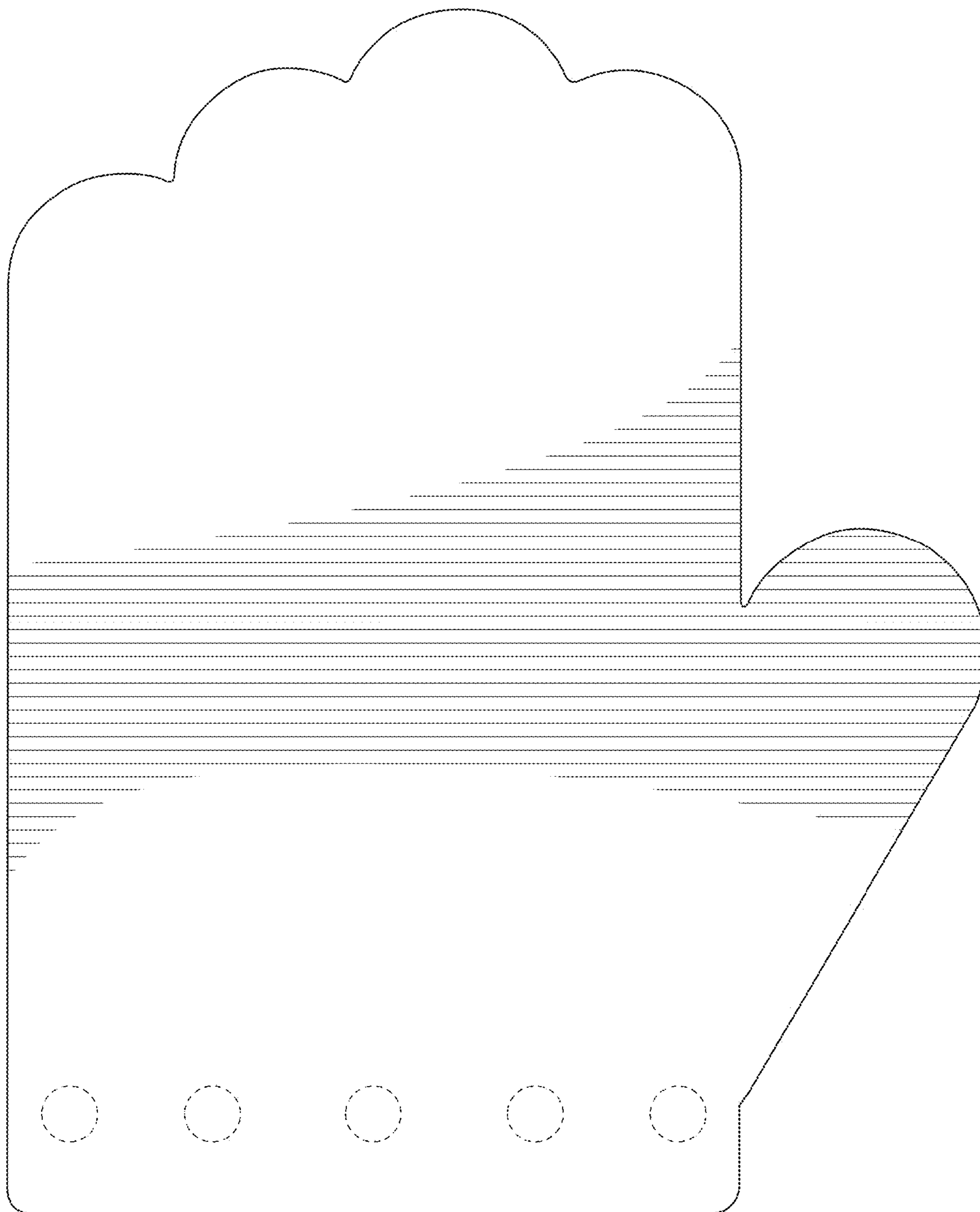


FIG. 2

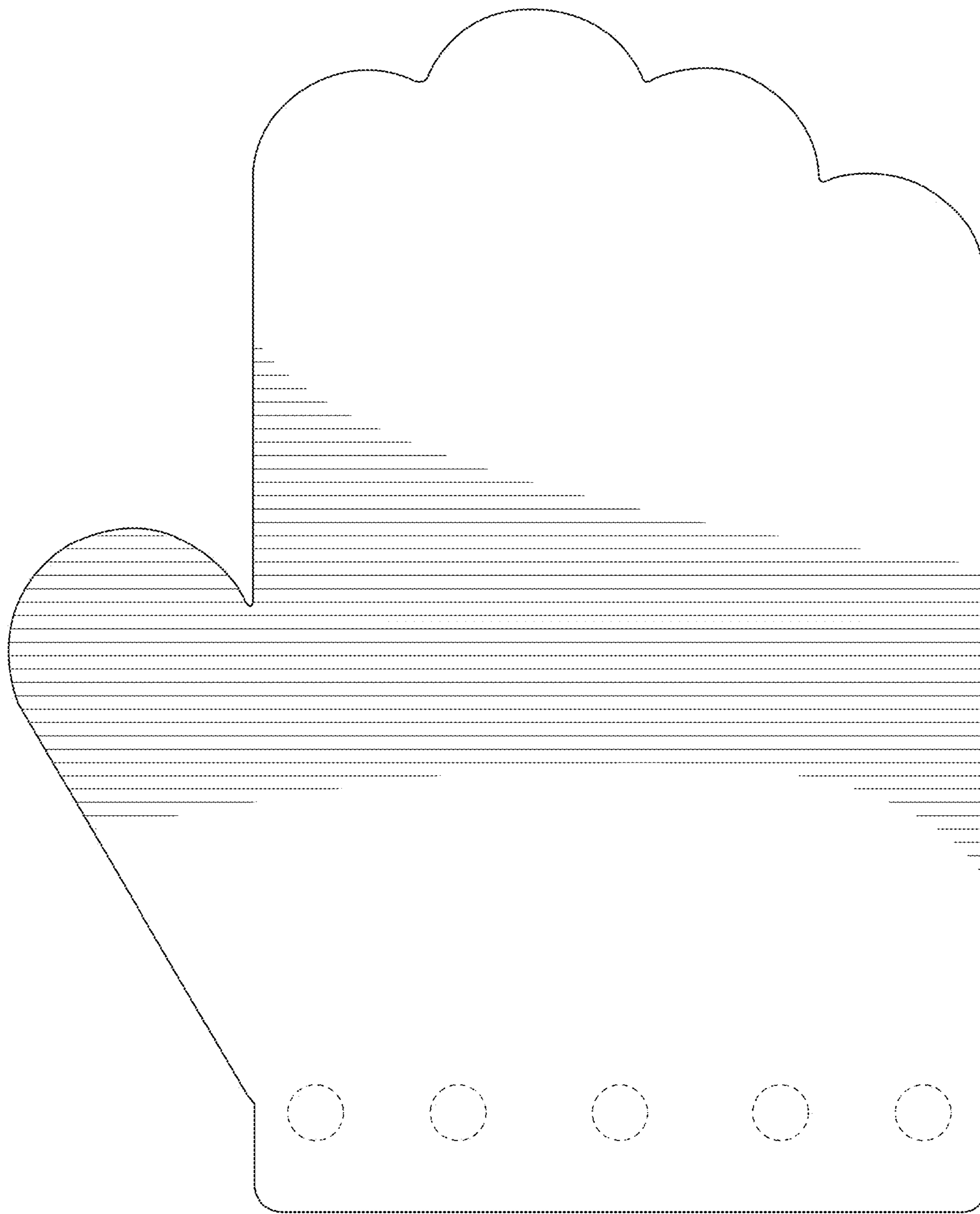


FIG. 3



FIG. 4



FIG. 5

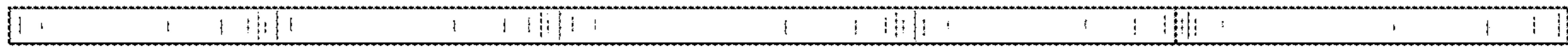


FIG. 6



FIG. 7