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(12) **United States Design Patent** (10) **Patent No.:** **US D877,708 S**
Vinciarelli et al. (45) **Date of Patent:** **** Mar. 10, 2020**

(54) **ELECTRICAL TERMINAL**
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(**) Term: **15 Years**

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(22) Filed: **Nov. 6, 2018**

Related U.S. Application Data

(60) Continuation of application No. 29/614,894, filed on Aug. 24, 2017, now Pat. No. Des. 834,548, which is a division of application No. 29/556,621, filed on Mar. 2, 2016, now Pat. No. Des. 798,249, which is a division of application No. 29/470,060, filed on Oct. 17, 2013, now Pat. No. Des. 754,083.

(51) **LOC (12) Cl.** **13-03**
(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/182; 257/678, 690; 361/679.31,
361/719, 720, 752, 777, 820
CPC H01L 21/00; H01L 21/02; H01L 23/3675;
H01L 23/3677; H01L 23/48; H01L
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H01L 23/49811; H05K 7/1422; H05K
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See application file for complete search history.

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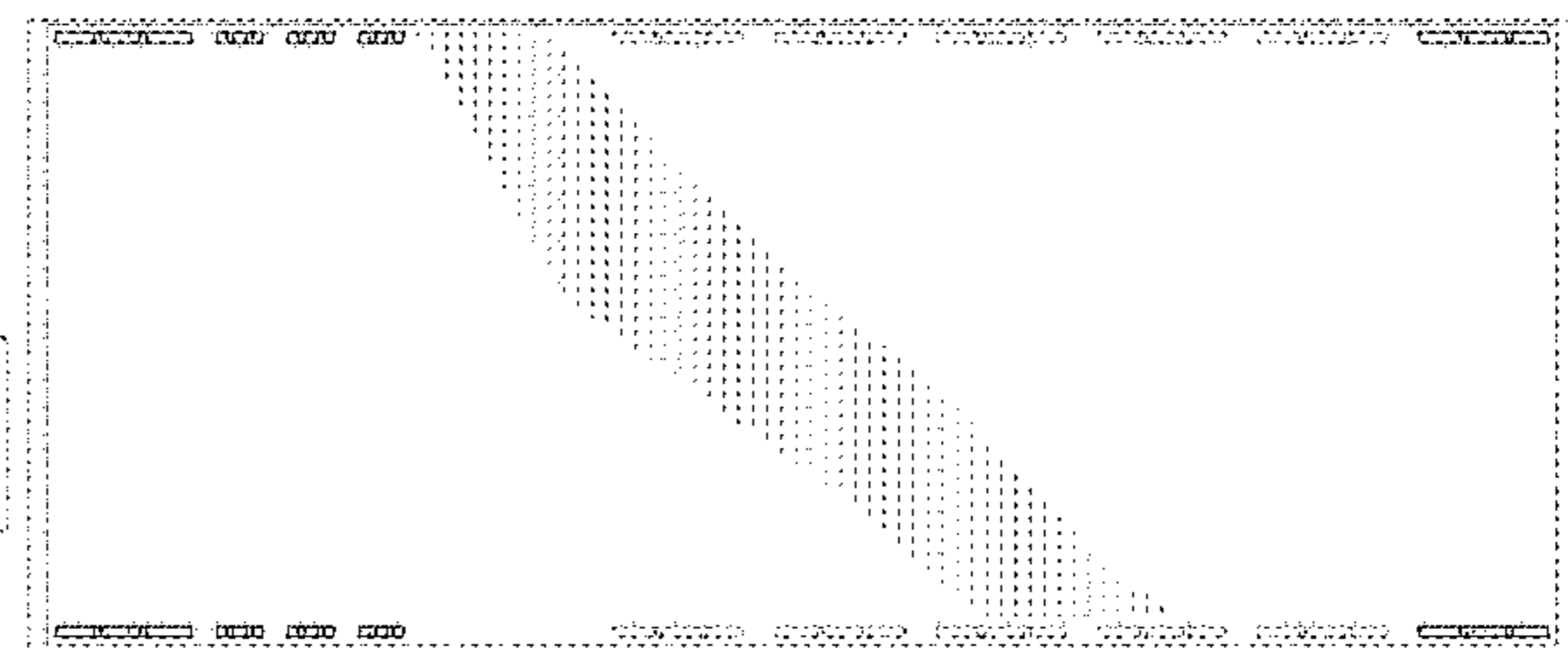
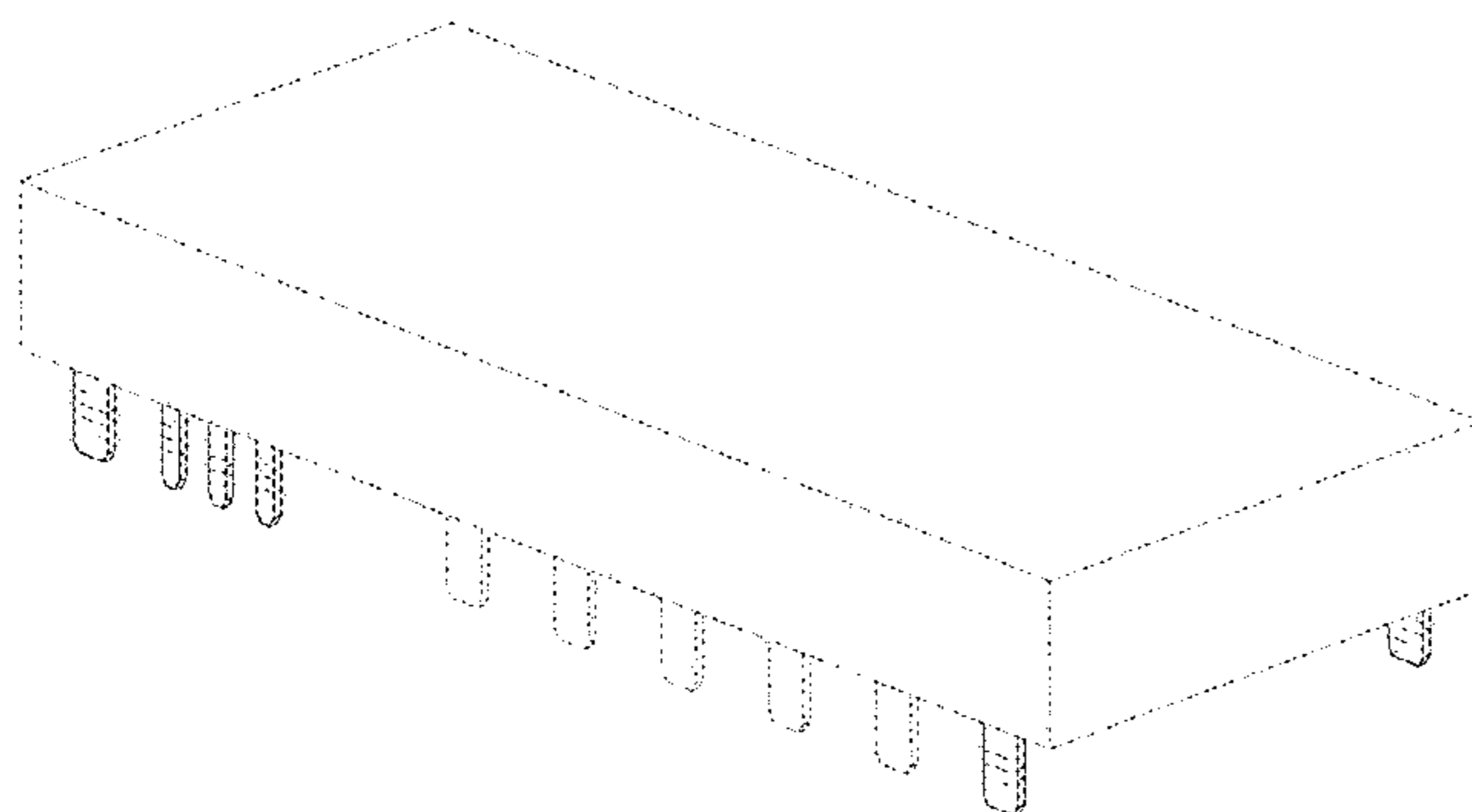
(57) **CLAIM**

The ornamental design for an electric terminal, as shown and described.

DESCRIPTION

FIG. 1 is a top, front, right perspective view of an electric terminal showing our new design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a right elevation view thereof;
FIG. 5 is a left elevation view thereof;
FIG. 6 is a top plan view thereof; and,
FIG. 7 is a bottom plan view thereof.
Broken lines formed by equal length dashes show unclaimed portions of the design. Broken lines formed of unequal length dashes (i.e., dash-dot) show boundaries between claimed and unclaimed portions of the design.

1 Claim, 5 Drawing Sheets



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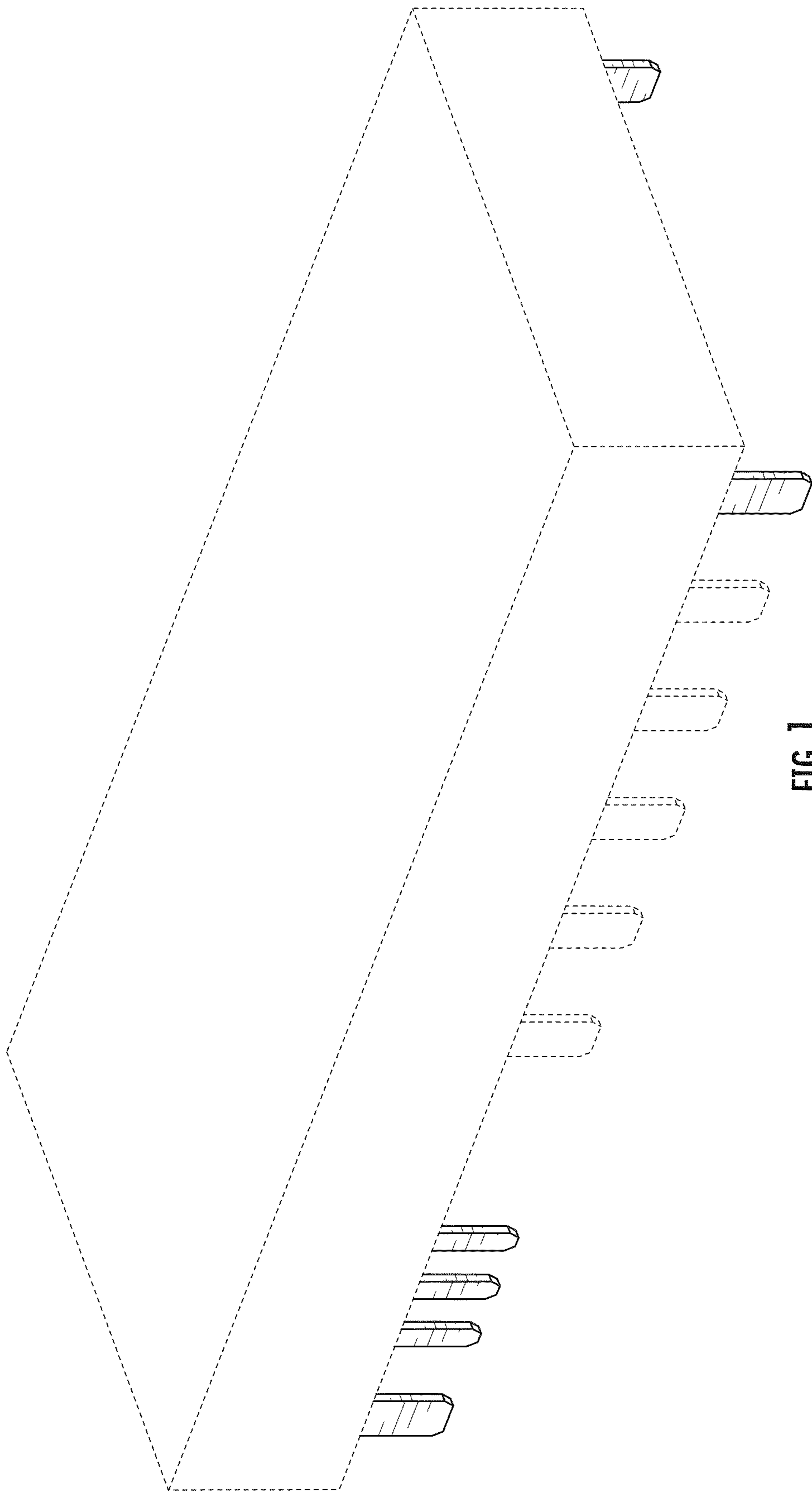


FIG. 1

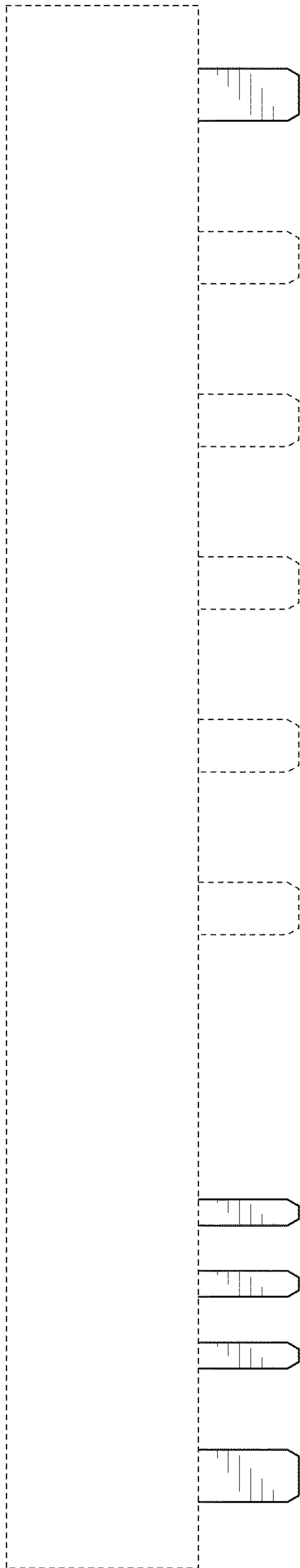


FIG. 2

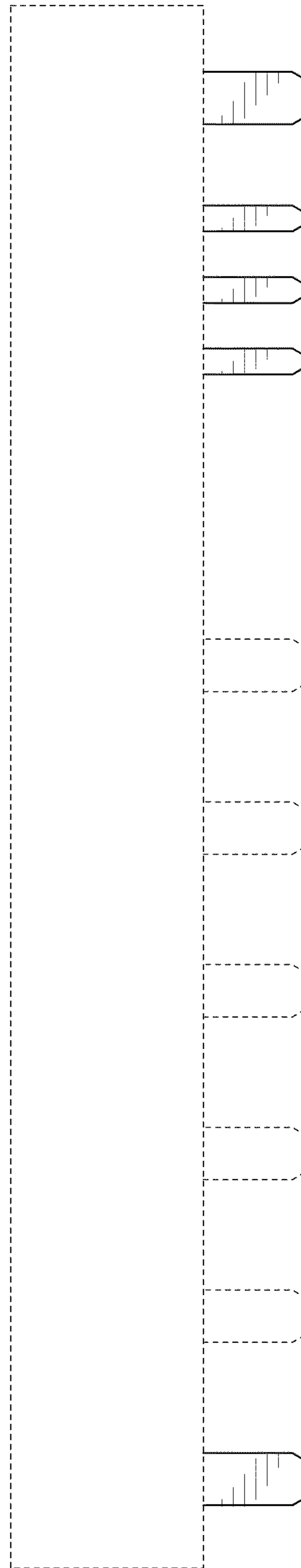


FIG. 3



FIG. 4

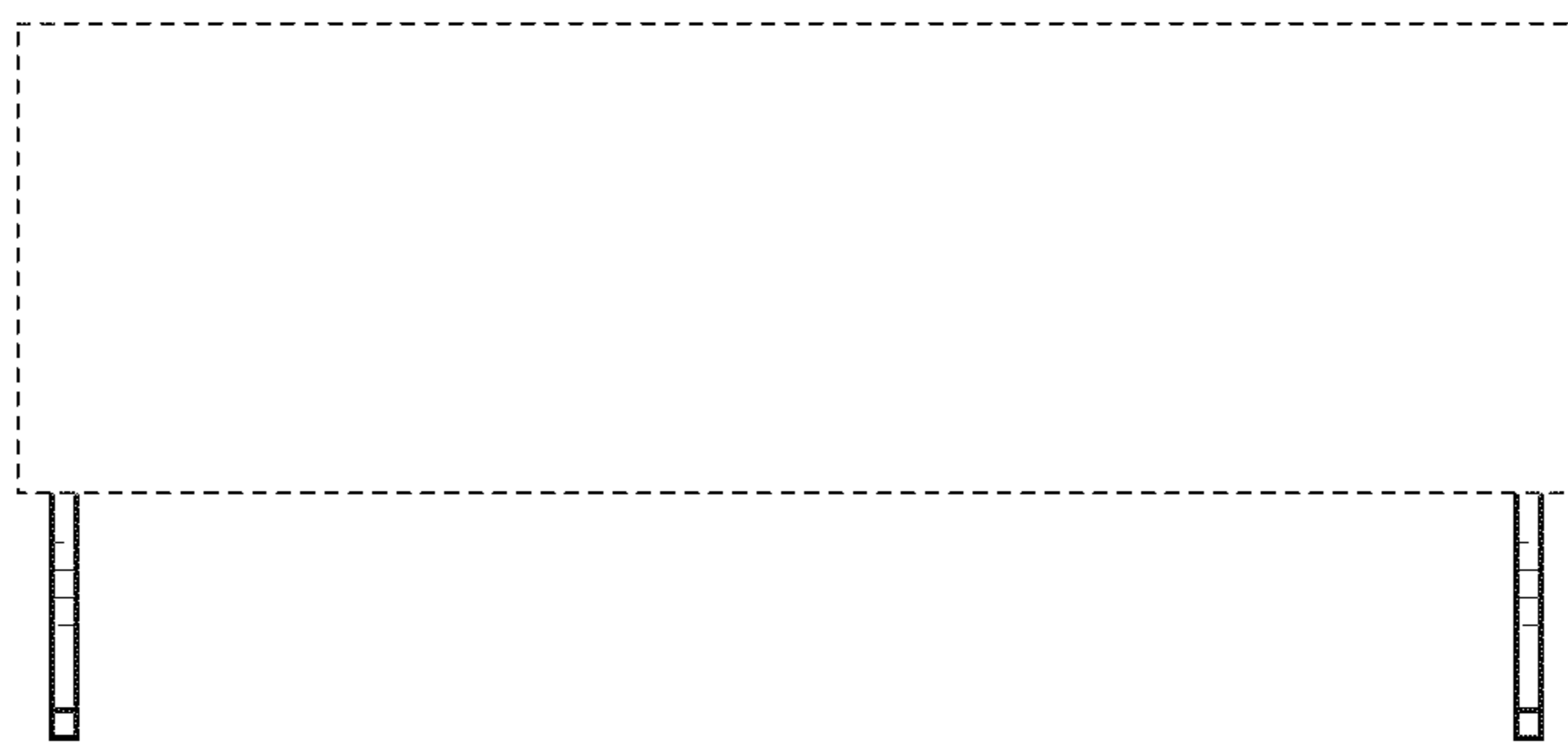


FIG. 5



FIG. 6

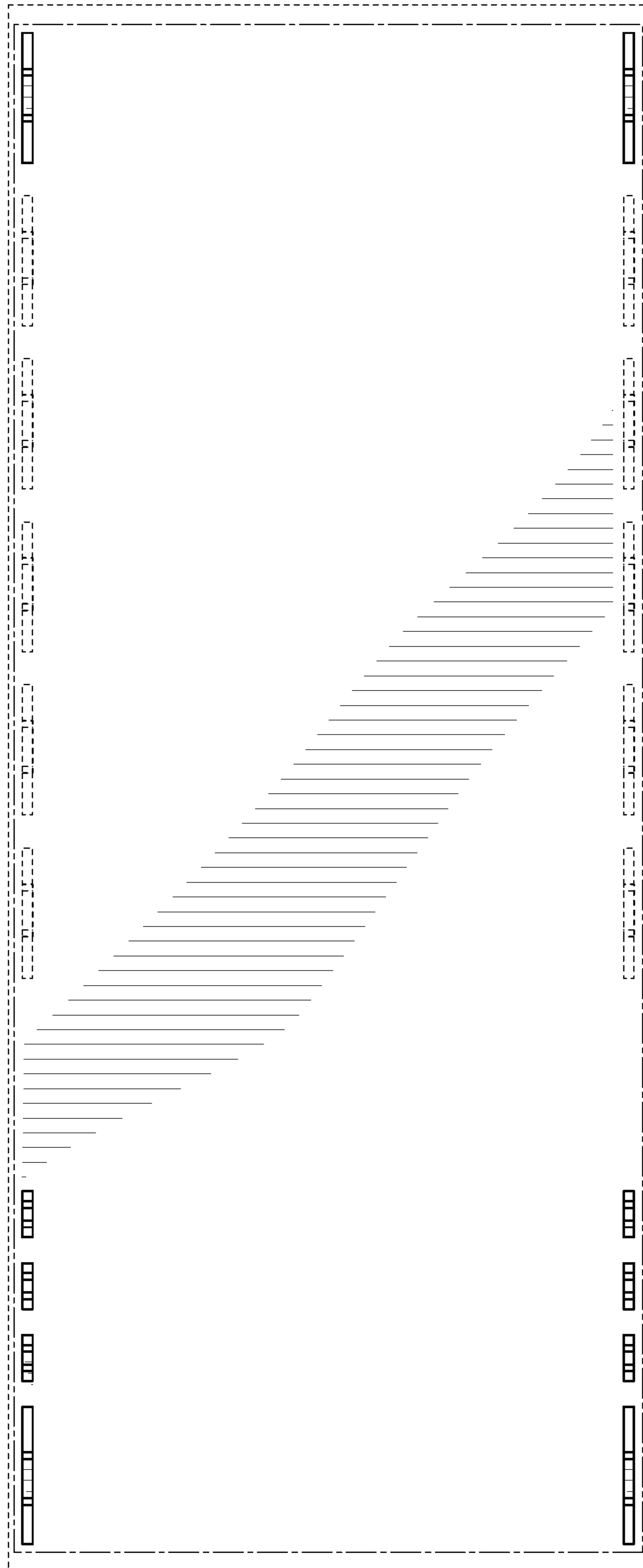


FIG. 7