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(12) **United States Design Patent** (10) **Patent No.:** **US D877,102 S**
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(54) **SEMICONDUCTOR MODULE**

1/182; H05K 1/0228; H05K 1/0245;
H05K 1/0236; H05K 1/0263

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See application file for complete search history.

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(52) **U.S. Cl.**
USPC **D13/182**

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2924/1905; H01L 2224/08054; H05B
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G02B 6/4257; G02B 6/4261; G02B
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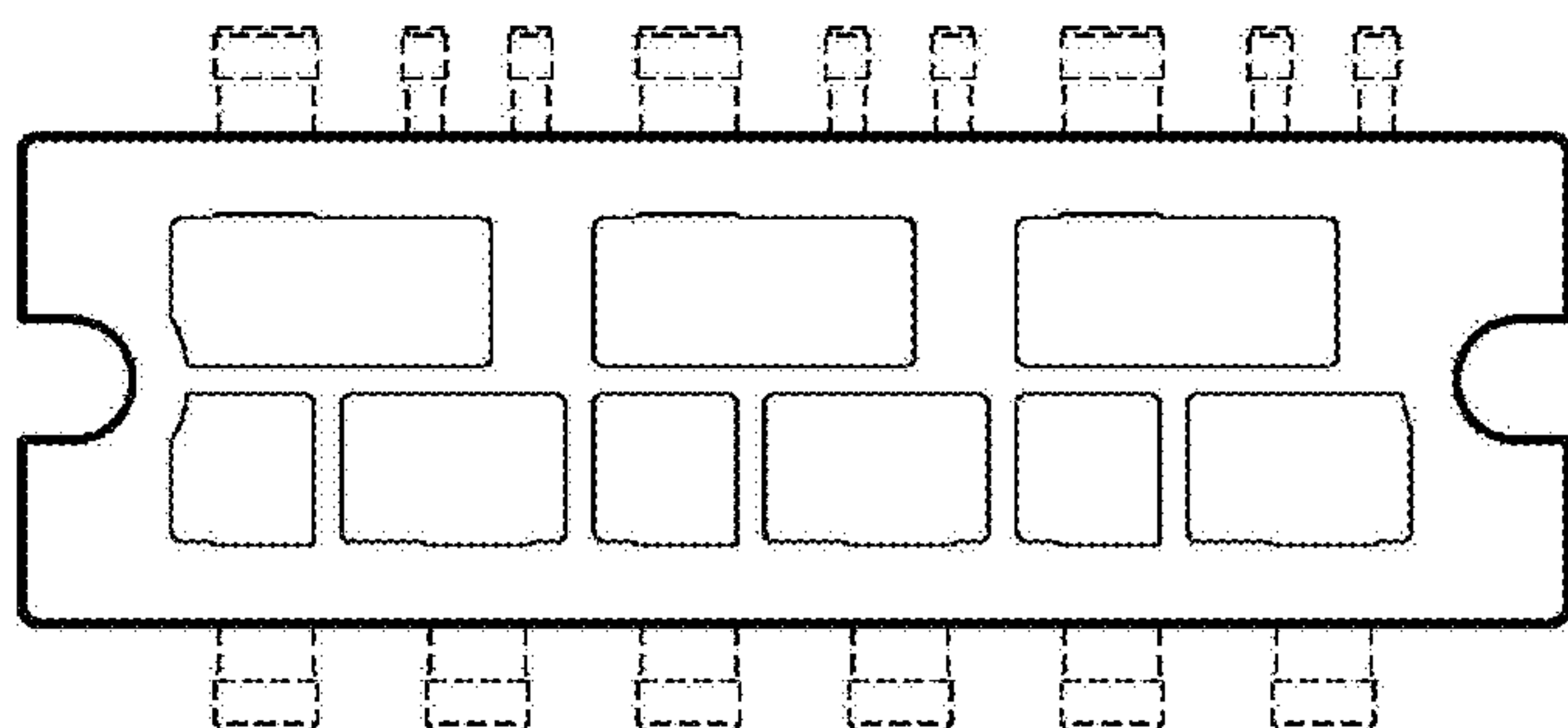
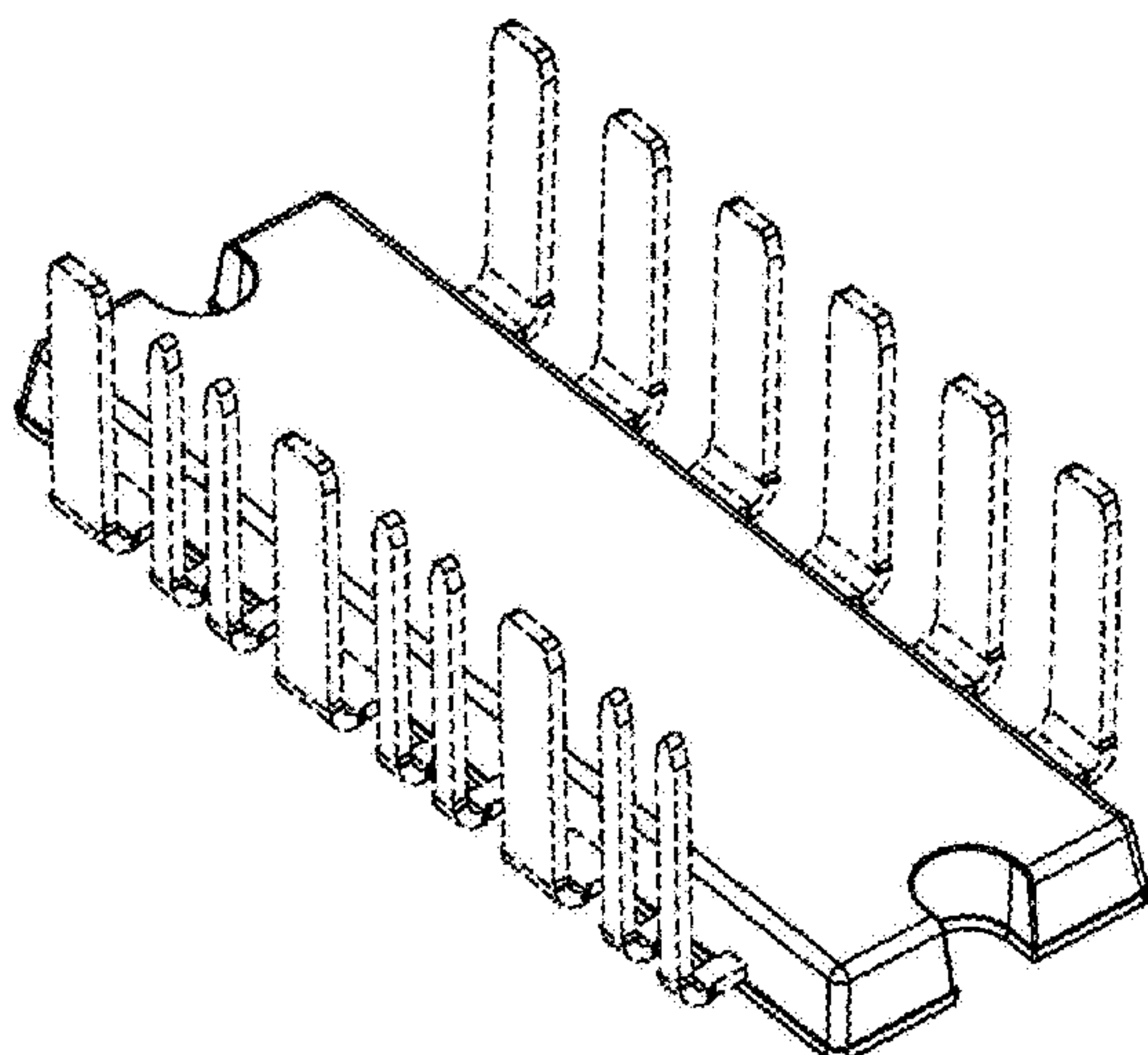
(57) **CLAIM**

The ornamental design for a semiconductor module, as
shown and described.

DESCRIPTION

FIG. 1 is a top perspective view of a semiconductor module,
showing my new design;
FIG. 2 is a front view thereof;
FIG. 3 is a rear view thereof;
FIG. 4 is a left side view thereof;
FIG. 5 is a right side view thereof;
FIG. 6 is a top plan view thereof; and,
FIG. 7 is a underside plan view thereof.
The broken lines in the Figures are for environmental
purposes only and form no part of the claimed design.

1 Claim, 4 Drawing Sheets



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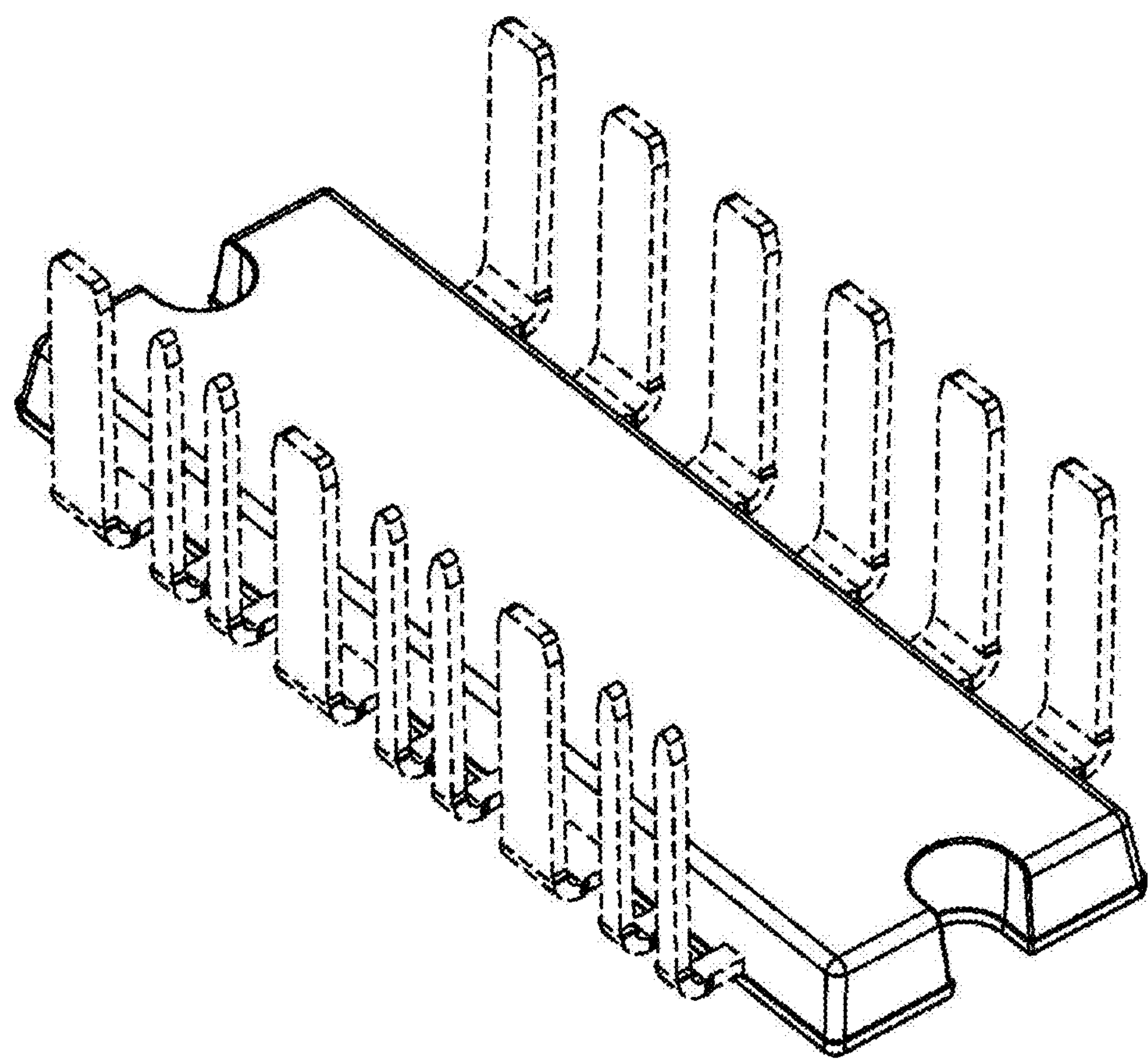


FIG. 1

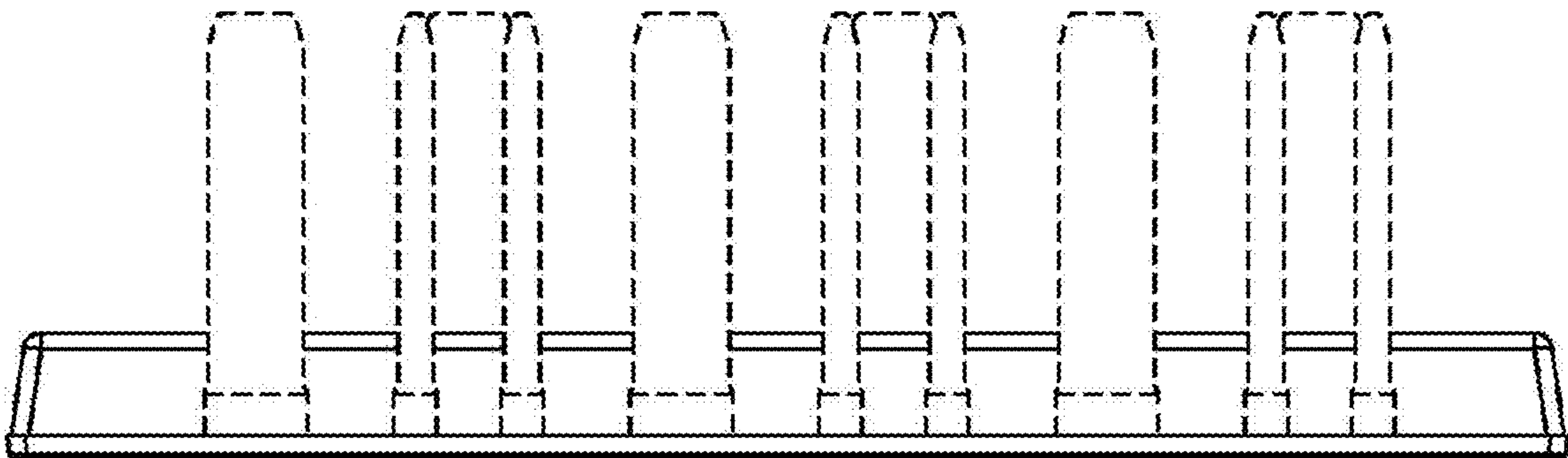


FIG. 2

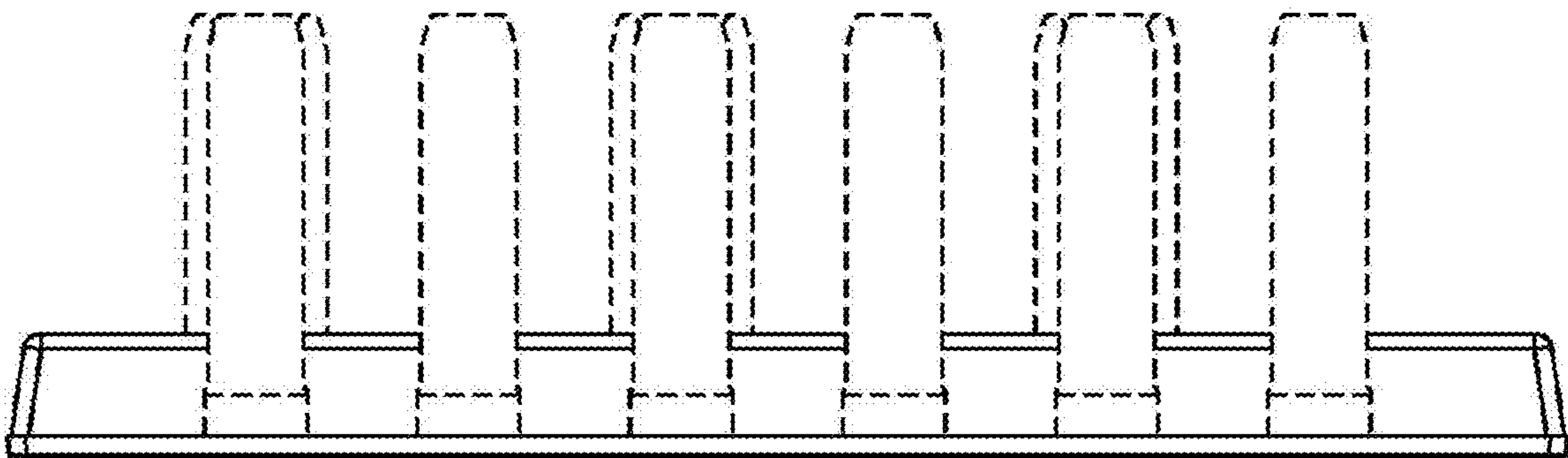


FIG. 3

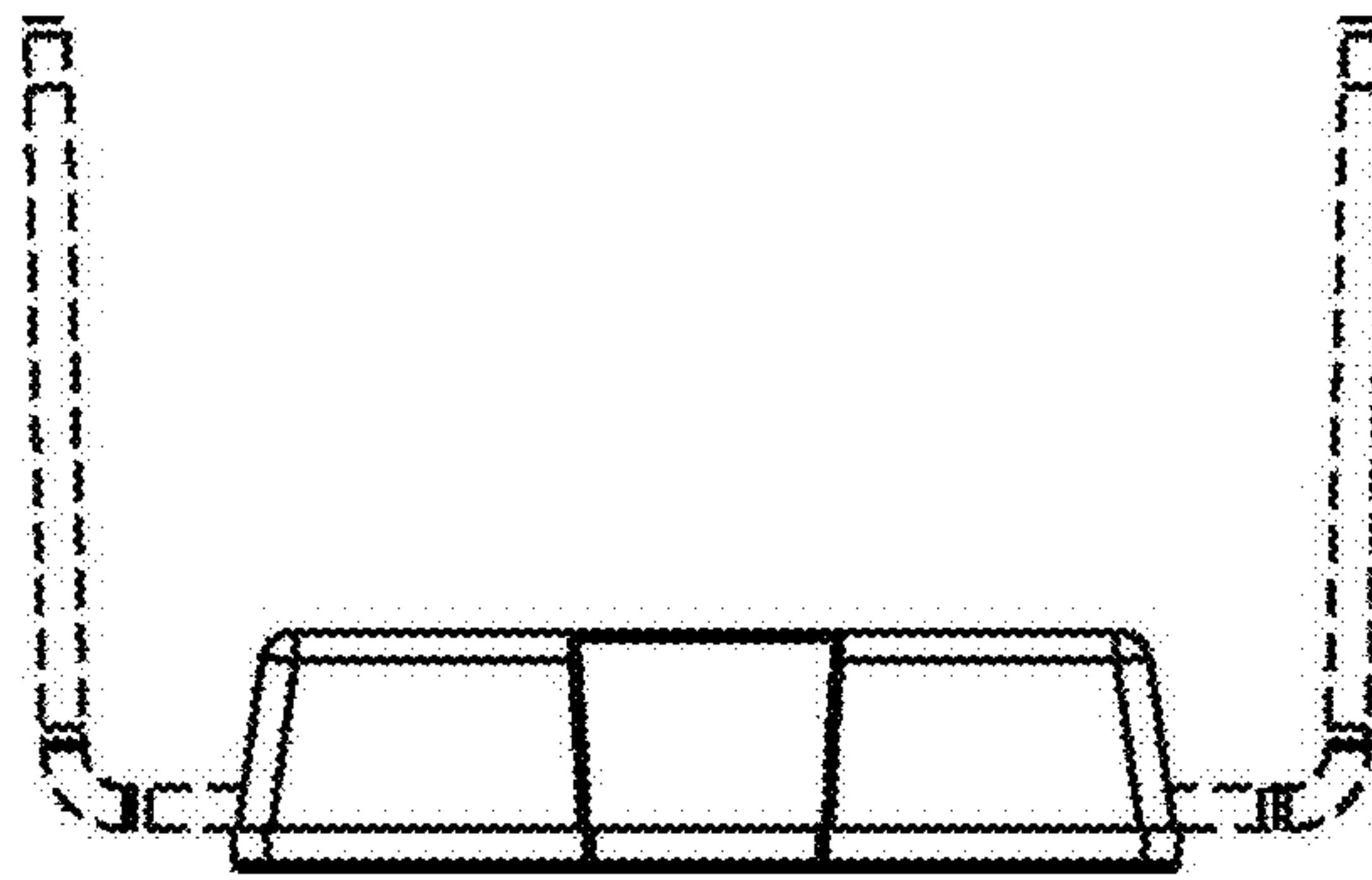


FIG. 4

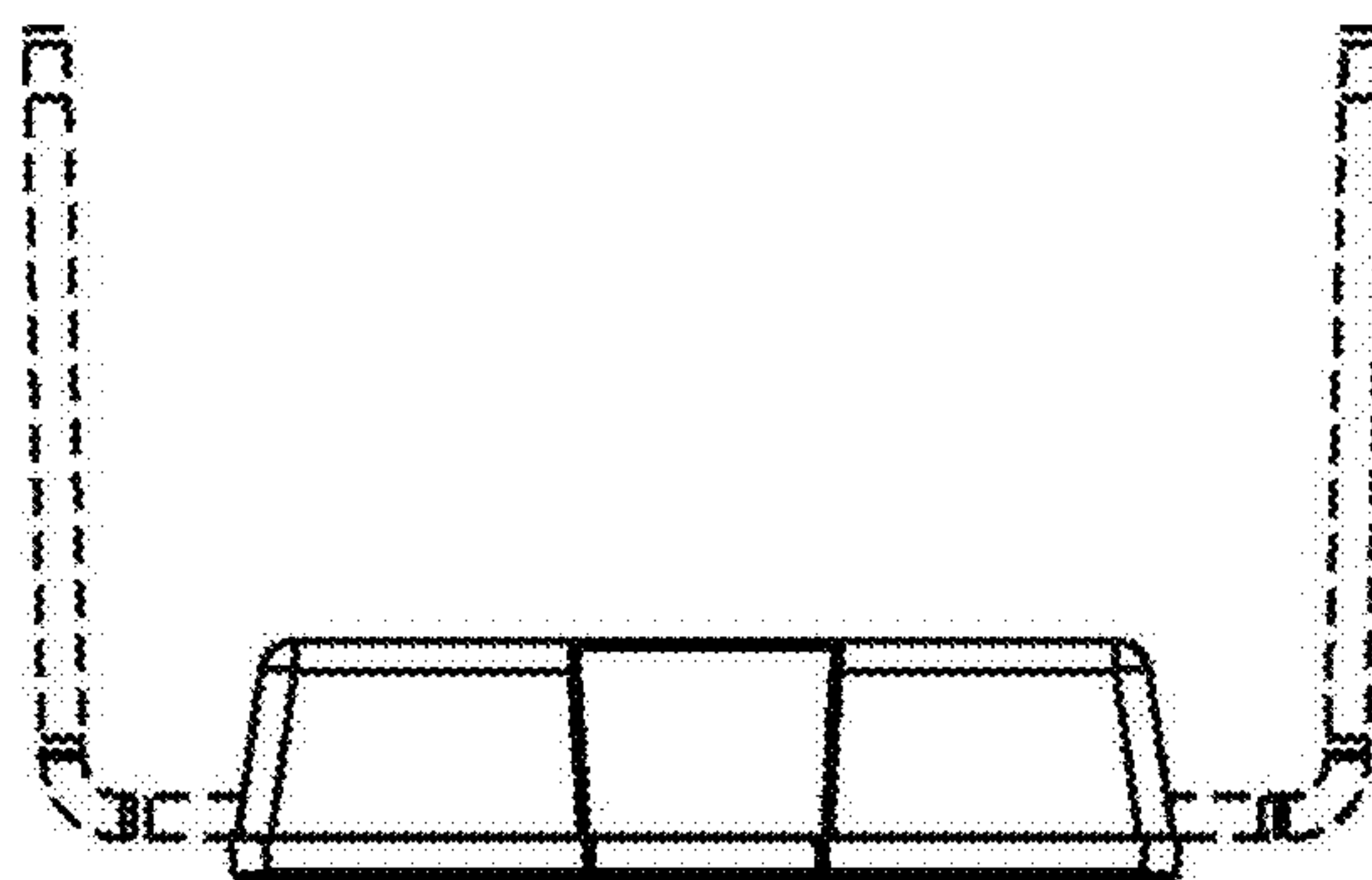


FIG. 5

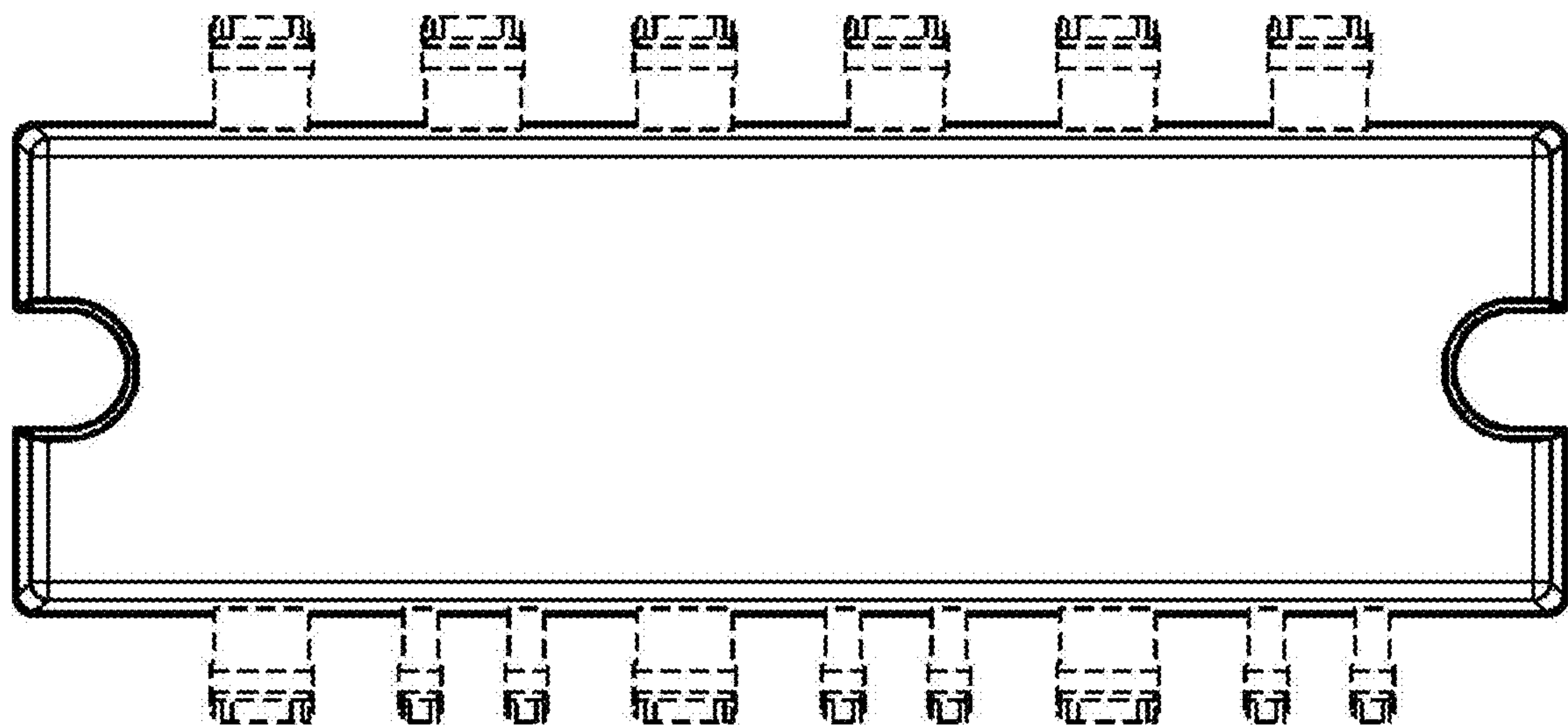


FIG. 6

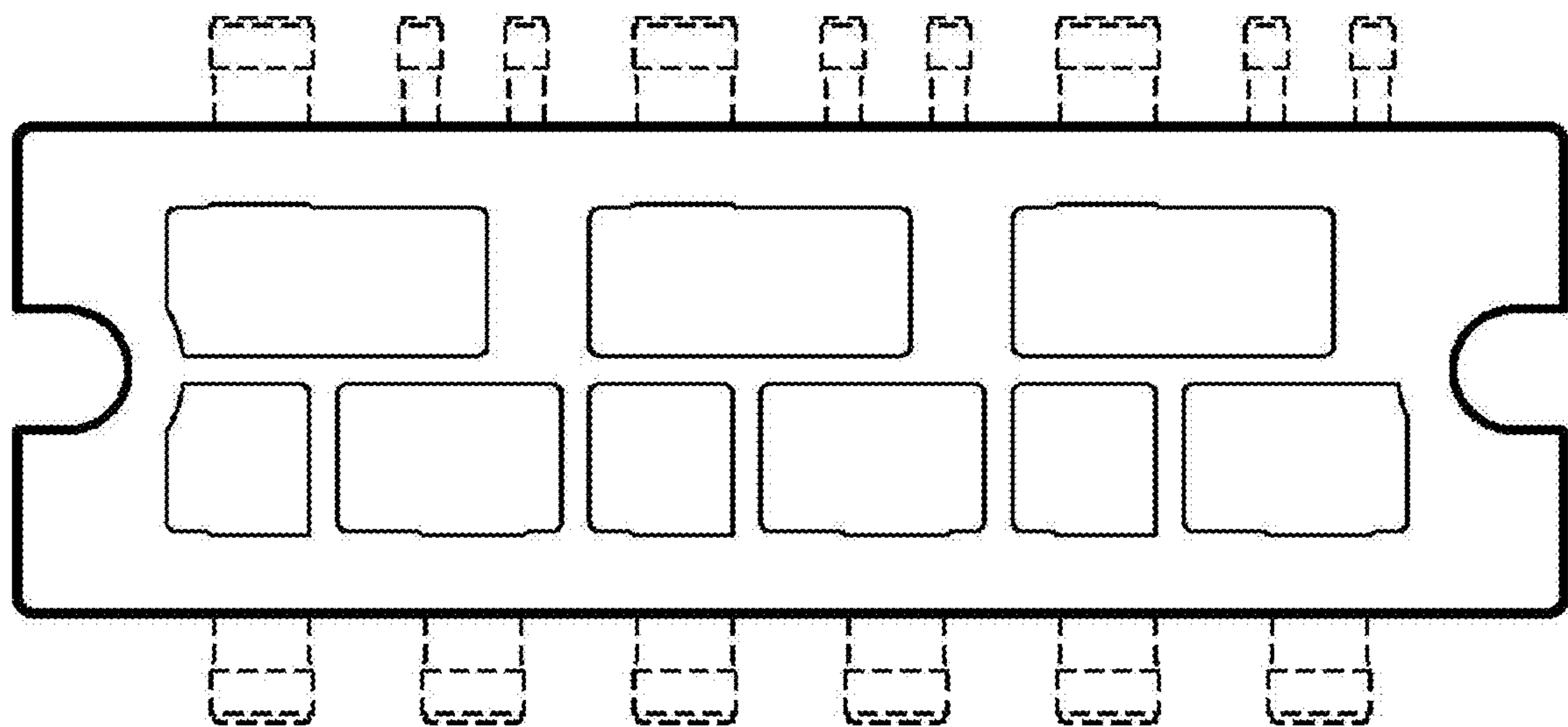


FIG. 7