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Holden et al.

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(54) **MULTIPROBE CIRCUIT TESTER DISPLAY WITH GRAPHICAL USER INTERFACE**

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(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **15 Years**

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(51) **LOC (12) Cl.** **14-04**

(52) **U.S. Cl.**
USPC **D14/486**

(58) **Field of Classification Search**
USPC D14/485–495
CPC G06F 3/048; G06F 3/0481; G06F 3/04817; G06F 3/0482; G06F 3/0483; G06F 3/04842; G06F 3/0485; G06F 3/04855; G06F 3/0486; G06F 3/0488; G06F 3/04886; G06F 9/4443; G06F 17/211; G06F 17/212
See application file for complete search history.

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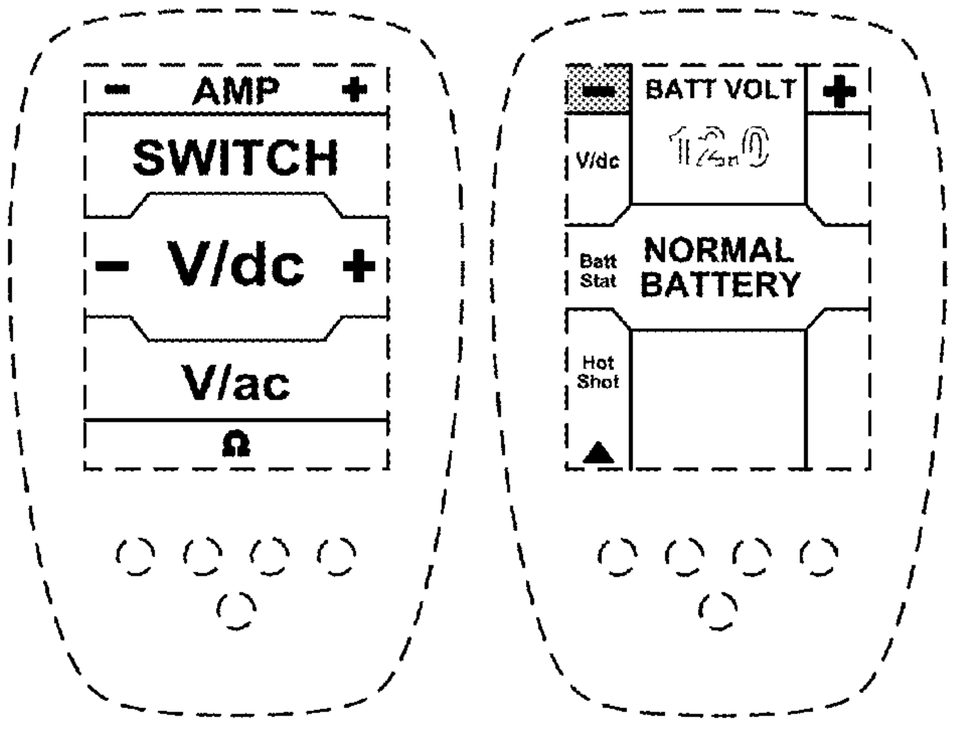

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Primary Examiner — Jack Reickel
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(57) **CLAIM**

The ornamental design for a multiprobe circuit tester display with graphical user interface, as shown and described.

DESCRIPTION

FIG. **1** is a front view of a multiprobe circuit tester display with an animated graphical user interface illustrating a first image in each of first and second sequences;
FIG. **2** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **1** illustrating a second image in each of the first and second sequences;
FIG. **3** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **1** illustrating a third image in each of the first and second sequences;
FIG. **4** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **1** illustrating a fourth image in each of the first and second sequences;
FIG. **5** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **1** illustrating a fifth image in the first sequence;
FIG. **6** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **1** illustrating a fifth image in the second sequence;
FIG. **7** is a front view of a multiprobe circuit tester display with an animated graphical user interface illustrating a first image in each of third and fourth sequences;
FIG. **8** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **7** illustrating a second image in each of the third and fourth sequences;
FIG. **9** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **7** illustrating a third image in each of the third and four sequences;
FIG. **10** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **7** illustrating a fourth image in each of the third and fourth sequences;
FIG. **11** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **7** illustrating a fifth image in each of the third and fourth sequences;
FIG. **12** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **7** illustrating a sixth image in the third sequence;
FIG. **13** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **7** illustrating a sixth image in the fourth sequence;
FIG. **14** is a front view of a multiprobe circuit tester display with an animated graphical user interface illustrating a first image in each of fifth and sixth sequences;
FIG. **15** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **14** illustrating a second image in each of the fifth and sixth sequences;
FIG. **16** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **14** illustrating a third image in each of the fifth and sixth sequences;
FIG. **17** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **14** illustrating a fourth image in each of the fifth and sixth sequences;
FIG. **18** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **14** illustrating a fifth image in each of the fifth and sixth sequences;
FIG. **19** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **14** illustrating a sixth image in the fifth sequence; and,
FIG. **20** is a front view of the multiprobe circuit tester display with the animated graphical user interface of FIG. **14** illustrating a sixth image in the sixth sequence.
The broken line showing of a multiprobe circuit tester display with graphical user interface is included for the purpose of illustrating portions of the article and form no part of the claimed design.
The appearance of the animated images sequentially transitions between the images shown in FIGS. **1-5**. The process or period in which one transitions to another forms no part of the claimed design.
The appearance of the animated images sequentially transitions between the images shown in FIGS. **1-4** and **6**. The process or period in which one transitions to another forms no part of the claimed design.
The appearance of the animated images sequentially transitions between the images shown in FIGS. **7-12**. The process or period in which one transitions to another forms no part of the claimed design.
The appearance of the animated images sequentially transitions between the images shown in FIGS. **7-11** and **13**. The process or period in which one transitions to another forms no part of the claimed design.
The appearance of the animated images sequentially transitions between the images shown in FIGS. **14-19**. The process or period in which one transitions to another forms no part of the claimed design.
The appearance of the animated images sequentially transitions between the images shown in FIGS. **14-18** and **20**. The process or period in which one transitions to another forms no part of the claimed design.

1 Claim, 7 Drawing Sheets

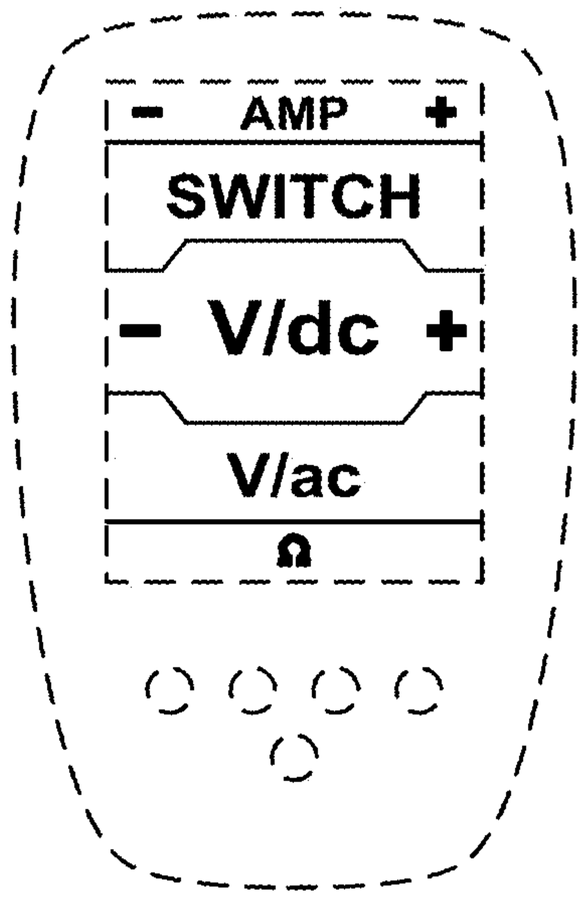

FIG. 1

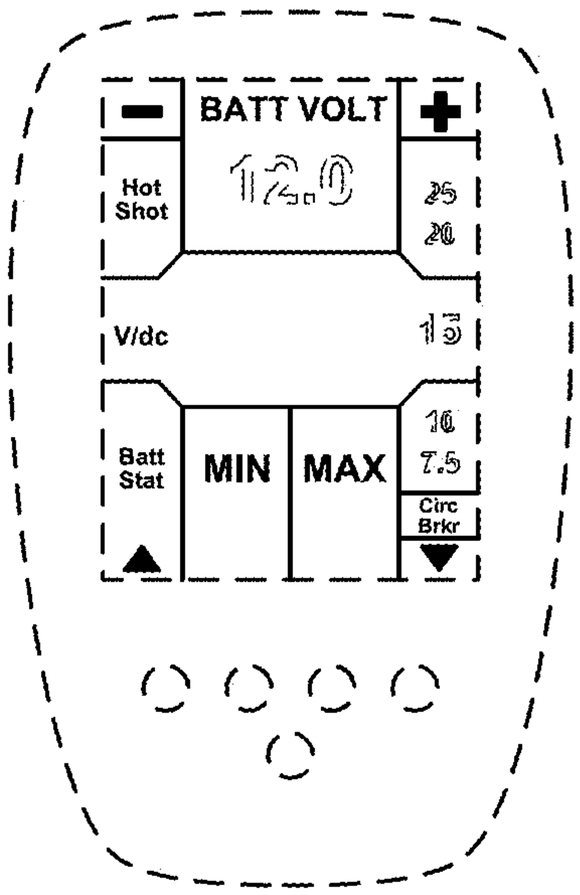

FIG. 2

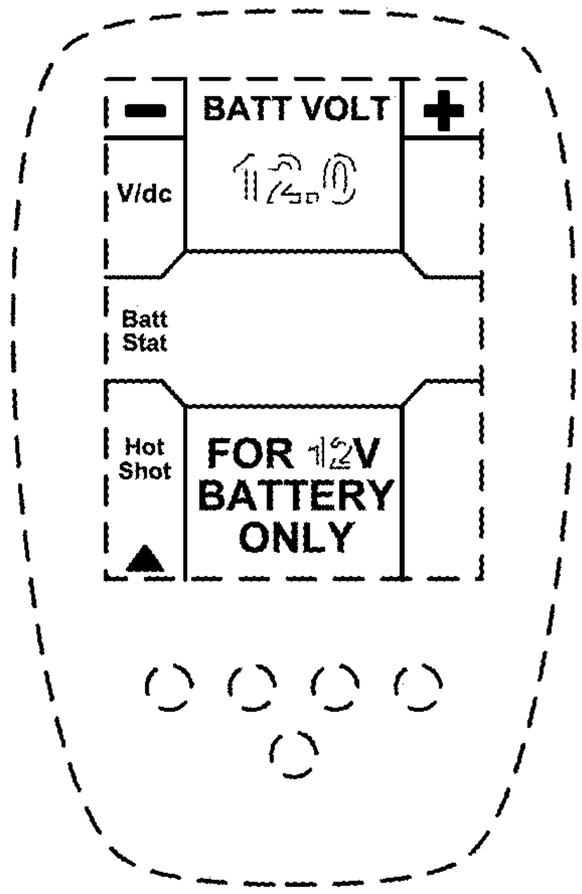

FIG. 3

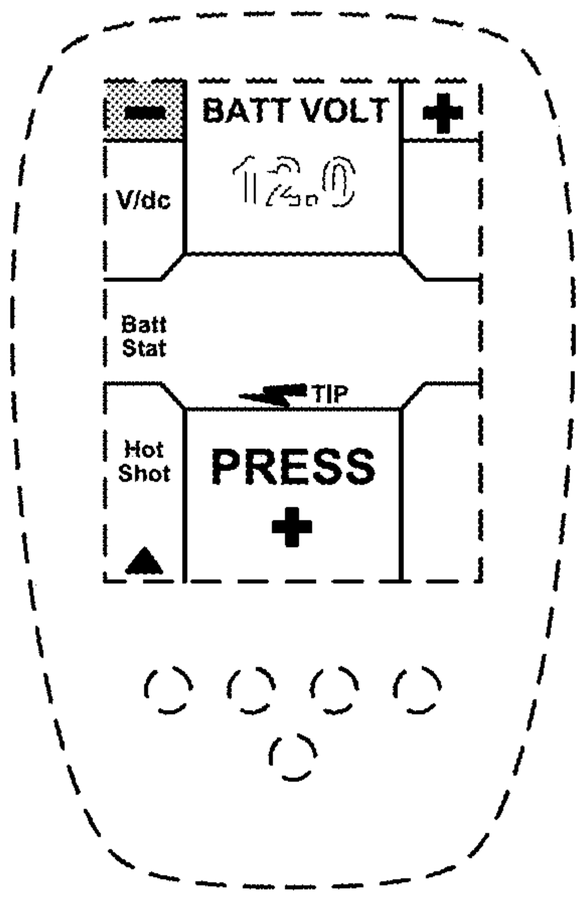

FIG. 4

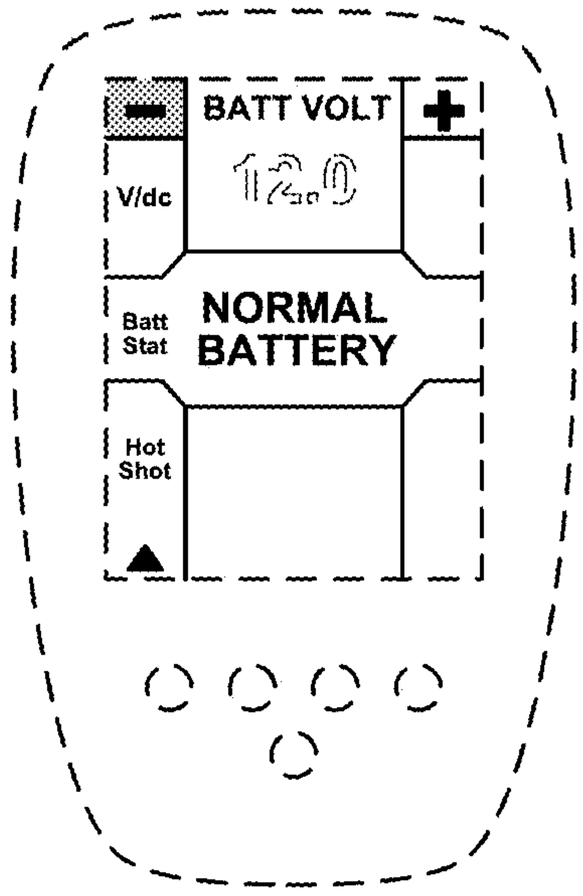

FIG. 5

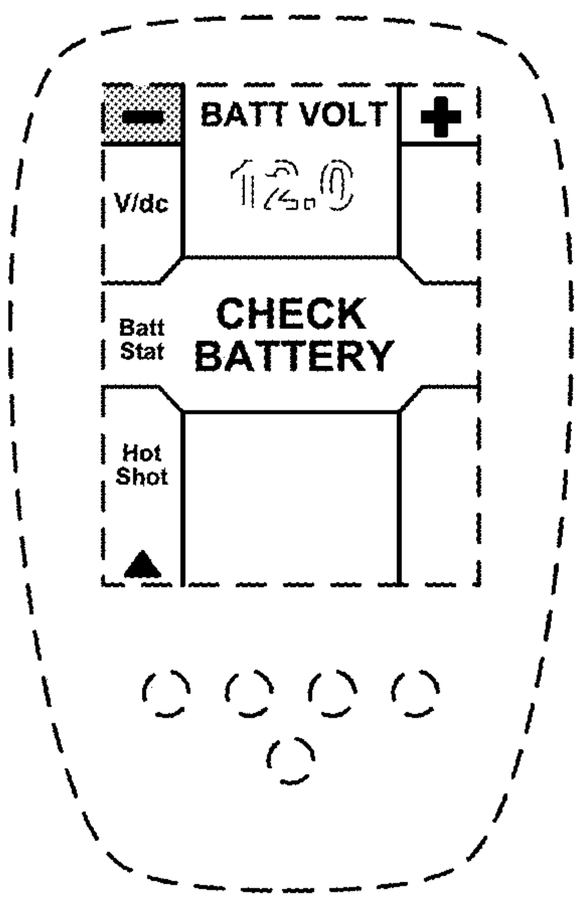

FIG. 6

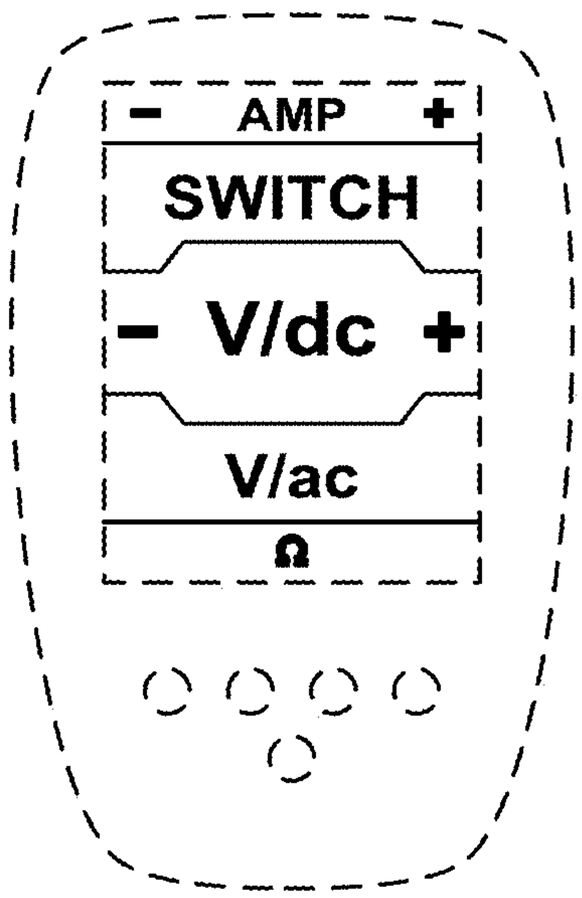

FIG. 7

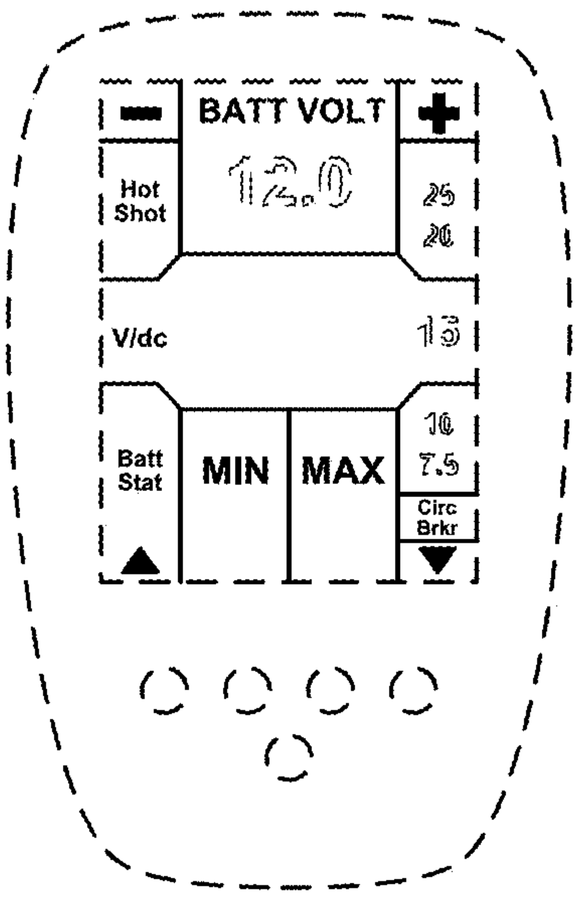

FIG. 8

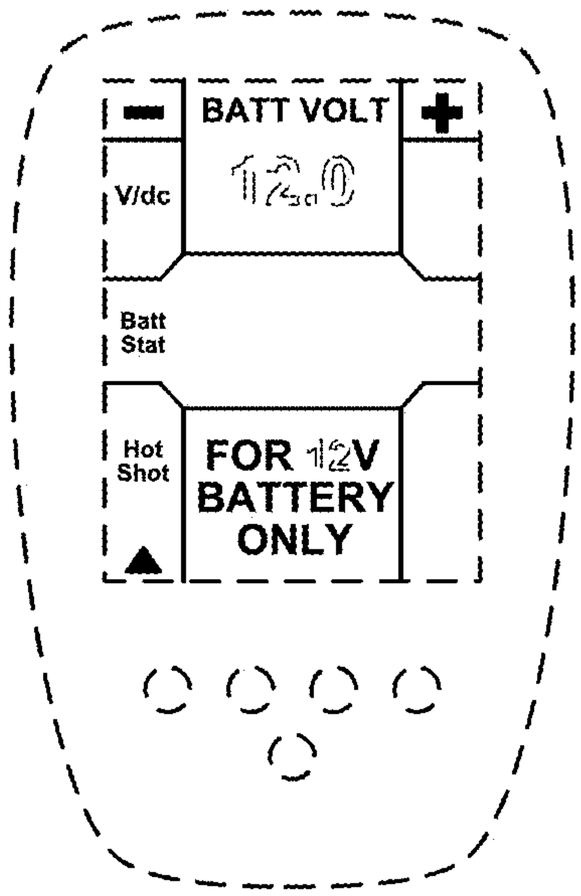

FIG. 9

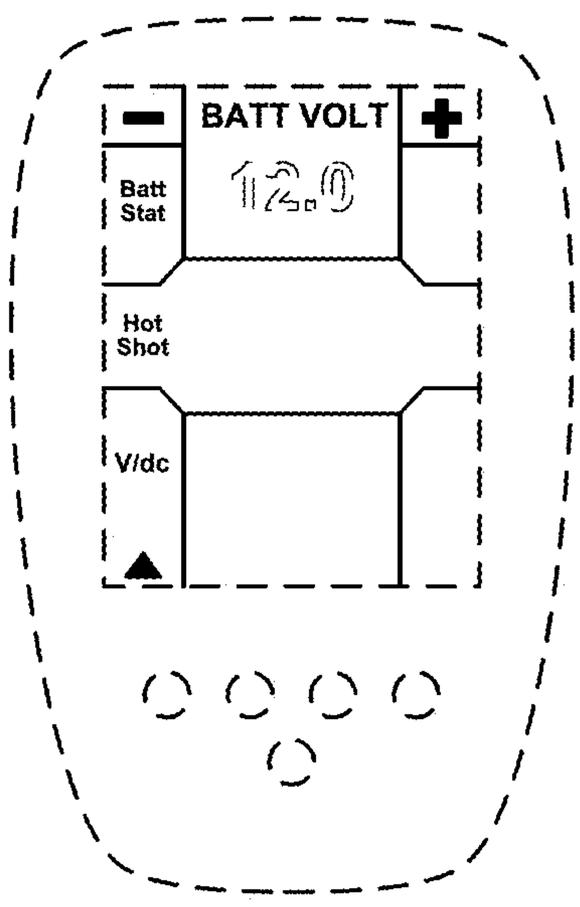

FIG. 10

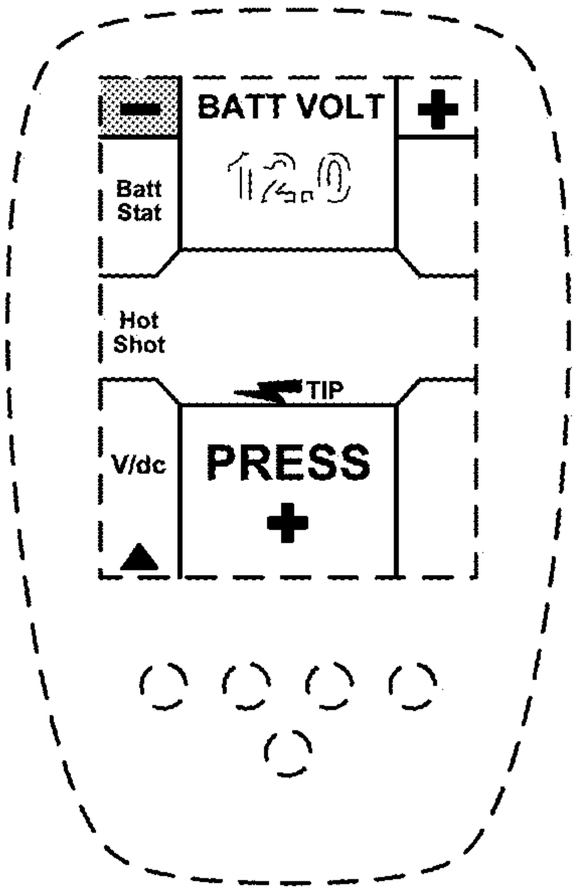

FIG. 11

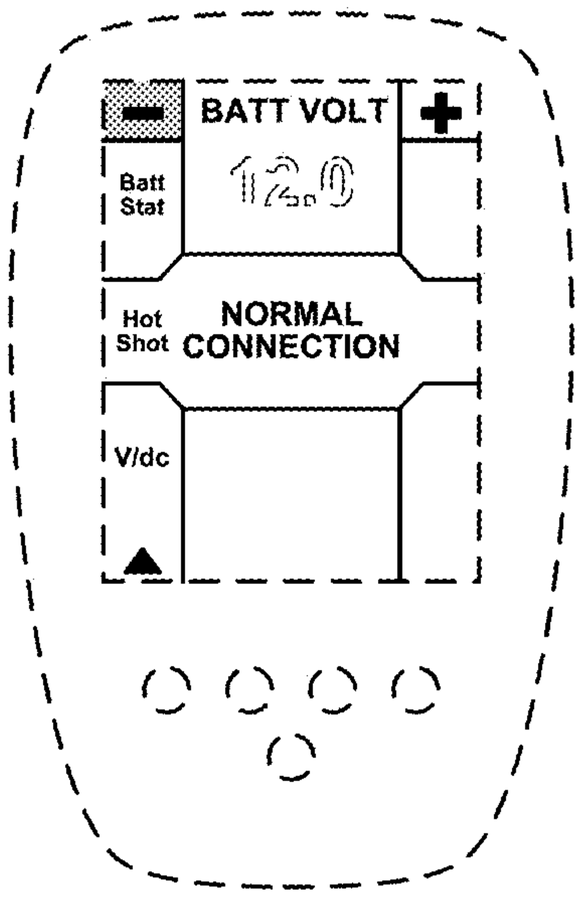

FIG. 12

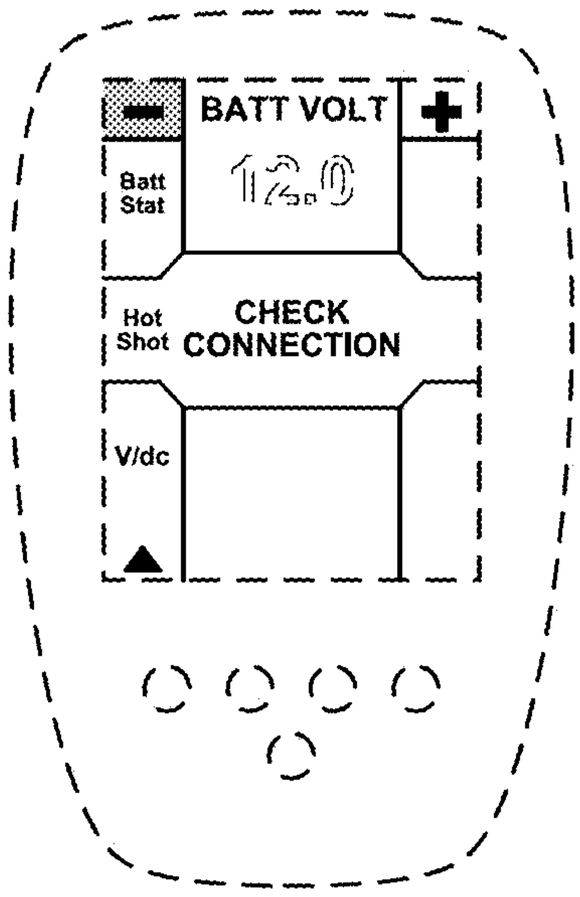

FIG. 13

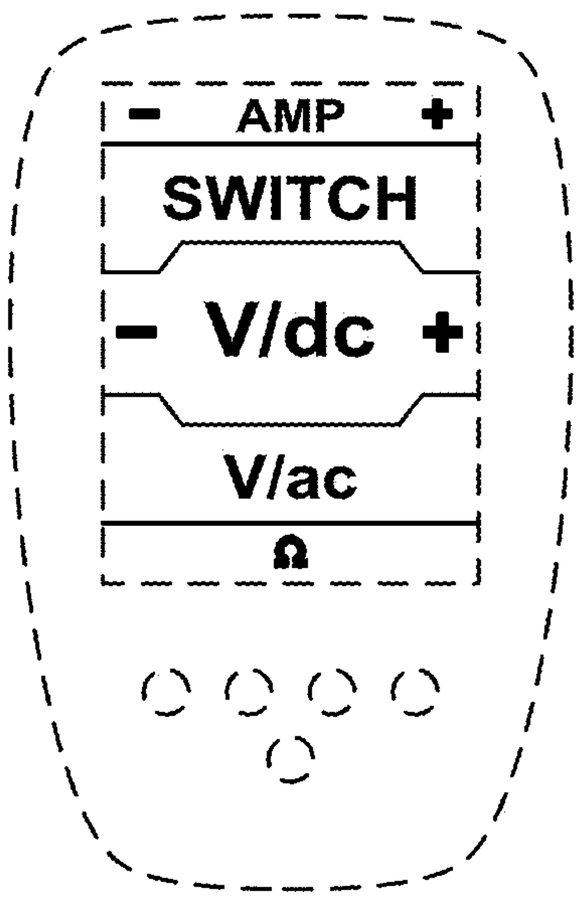

FIG. 14

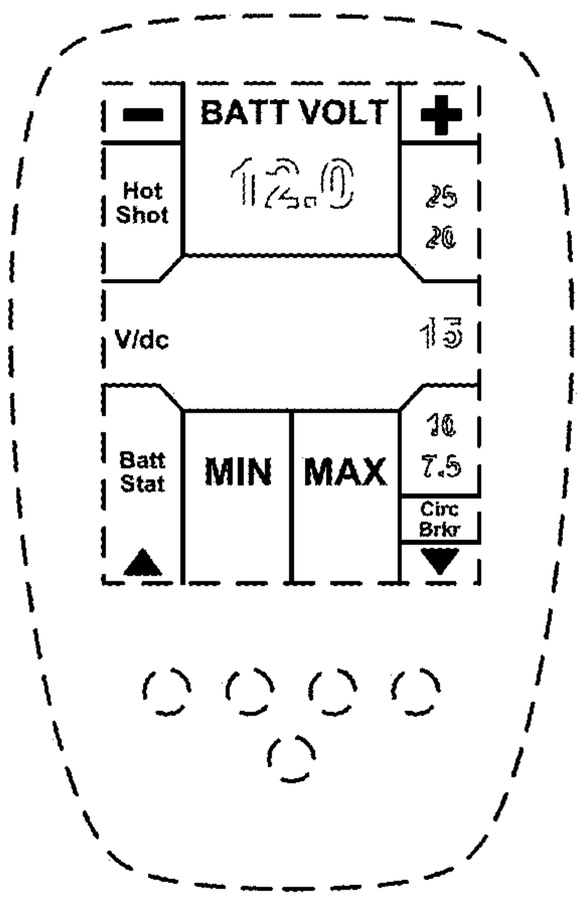

FIG. 15

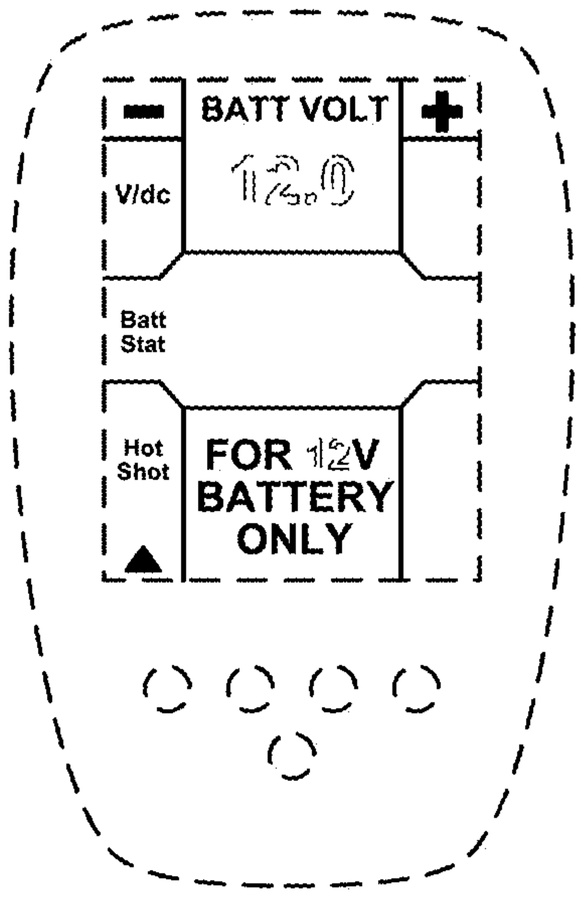

FIG. 16

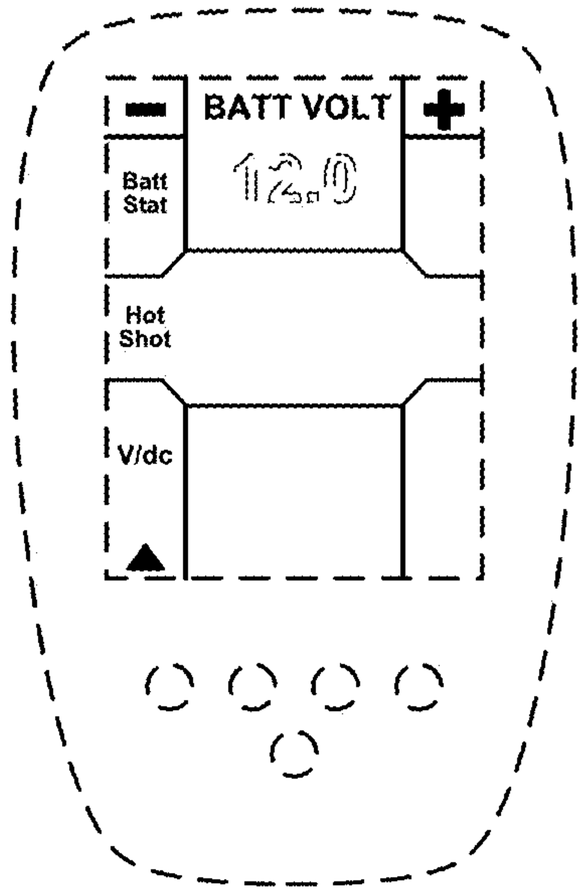

FIG. 17

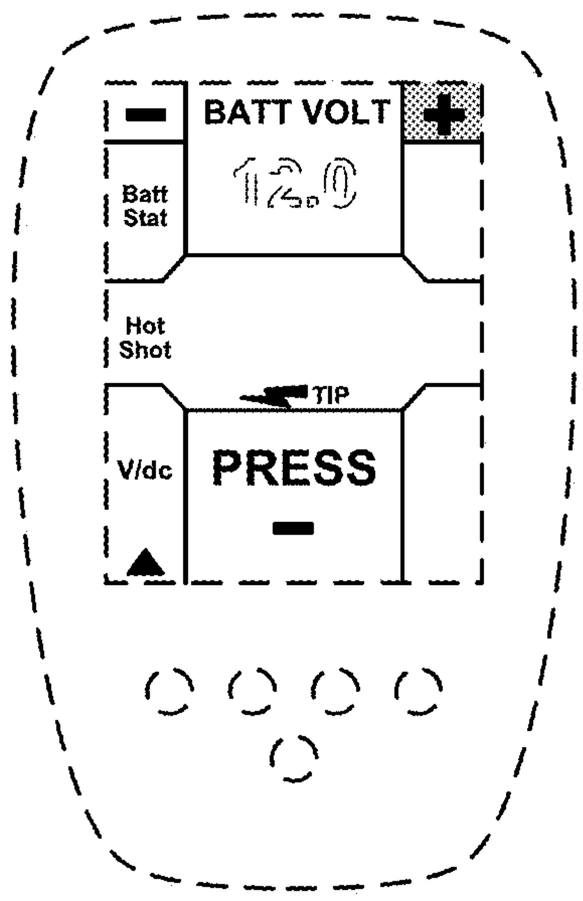

FIG. 18

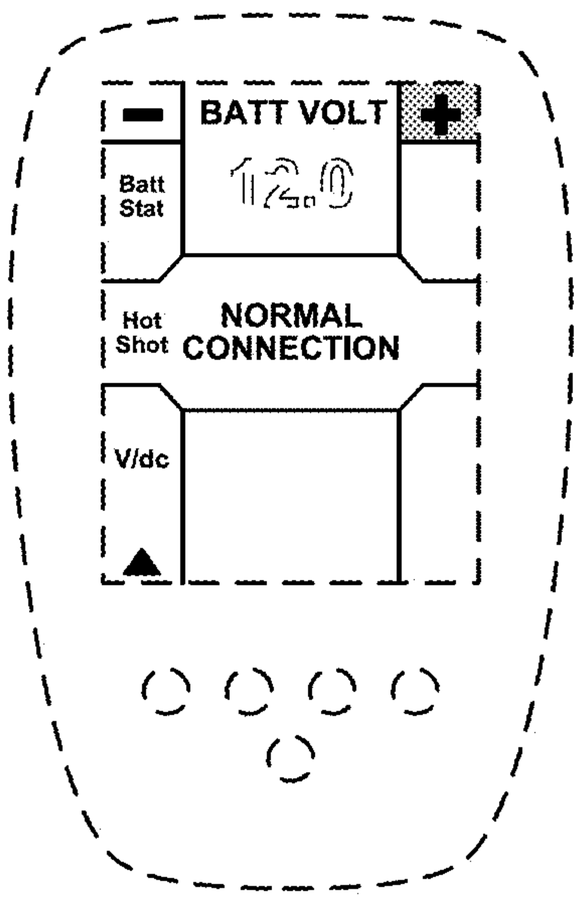

FIG. 19

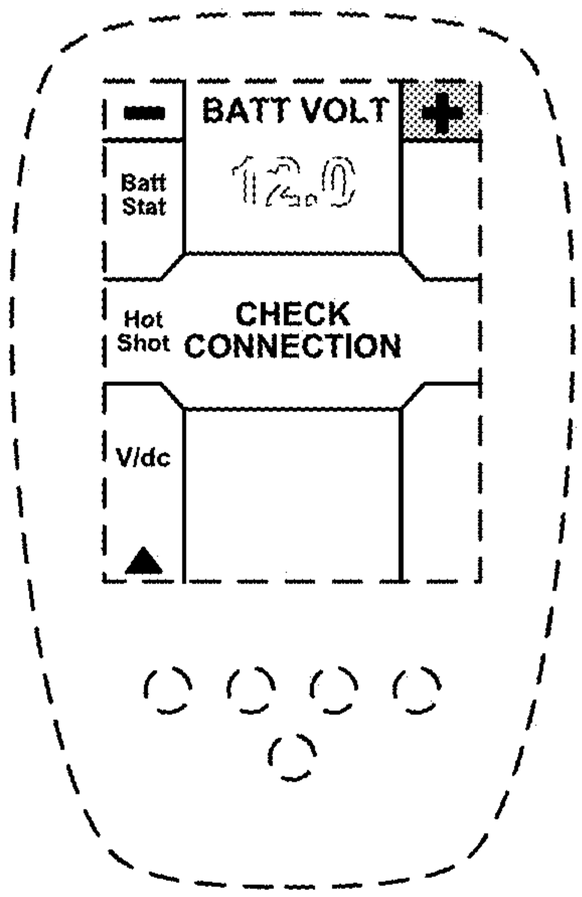

FIG. 20