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(12) **United States Design Patent** (10) **Patent No.:** **US D875,443 S**  
**Corbo et al.** (45) **Date of Patent:** **\*\* Feb. 18, 2020**

(54) **PANEL FOR AN ELECTRONICS RACK**

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(\*\*) Term: **15 Years**

(21) Appl. No.: **29/607,599**

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(52) **U.S. Cl.**  
USPC ..... **D6/675**

(58) **Field of Classification Search**  
USPC ..... D6/627, 672, 673-674, 675-675.1, 682,  
D6/682.1, 682.2; D25/113, 114, 138,  
D25/151, 152, 153  
CPC .. A47F 2005/0075; A47F 5/0807; A47F 7/00;  
B44F 9/00; B44F 9/04  
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a panel for an electronics rack, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of a panel for an electronics rack showing my new design;  
FIG. 2 is a front view thereof;  
FIG. 3 is a rear view thereof;  
FIG. 4 is a top view thereof;  
FIG. 5 is a bottom view thereof;  
FIG. 6 is a left side view thereof;  
FIG. 7 is a right side view thereof;  
FIG. 8 is a cross-sectional view thereof, taken along lines 8-8 in FIG. 2; and,  
FIG. 9 is an enlarged segment of the perspective view thereof.  
The dash-dot break lines that represent the bounds of the claim. The portions of the drawings appearing in broken lines are for environment only and do not form a part of the claimed design.

**1 Claim, 6 Drawing Sheets**

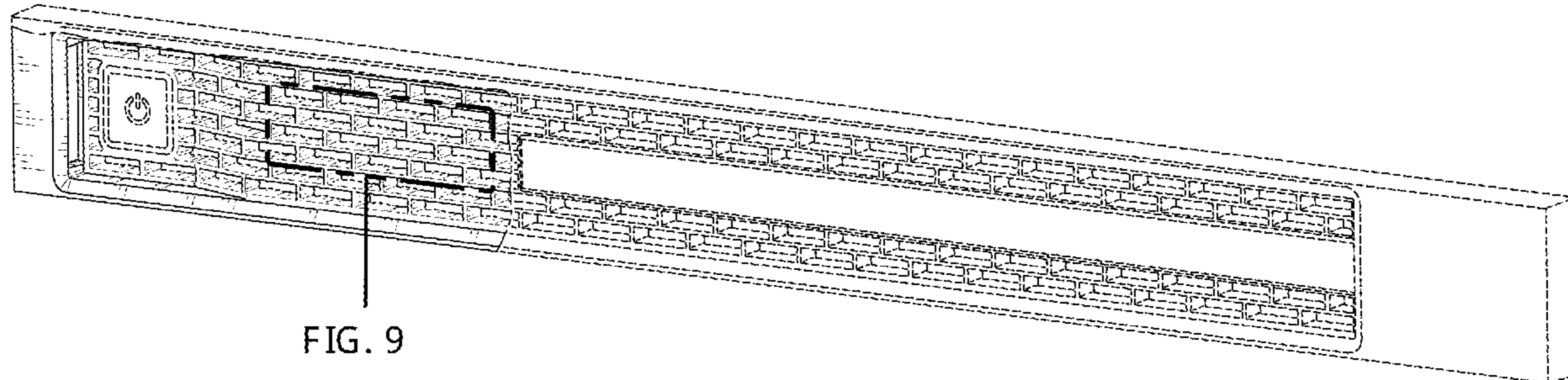


FIG. 9

(56)

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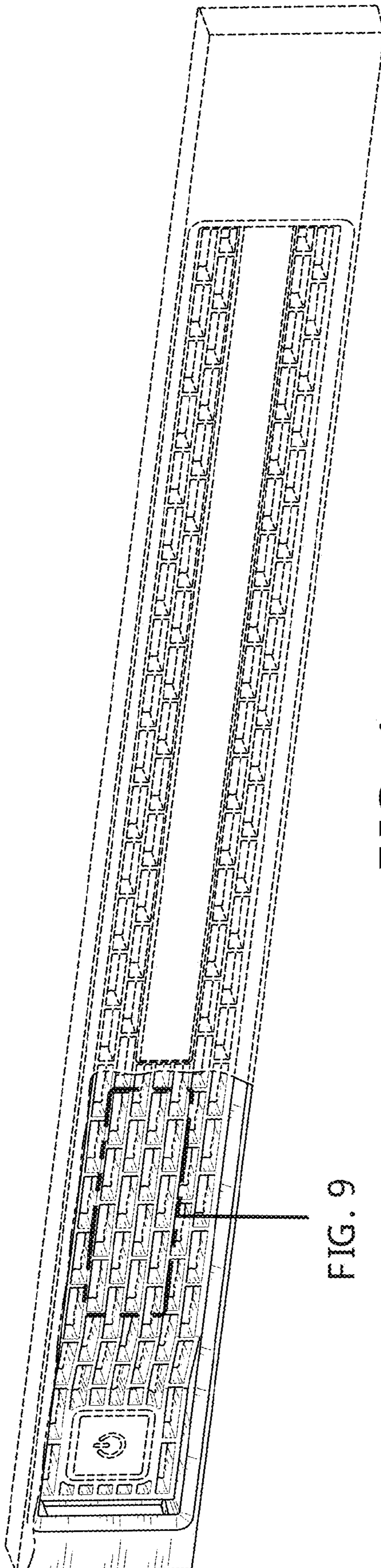
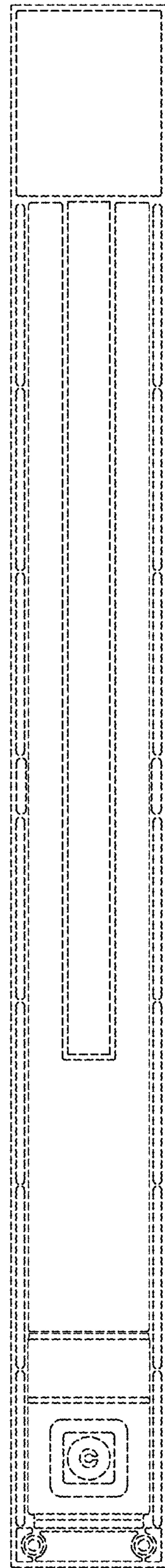


FIG. 9

FIG. 1



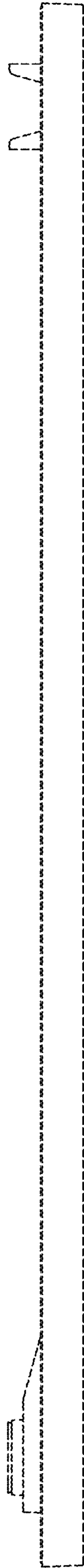


FIG. 4

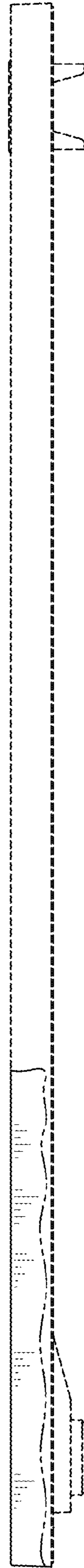


FIG. 5



FIG. 7

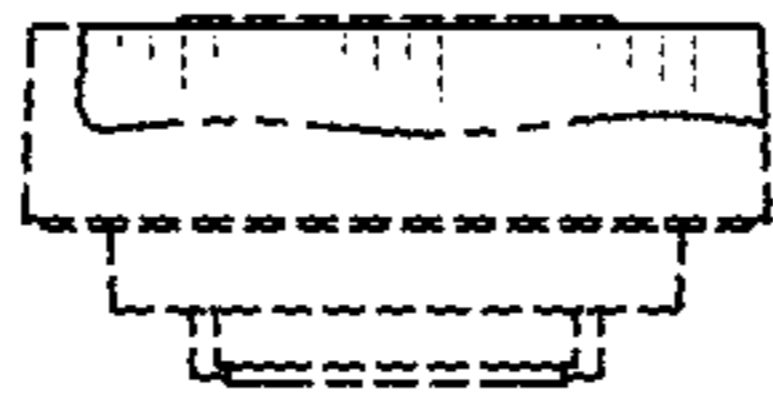


FIG. 6

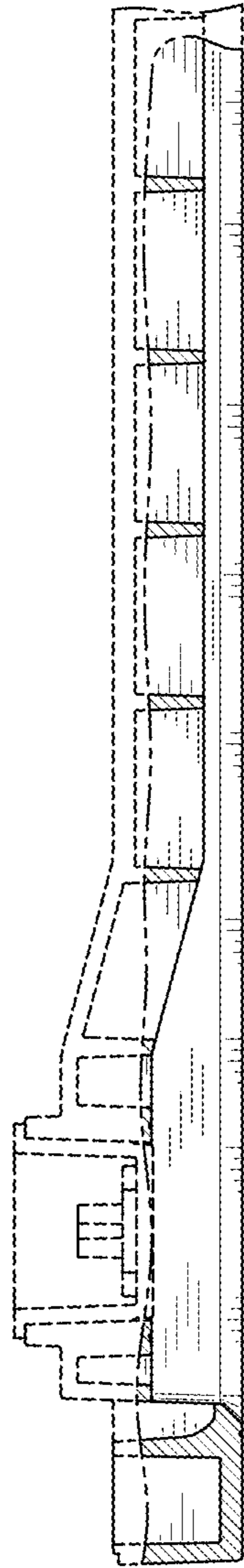


FIG. 8

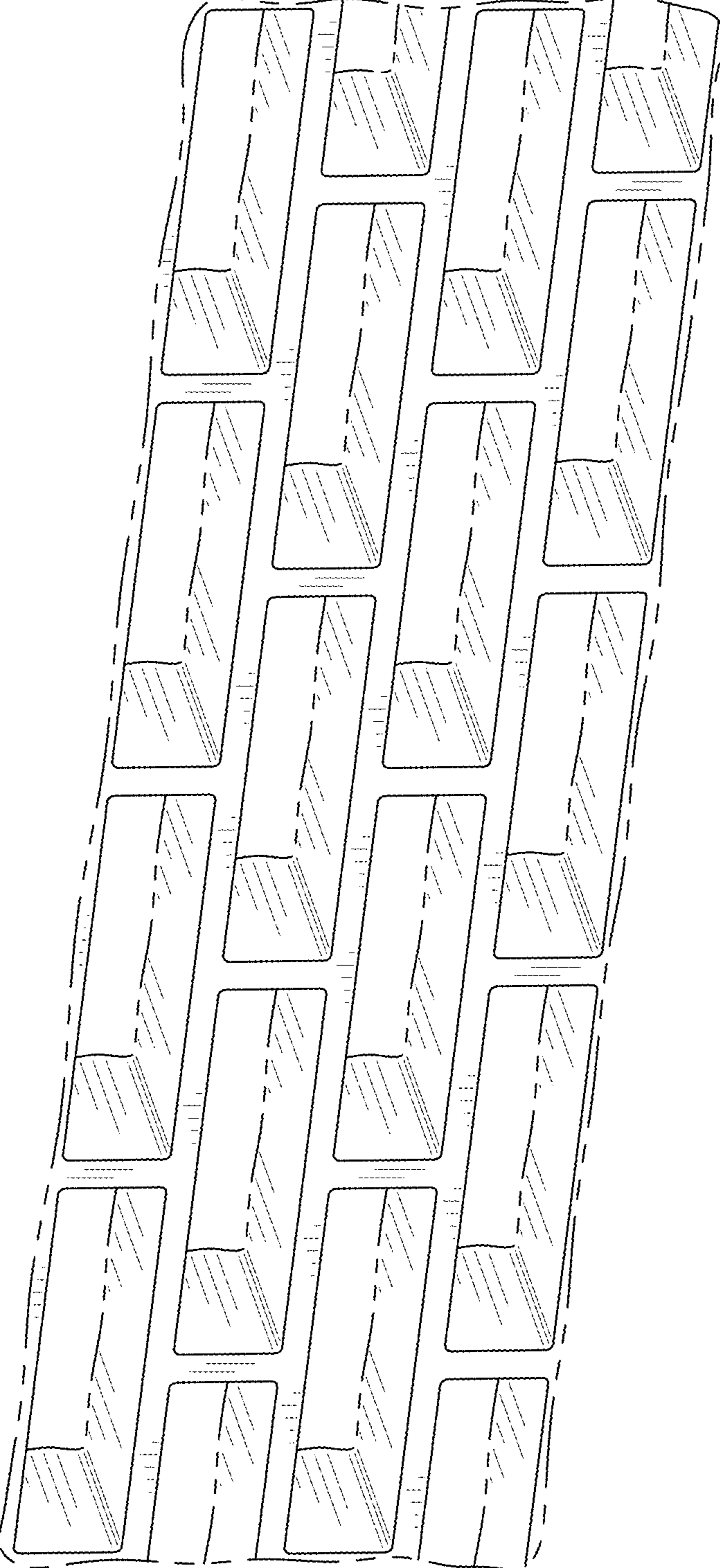


FIG. 9