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(12) **United States Design Patent** (10) **Patent No.:** **US D874,470 S**  
**Bolotin et al.** (45) **Date of Patent:** **\*\* Feb. 4, 2020**

- (54) **ELECTRONIC DEVICE**
- (71) Applicant: **ClevX, LLC**, Kirkland, WA (US)
- (72) Inventors: **Lev M. Bolotin**, Kirkland, WA (US);  
**Robert Hubler**, San Jose, CA (US)
- (73) Assignee: **ClevX, LLC**, Kirkland, WA (US)
- (\*\*) Term: **15 Years**
- (21) Appl. No.: **29/656,335**
- (22) Filed: **Jul. 11, 2018**

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*Primary Examiner* — Angela J Lee

(74) *Attorney, Agent, or Firm* — Schwegman Lundberg & Woessner, P.A.

**Related U.S. Application Data**

- (63) Continuation-in-part of application No. 29/648,875, filed on May 24, 2018.
- (51) **LOC (12) Cl.** ..... **14-03**
- (52) **U.S. Cl.**  
USPC ..... **D14/480.5**
- (58) **Field of Classification Search**  
USPC ..... D14/480.1–480.7, 435.1, 411, 433, 436,  
D14/484.1, 137, 155, 203.1–203.8, 240,  
D14/242; D13/133, 154; D21/325, 329  
CPC ..... H05K 5/0278; G06K 19/07732  
See application file for complete search history.

(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

**DESCRIPTION**

FIG. 1 is a front view of an electronic device (e.g., a secure electronic data storage device or other electronic storage device), showing our new design.

FIG. 2 is a rear view thereof.

FIG. 3 is a left view thereof.

FIG. 4 is a right view thereof.

FIG. 5 is a top view thereof.

FIG. 6 is a bottom view thereof.

FIG. 7 is a top-front-right perspective view thereof.

FIG. 8 is a bottom-front-left perspective view thereof.

FIG. 9 is a bottom-rear-left perspective view thereof; and,

FIG. 10 is a top-rear-right perspective view thereof.

The broken line showing of a connector on the top side of the electronic device is for the purpose of illustrating environmental structure, and the broken lines forms no part of the claimed design.

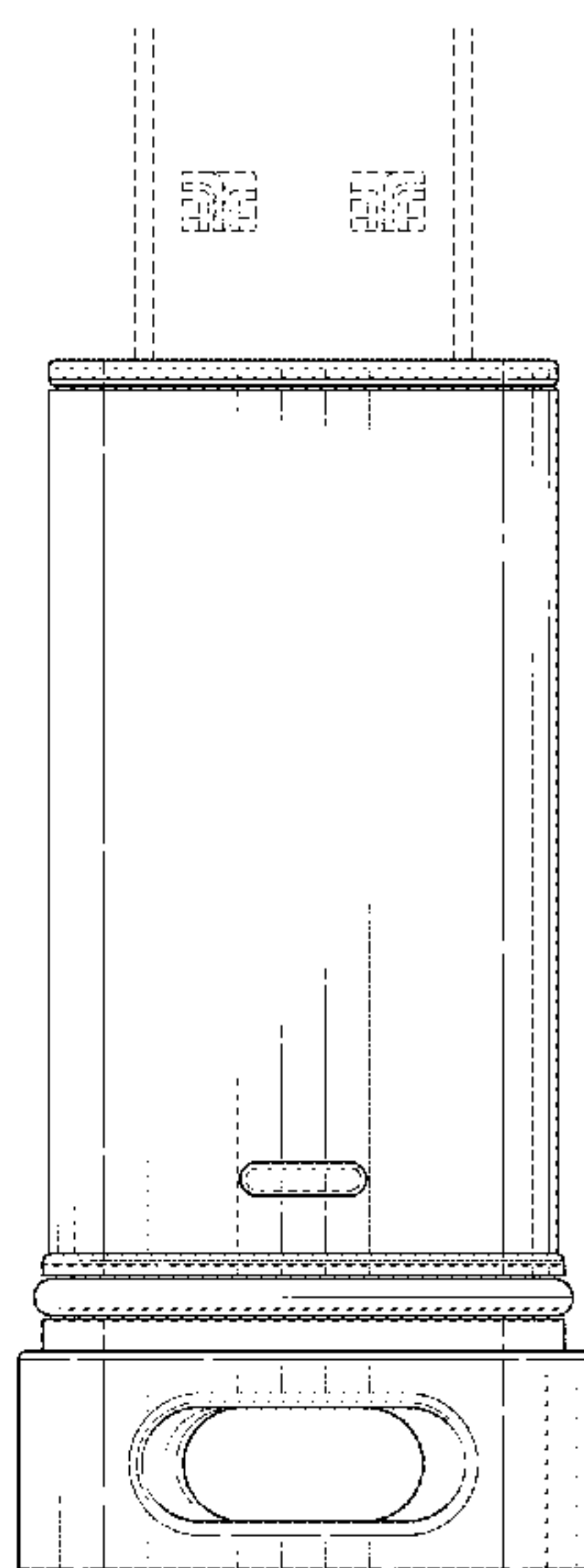
The shade lines show surface contour and not surface ornamentation.

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**1 Claim, 7 Drawing Sheets**



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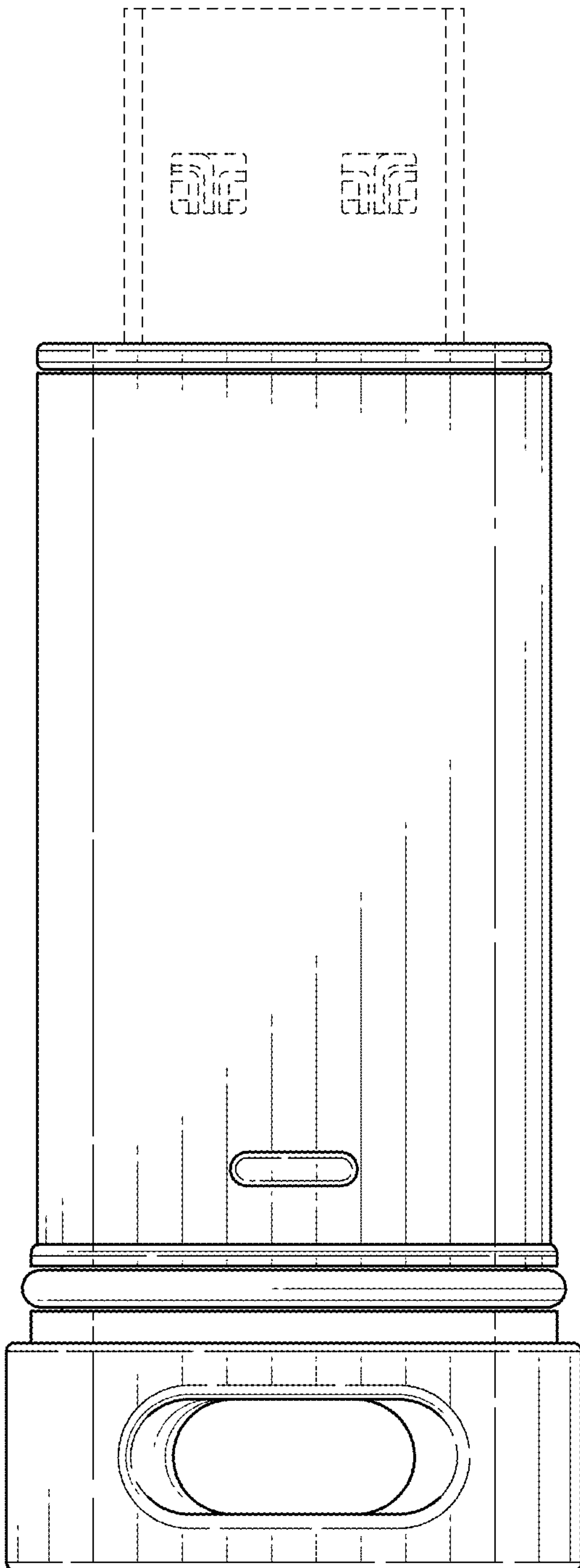


FIG. 1

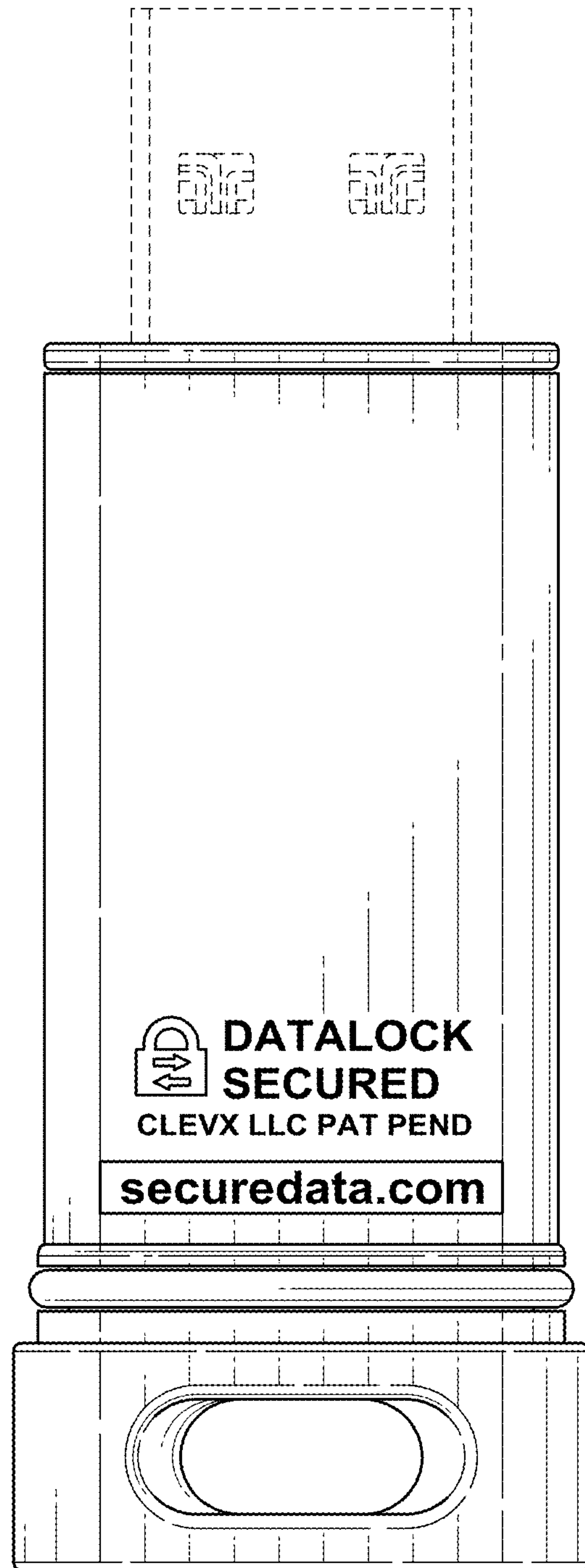


FIG. 2

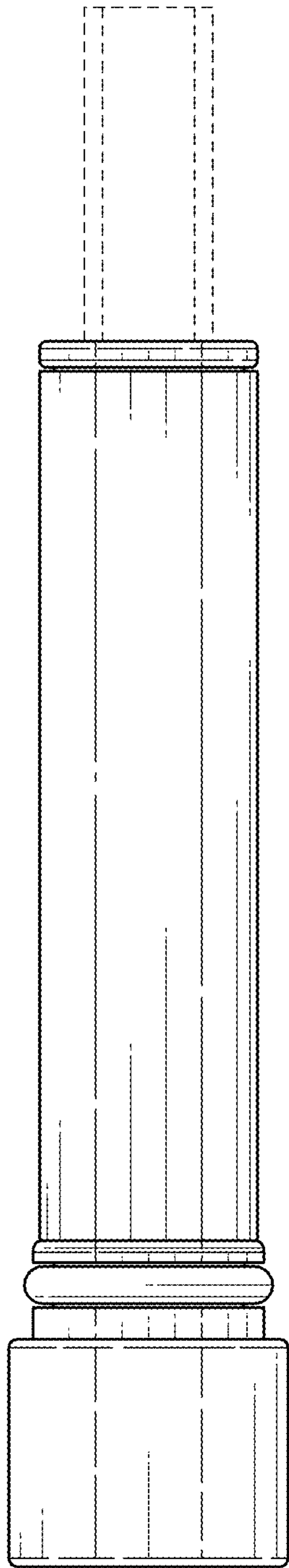


FIG. 3

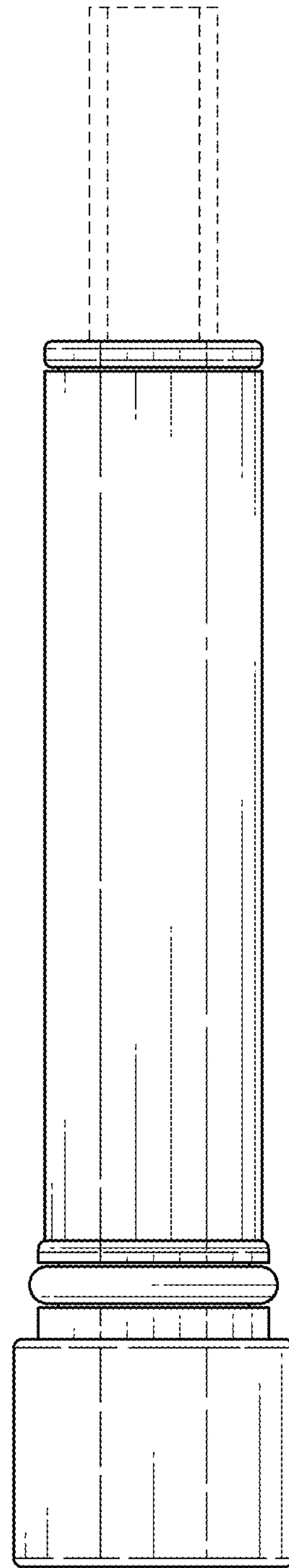


FIG. 4

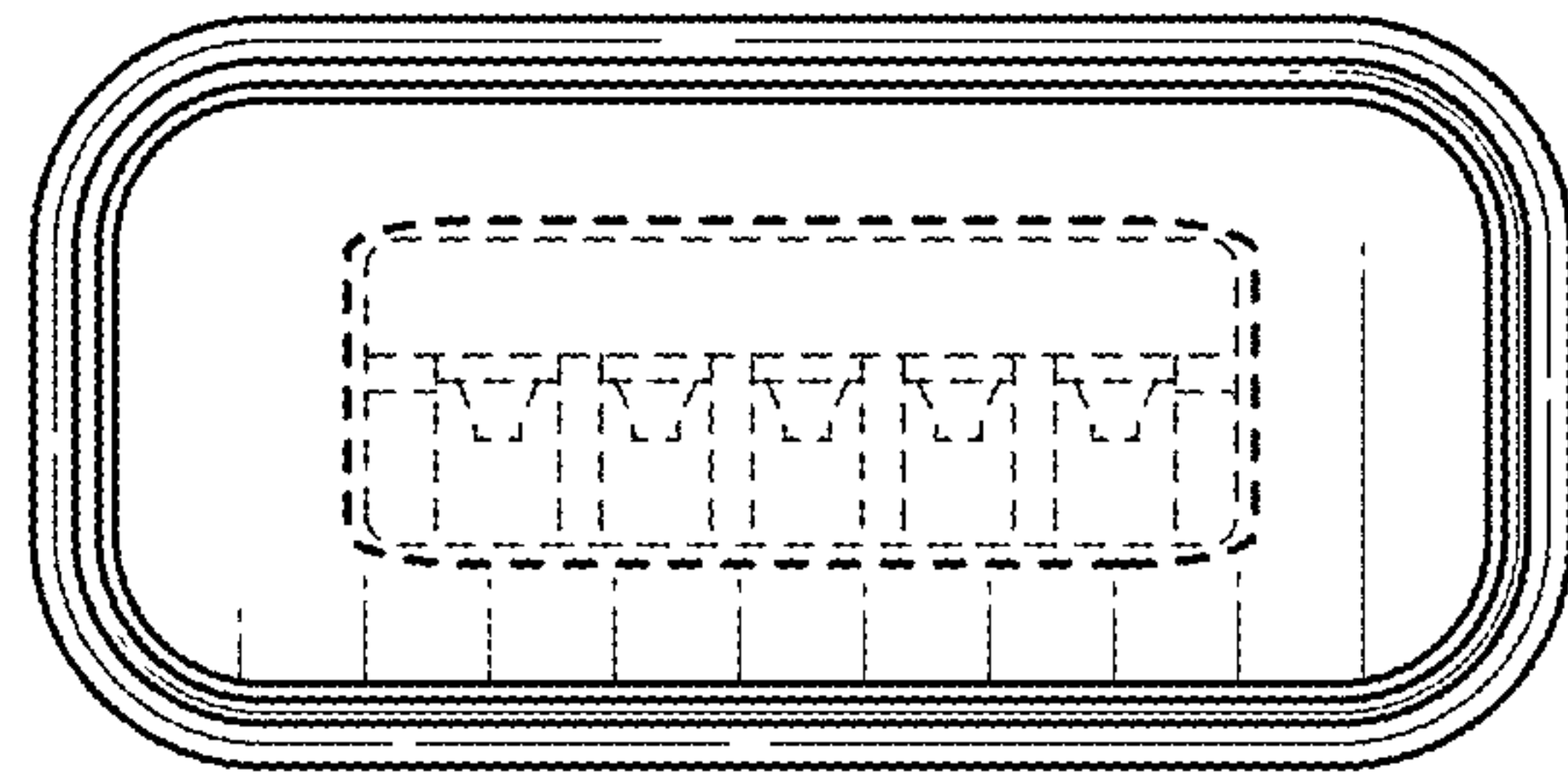


FIG. 5

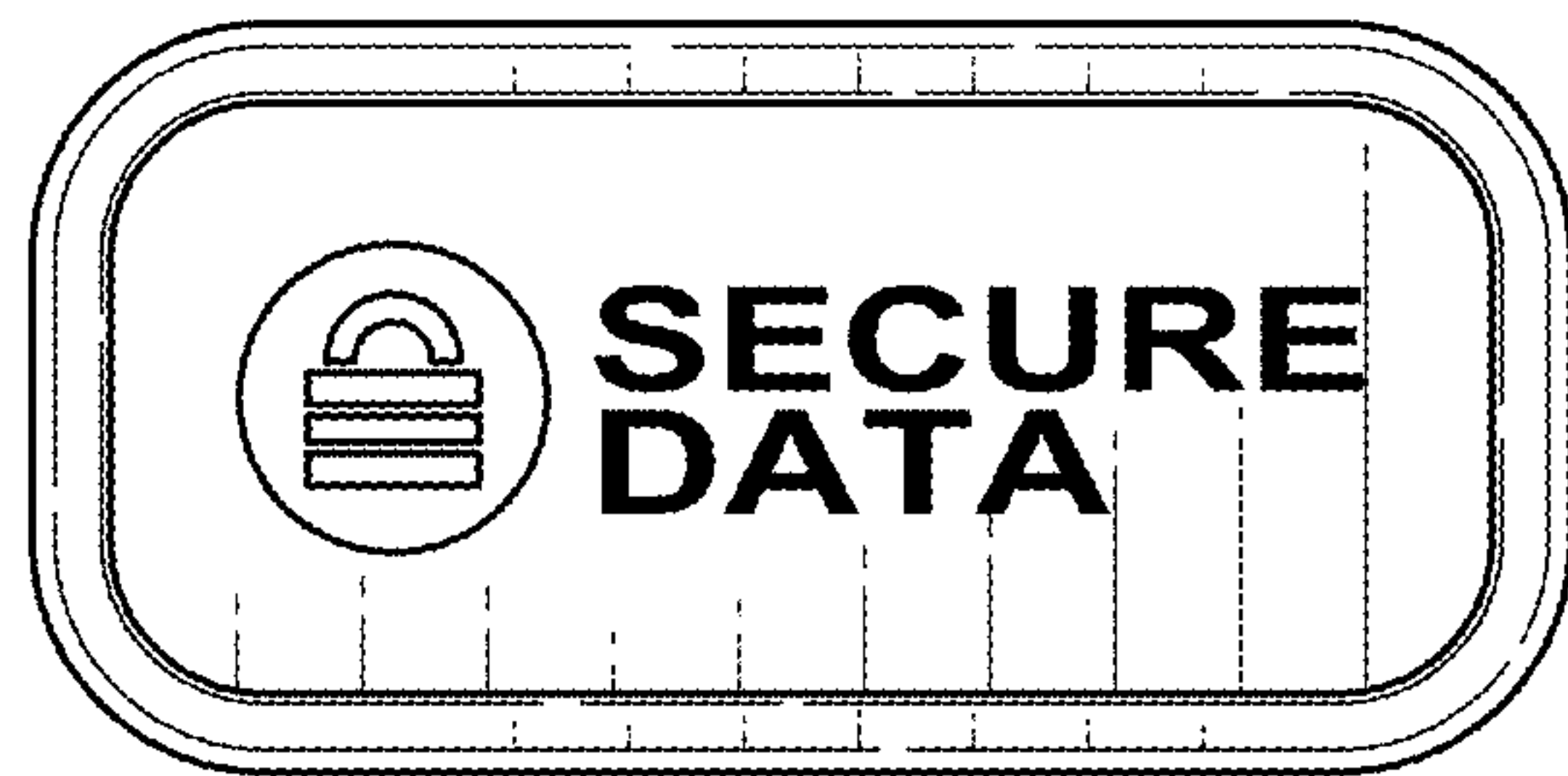


FIG. 6

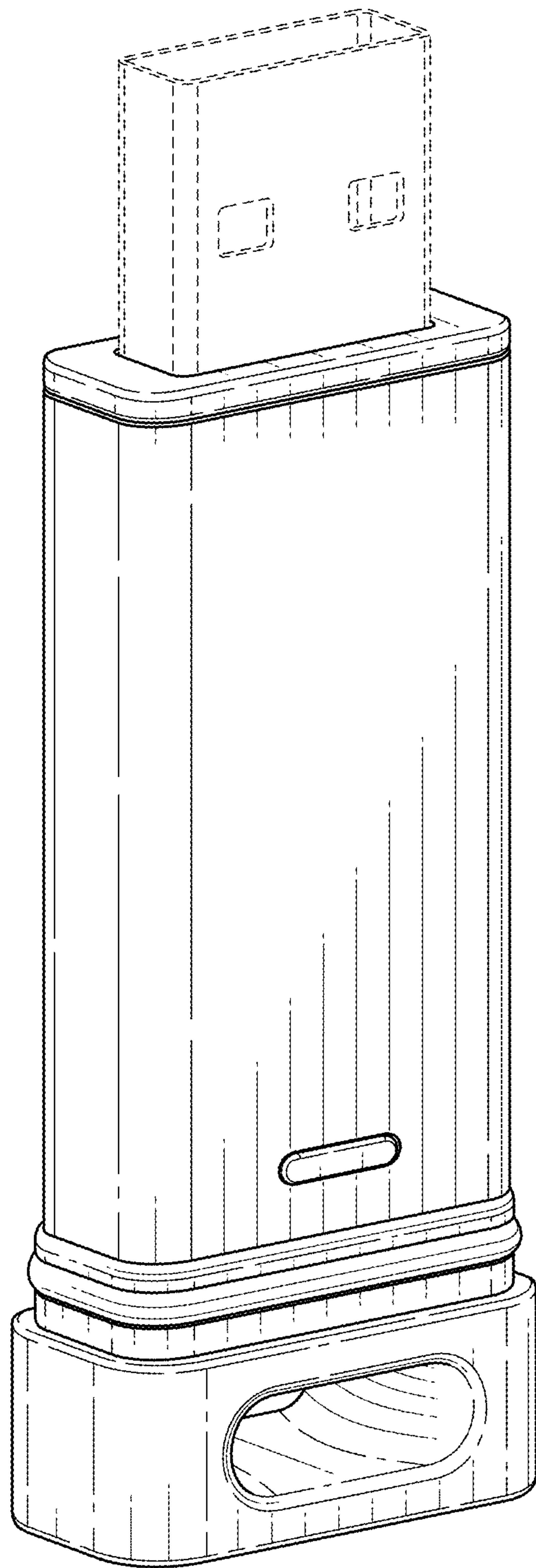


FIG. 7



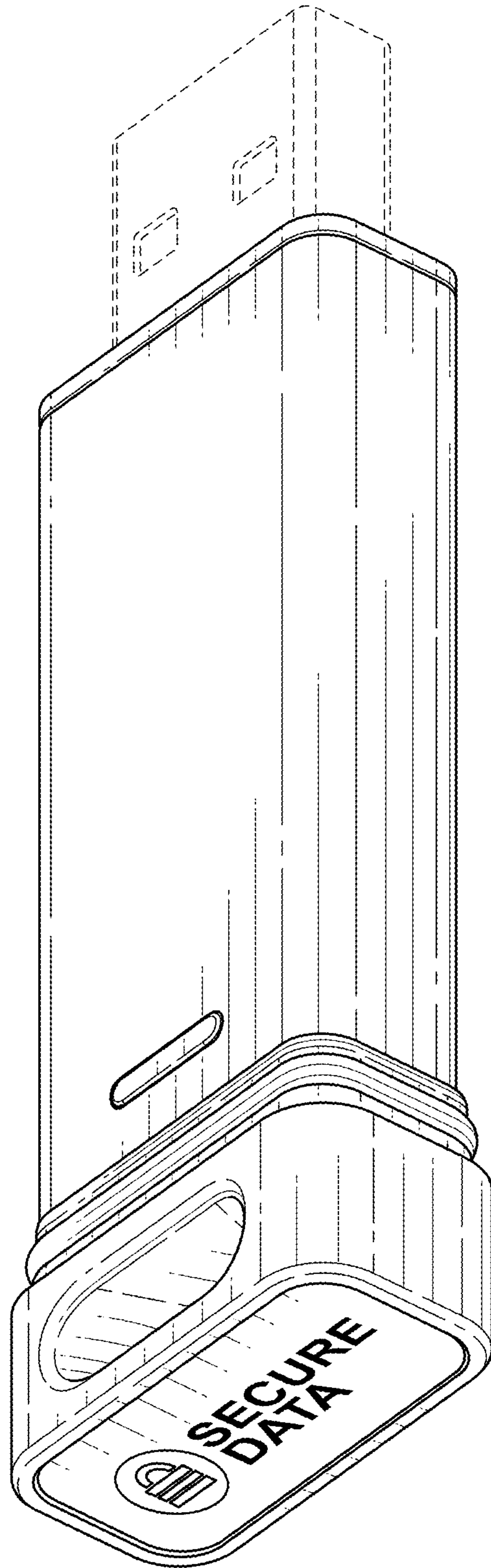


FIG. 8

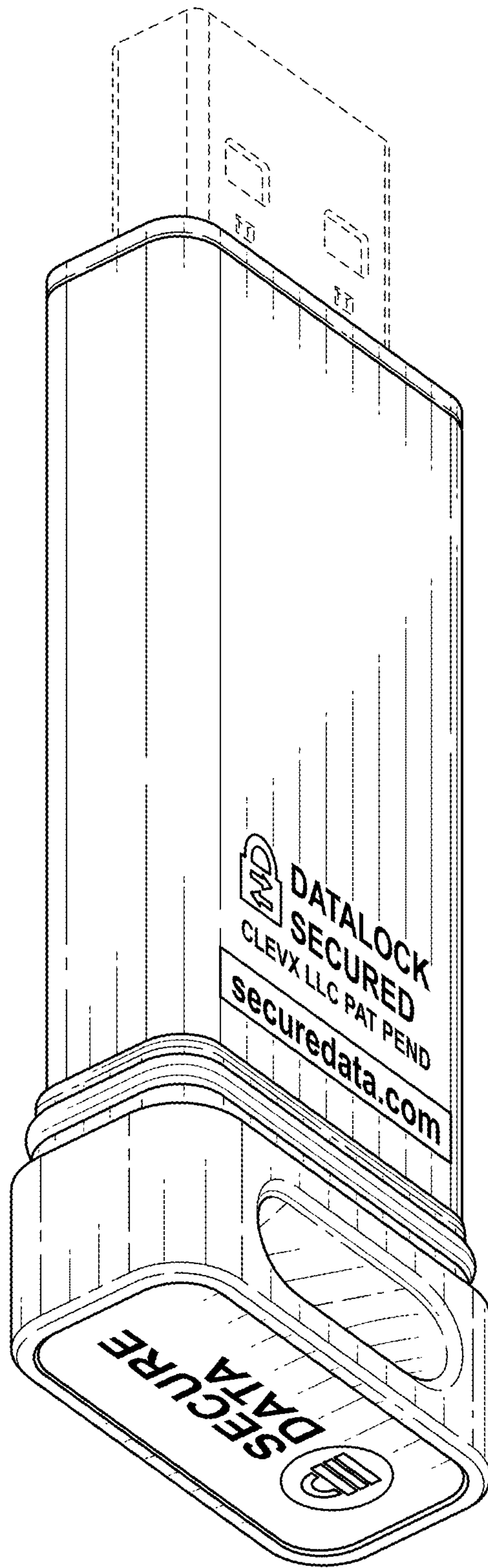


FIG. 9



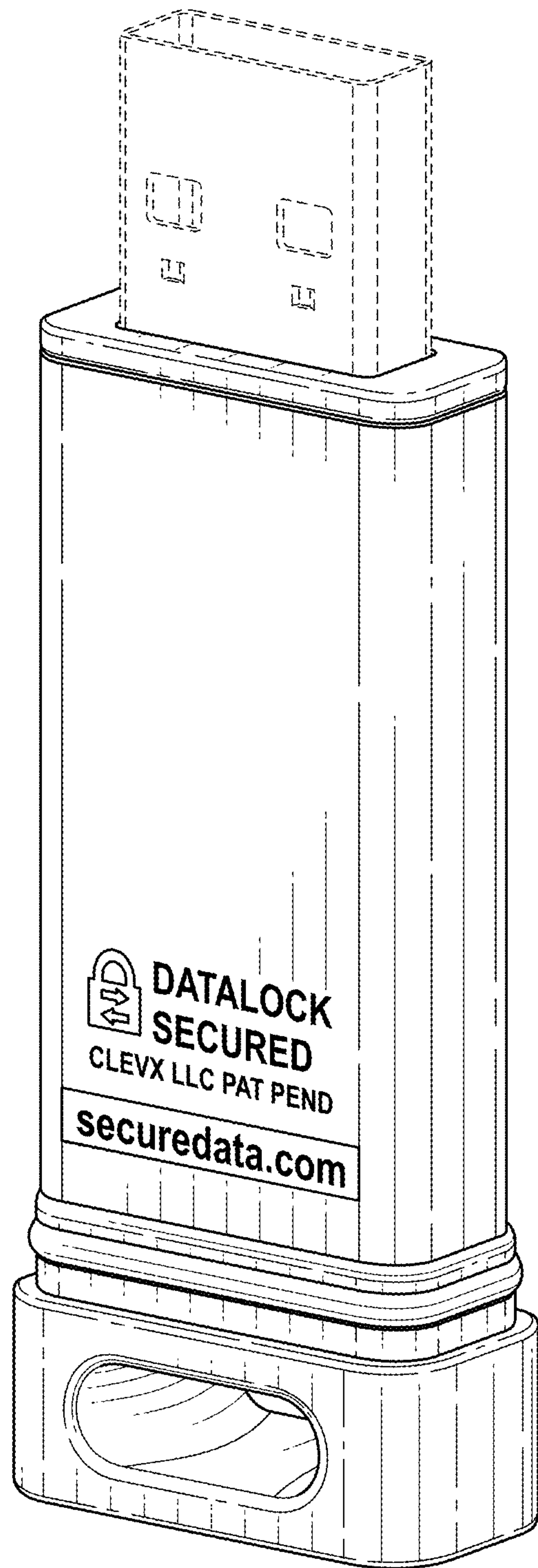


FIG. 10