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(12) **United States Design Patent** (10) **Patent No.:** **US D873,779 S**  
**Lee** (45) **Date of Patent:** **\*\* \*Jan. 28, 2020**

(54) **ELECTRONIC OVERLOAD RELAY**

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(\*) Notice: This patent is subject to a terminal disclaimer.

(\*\*) Term: **15 Years**

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(30) **Foreign Application Priority Data**

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(52) **U.S. Cl.**  
USPC ..... **D13/159**

(58) **Field of Classification Search**  
USPC ..... D13/107, 112, 118, 123, 158-162, 173, D13/174, 184, 199  
CPC ..... G05B 23/0205; H01H 1/00; H01H 9/00; H01H 9/02; H01H 9/24; H01H 9/26; H01H 11/00; H01H 33/02; H01H 36/00; H01H 47/00; H01H 50/14; H01H 61/00; H01H 71/00; H01H 71/02; H01H 71/10; H01H 71/12; H01H 71/16; H01H 73/30; H01H 83/00; H02H 3/02; H02H 3/093; H02H 7/08; H05K 7/02  
See application file for complete search history.

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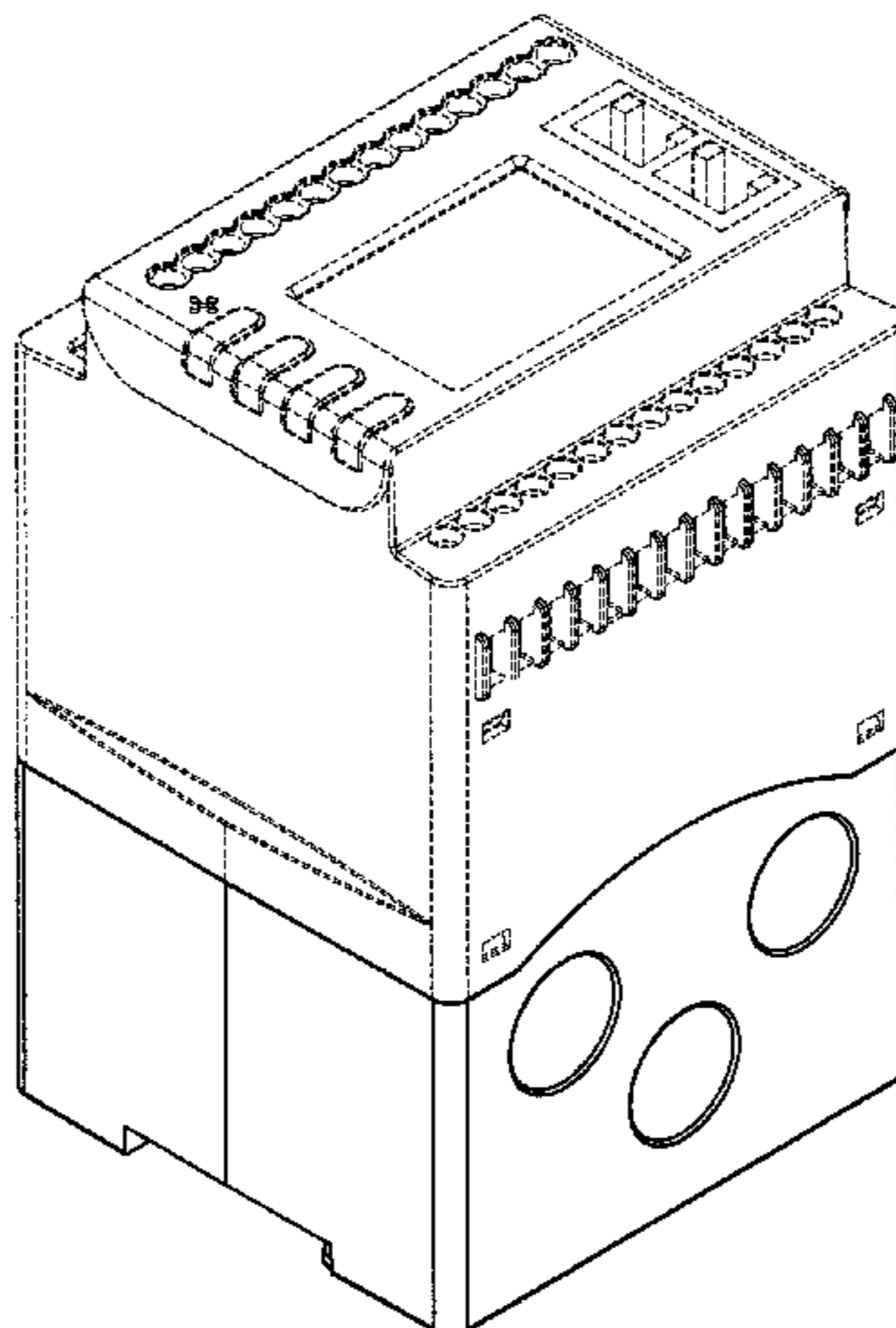
(57) **CLAIM**

The ornamental design for electronic overload relay, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of the electronic overload relay in accordance with the present invention; FIG. 2 is a front elevational view thereof; FIG. 3 is a rear elevational view thereof; FIG. 4 is a left side elevational view thereof; FIG. 5 is a right side elevational view thereof; FIG. 6 is a top plan view thereof; and, FIG. 7 is a bottom plan view thereof. The broken line portion of the figure drawings is included to show portions of the electronic overload relay that form no part of the claimed design.

**1 Claim, 7 Drawing Sheets**



(56)

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Fig. 1

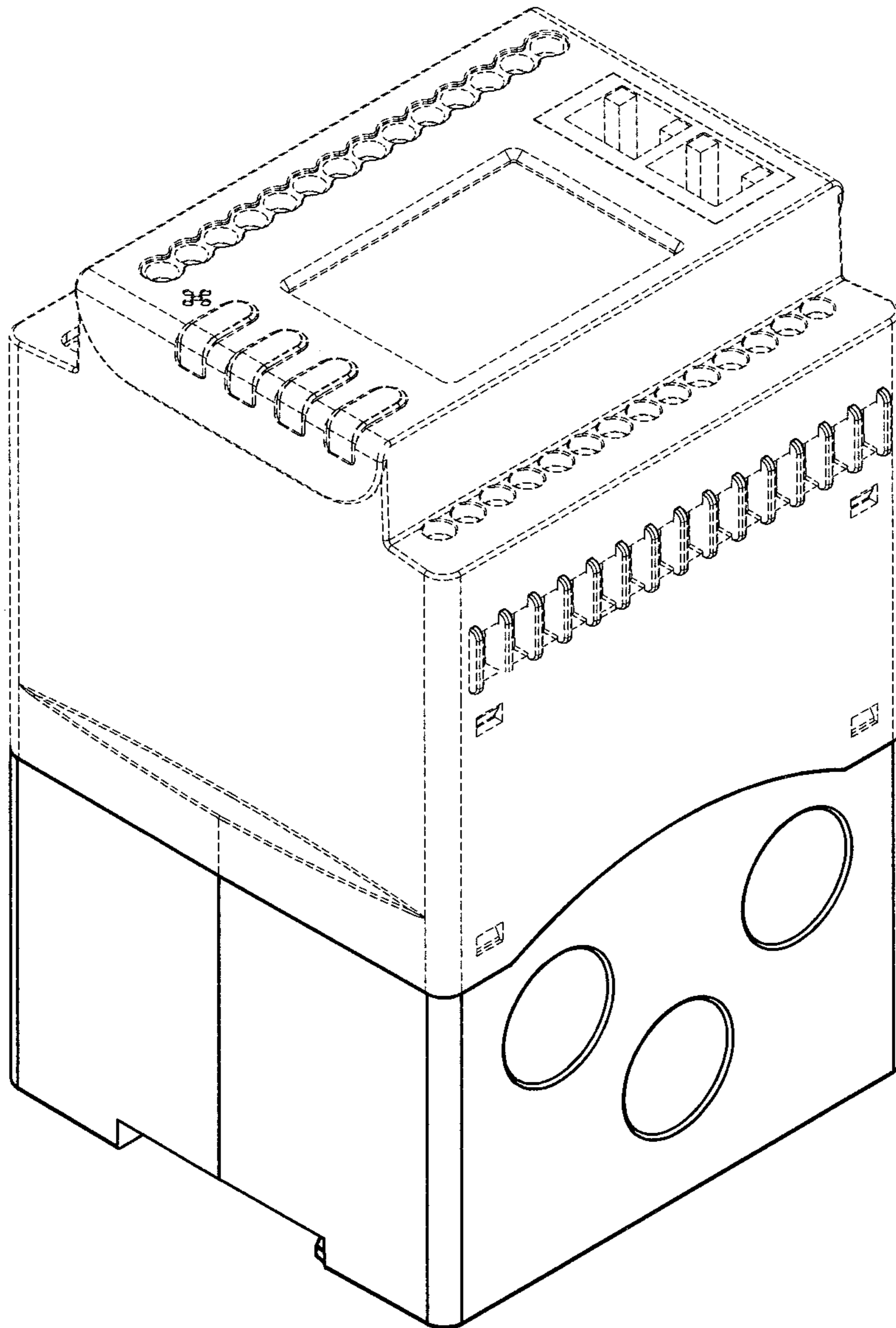


Fig. 2

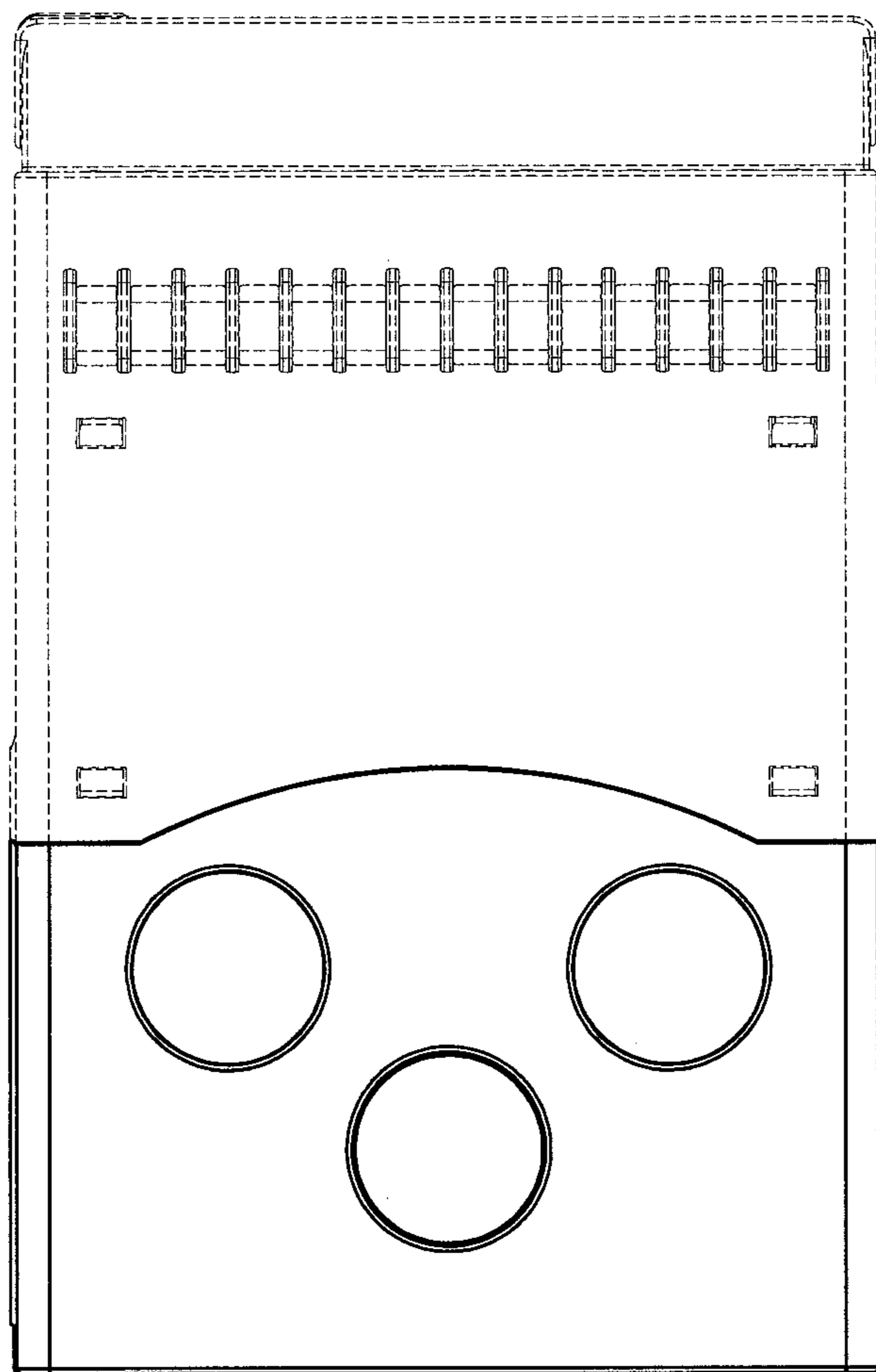


Fig. 3

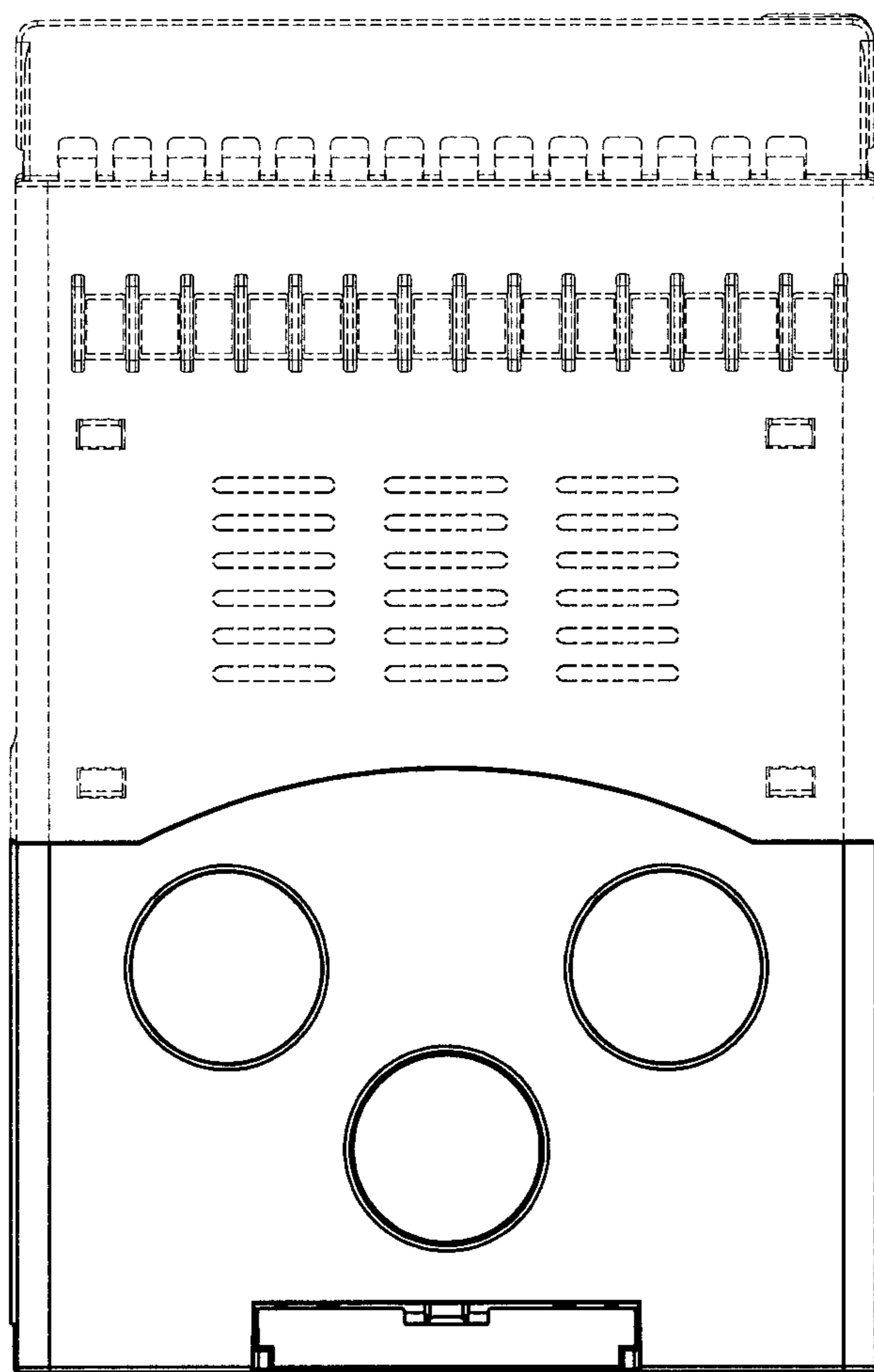


Fig. 4

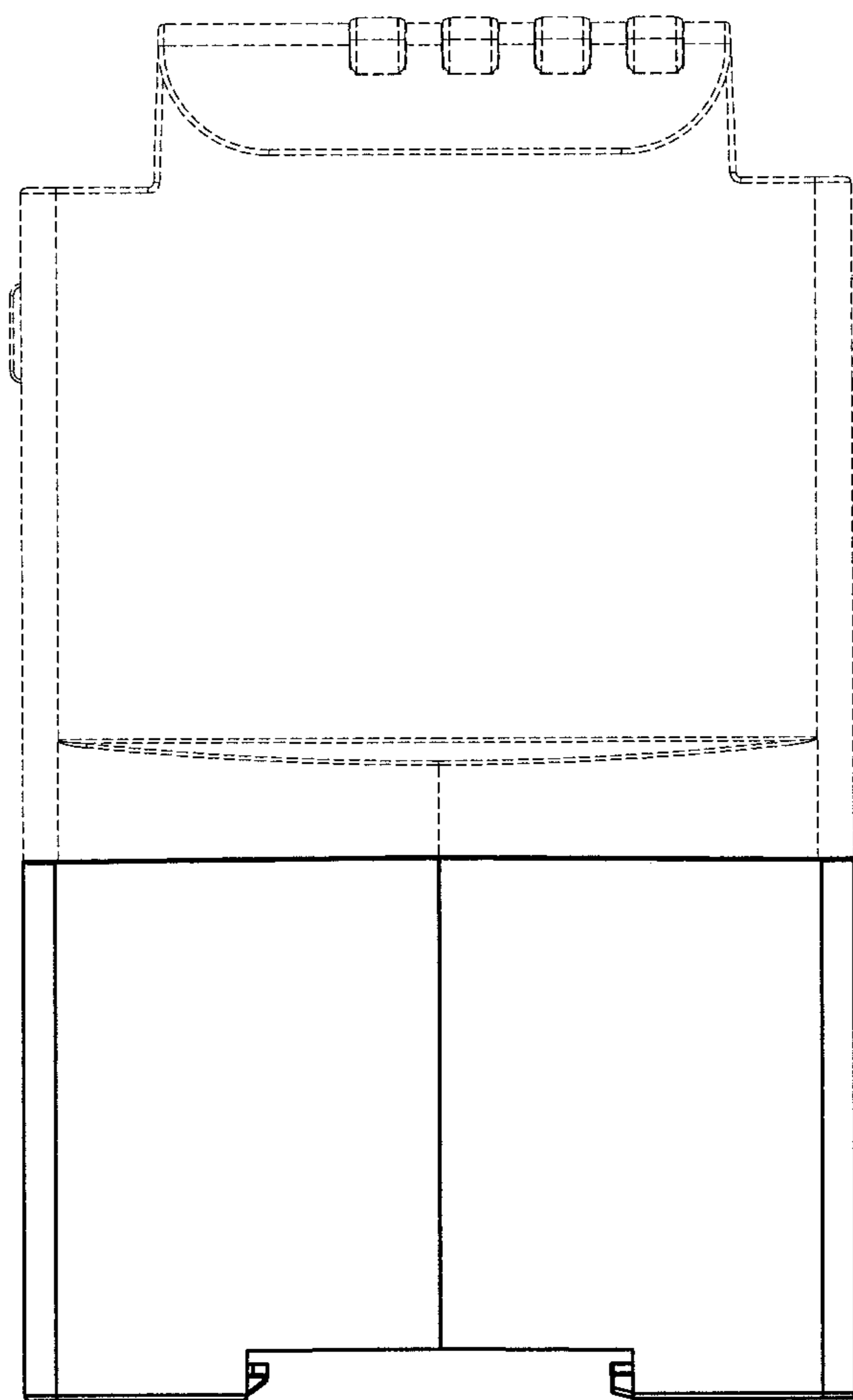


Fig. 5

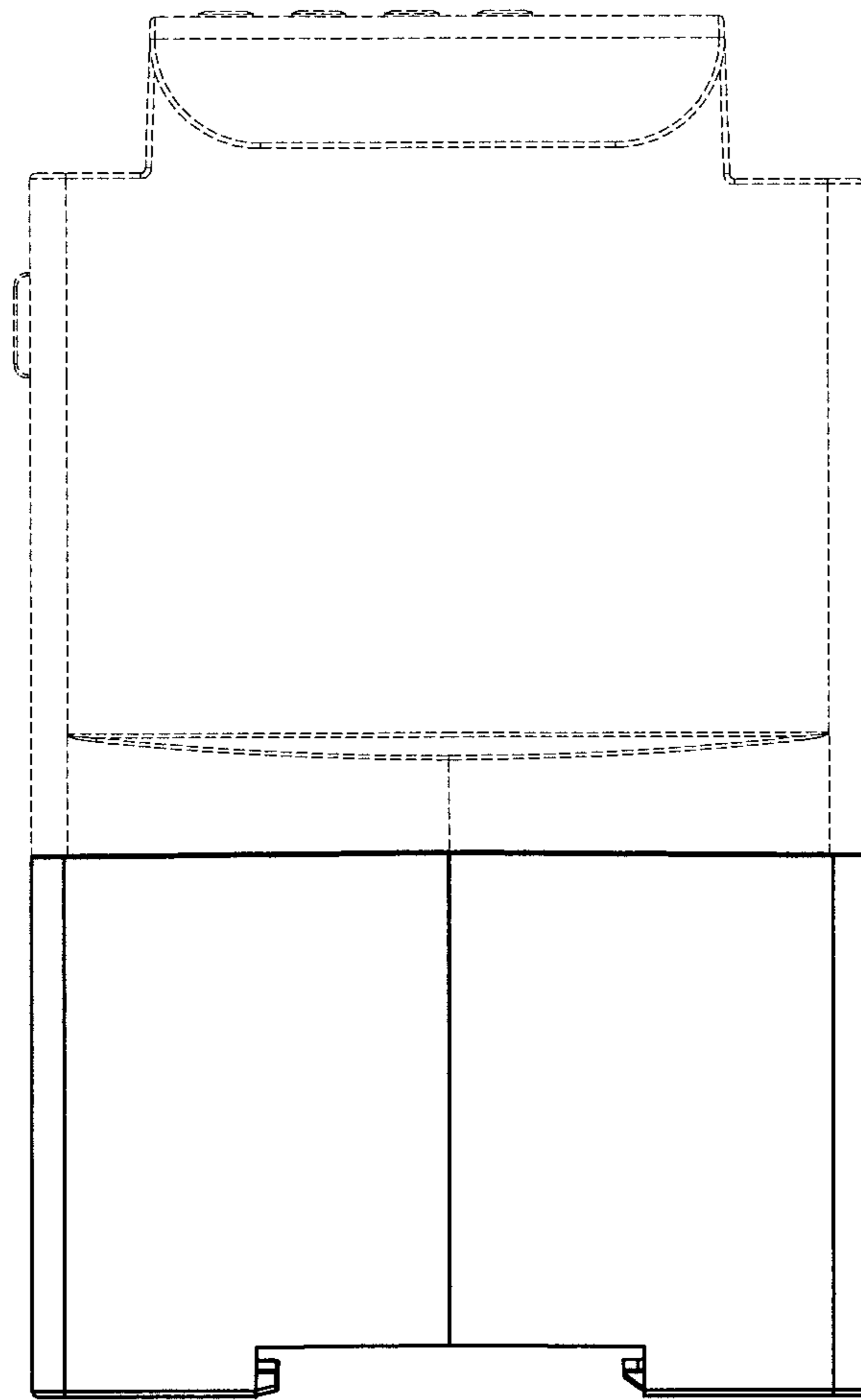




Fig. 6

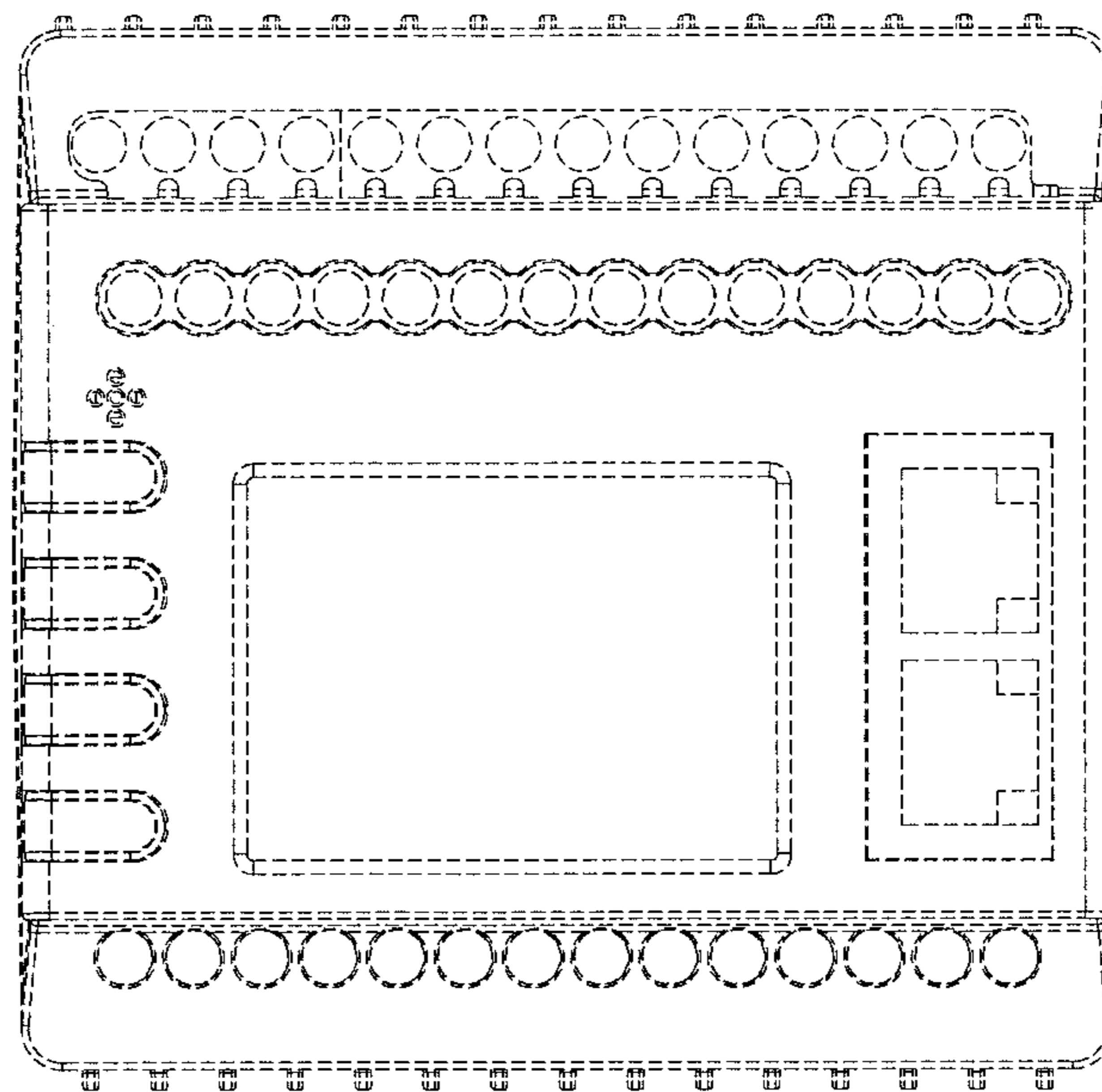




Fig. 7

