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(12) **United States Design Patent**
Krivosnak et al.

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- (54) **ELECTRONICS ENCLOSURE**
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(**) Term: **15 Years**
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(51) **LOC (12) Cl.** **13-03**
(52) **U.S. Cl.**
USPC **D13/184**
(58) **Field of Classification Search**
USPC D13/123, 158, 162, 184, 199, 110, 124,
D13/137.1
CPC . G06F 1/20; G06F 1/181; G06F 1/187; G06F
1/183; G06F 1/184; G06F 1/16; H05K
7/16; H05K 7/1424; H05K 7/1409; H05K
7/20; H05K 7/20545; H05K 7/20727;
H05K 7/1425
See application file for complete search history.

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Design U.S. Appl. No. 29/614,800, filed Aug. 23, 2017 (divisional of U.S. Pat. No. D. 802,582 noted above).

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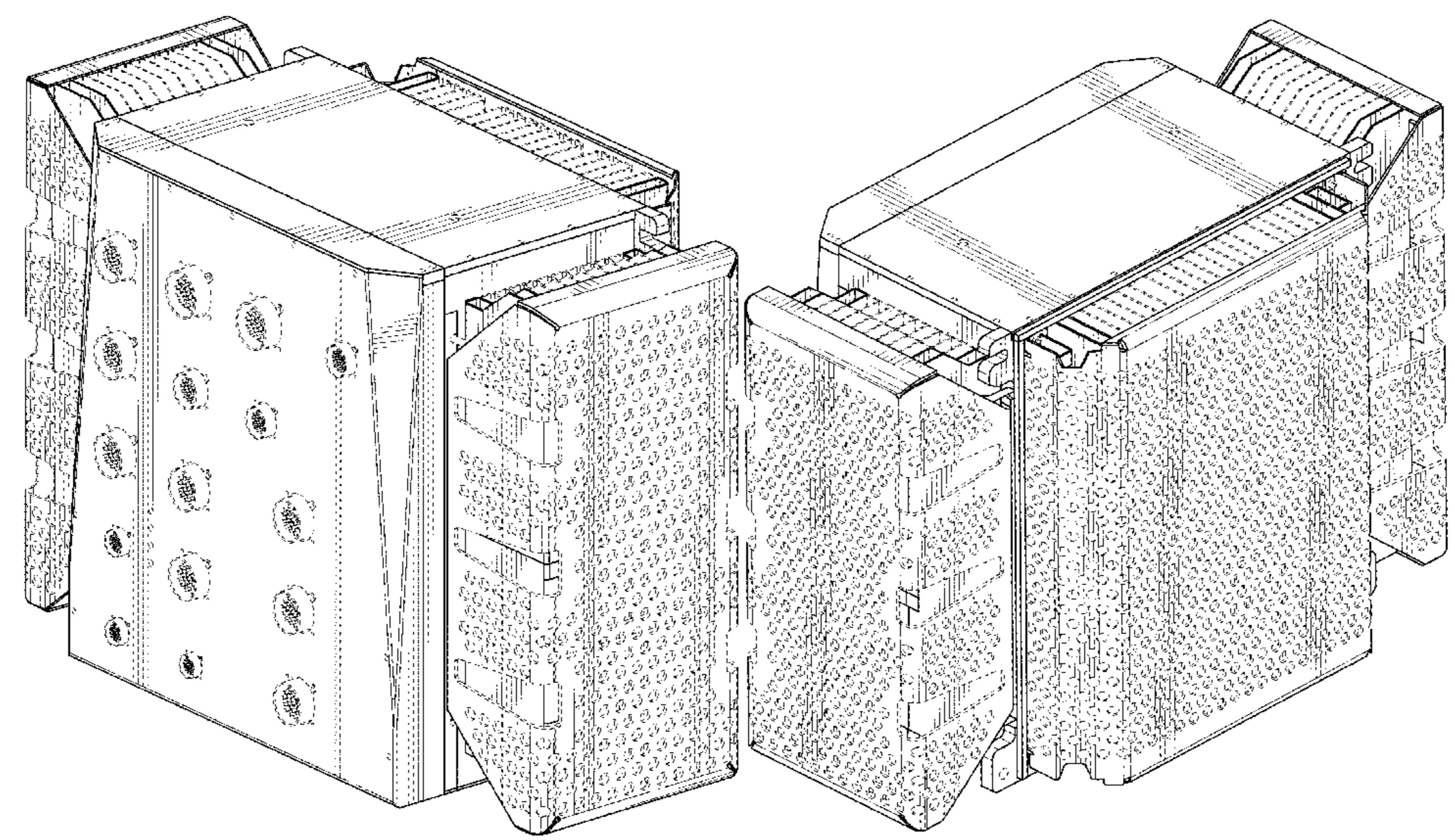
(57) **CLAIM**

The ornamental design for an electronics enclosure, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an electronics enclosure depicting a new design;
FIG. 2 is a front view thereof;
FIG. 3 is a back view thereof;
FIG. 4 is a side view thereof;
FIG. 5 is an opposite side view thereof;
FIG. 6 is a top plan view thereof;
FIG. 7 is a bottom plan view thereof; and,
FIG. 8 is an additional perspective view thereof.
The broken line portion of the figure drawings is included to show unclaimed subject matter only and forms no part of the claimed design.

1 Claim, 8 Drawing Sheets



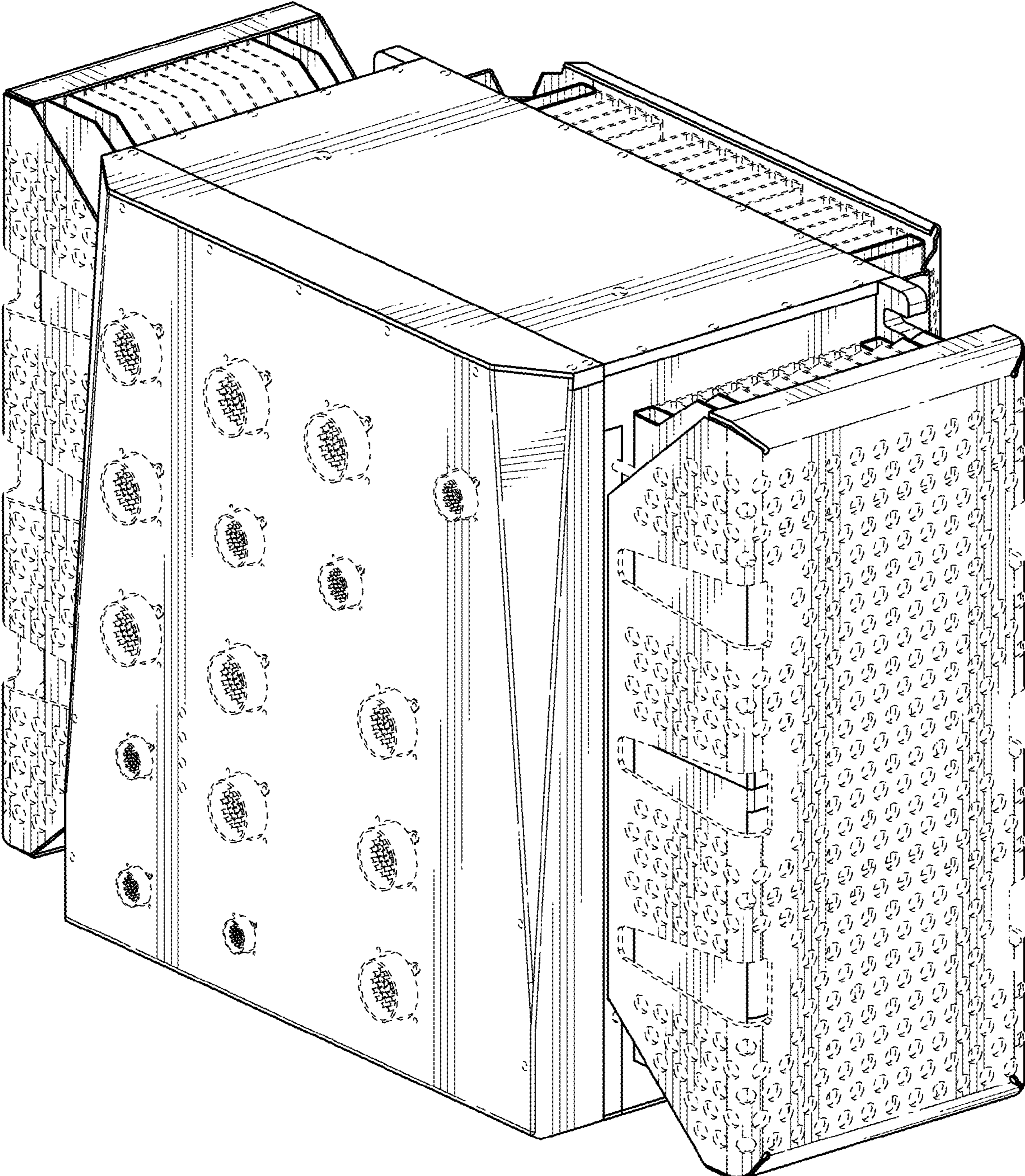


FIG. 1

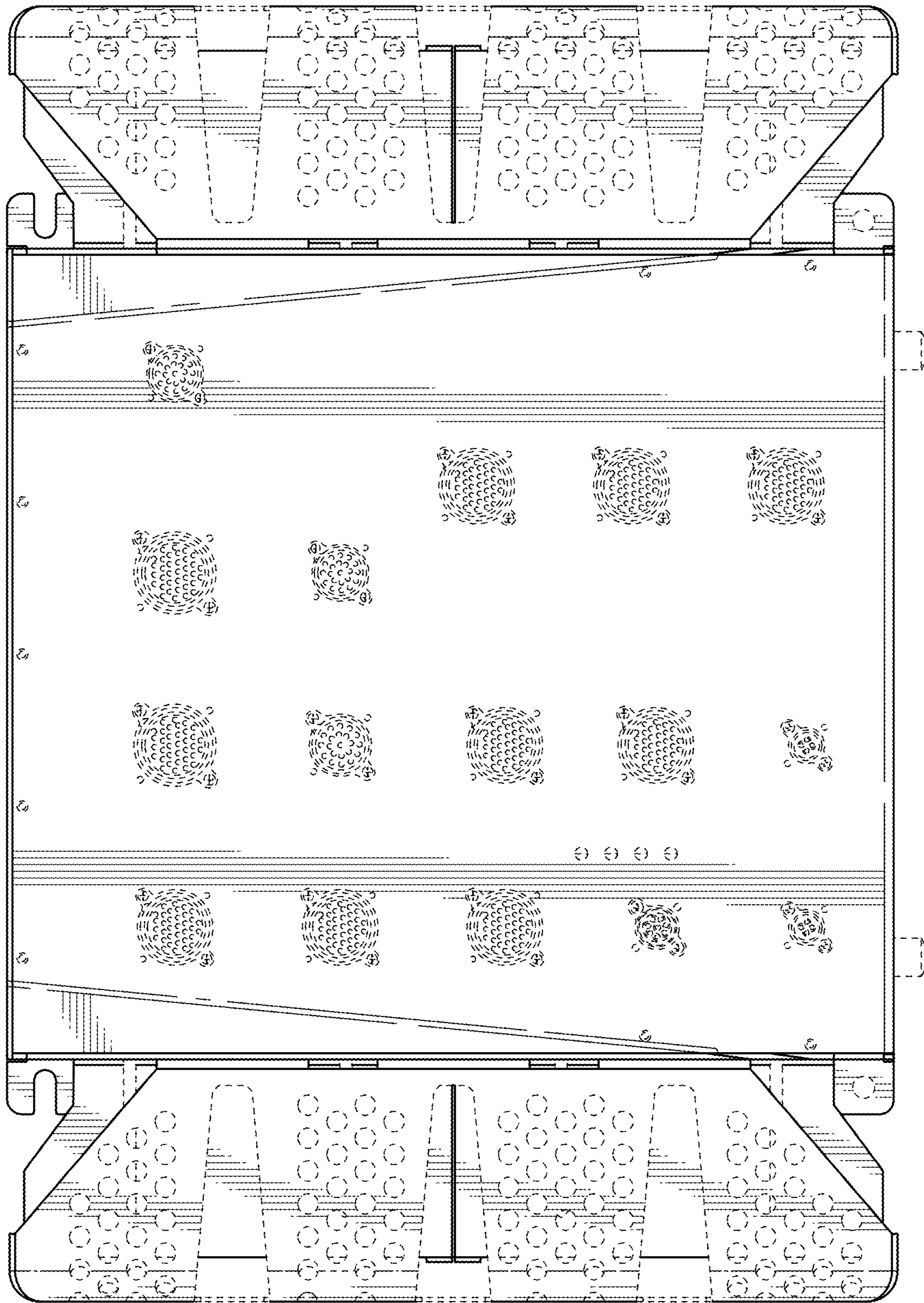


FIG. 2

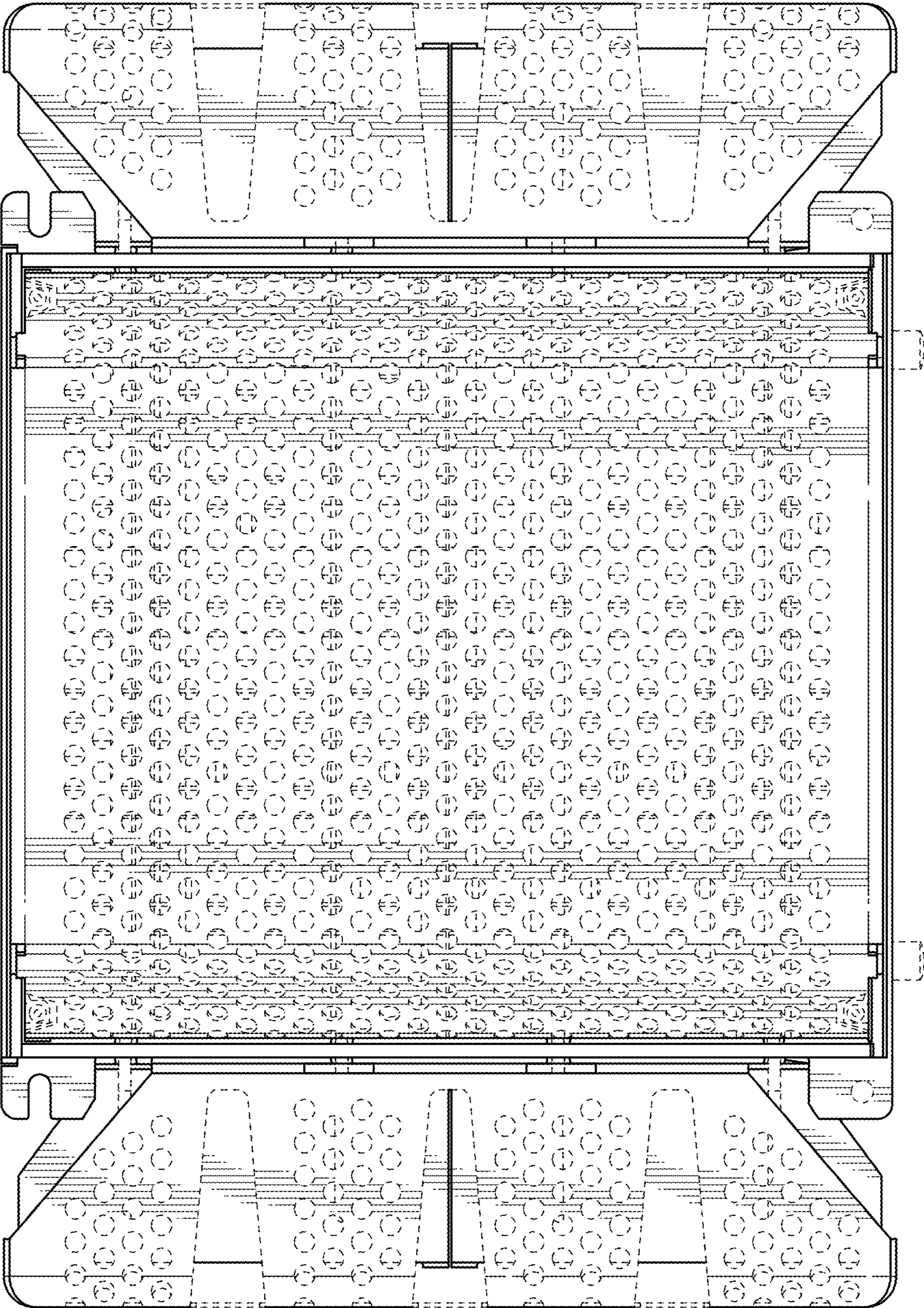


FIG. 3

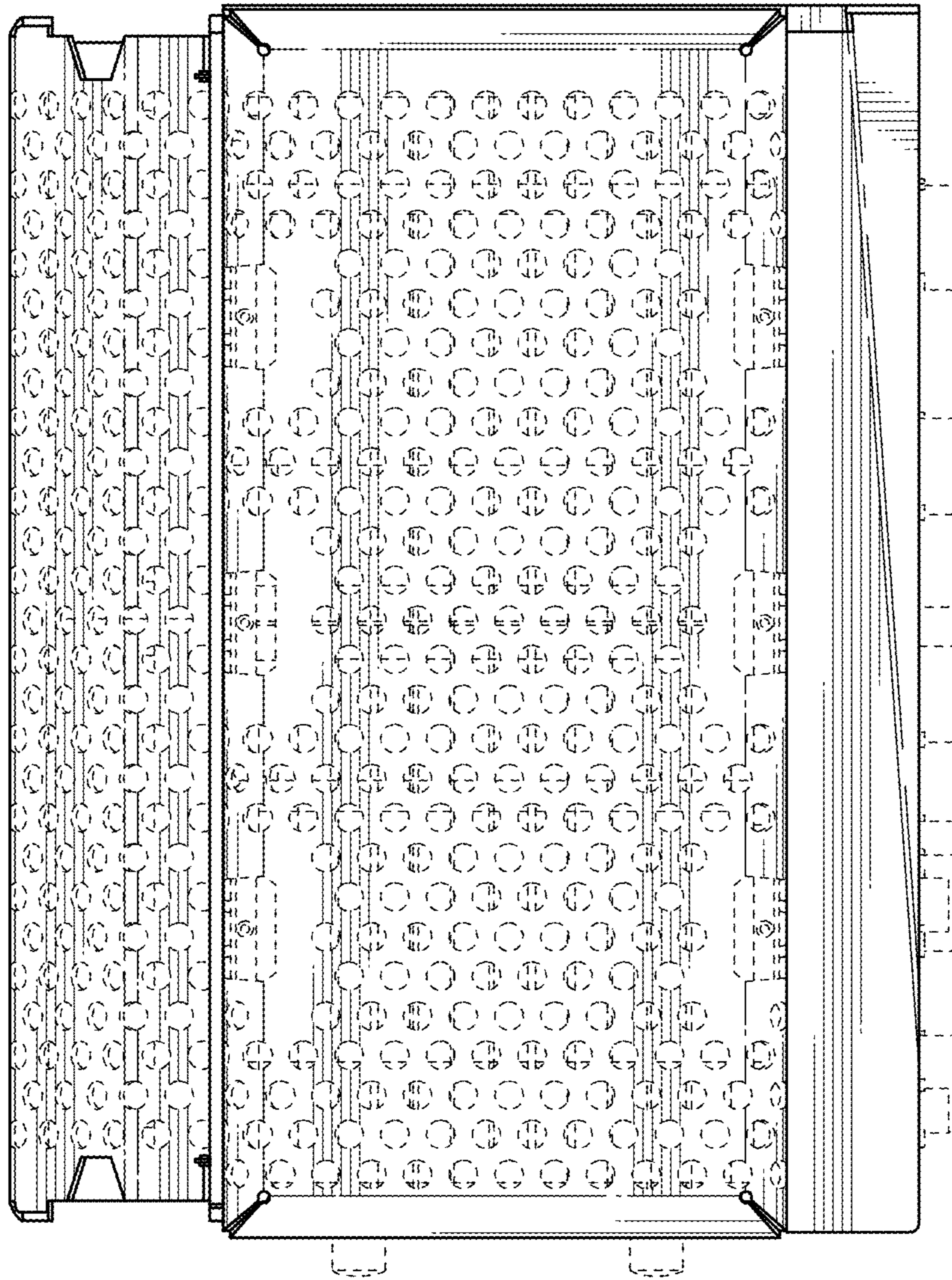


FIG. 4

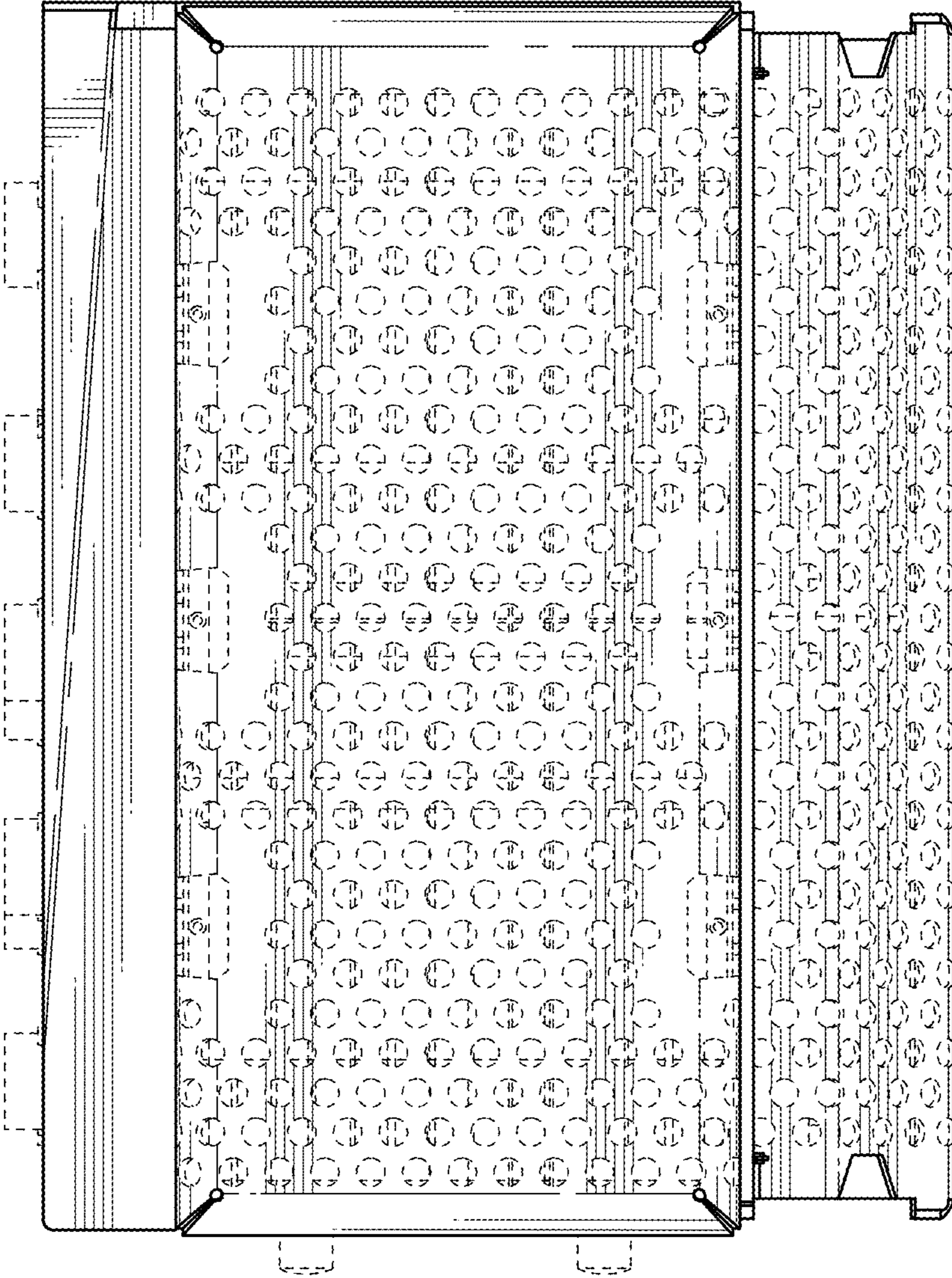


FIG. 5

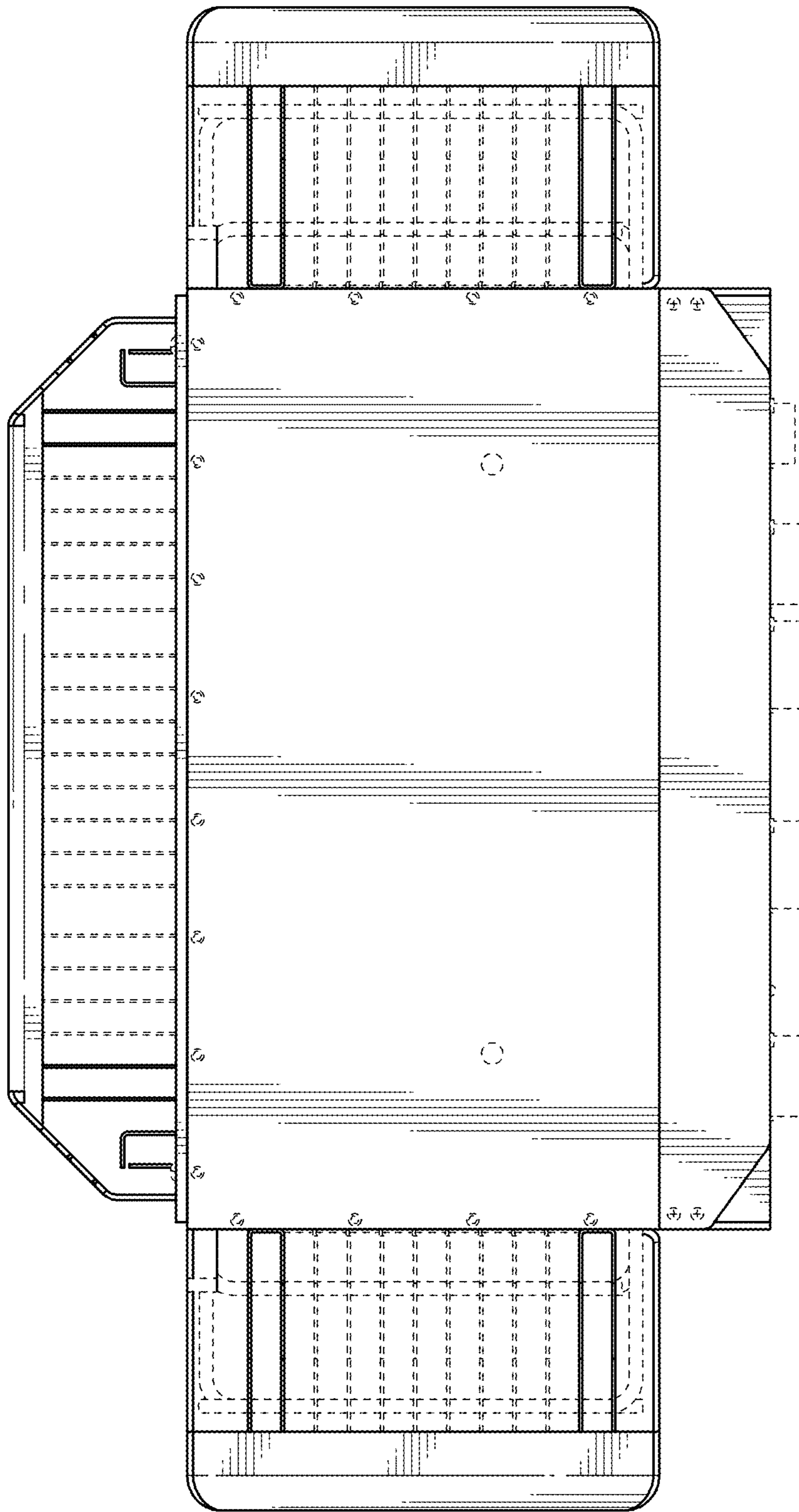


FIG. 6

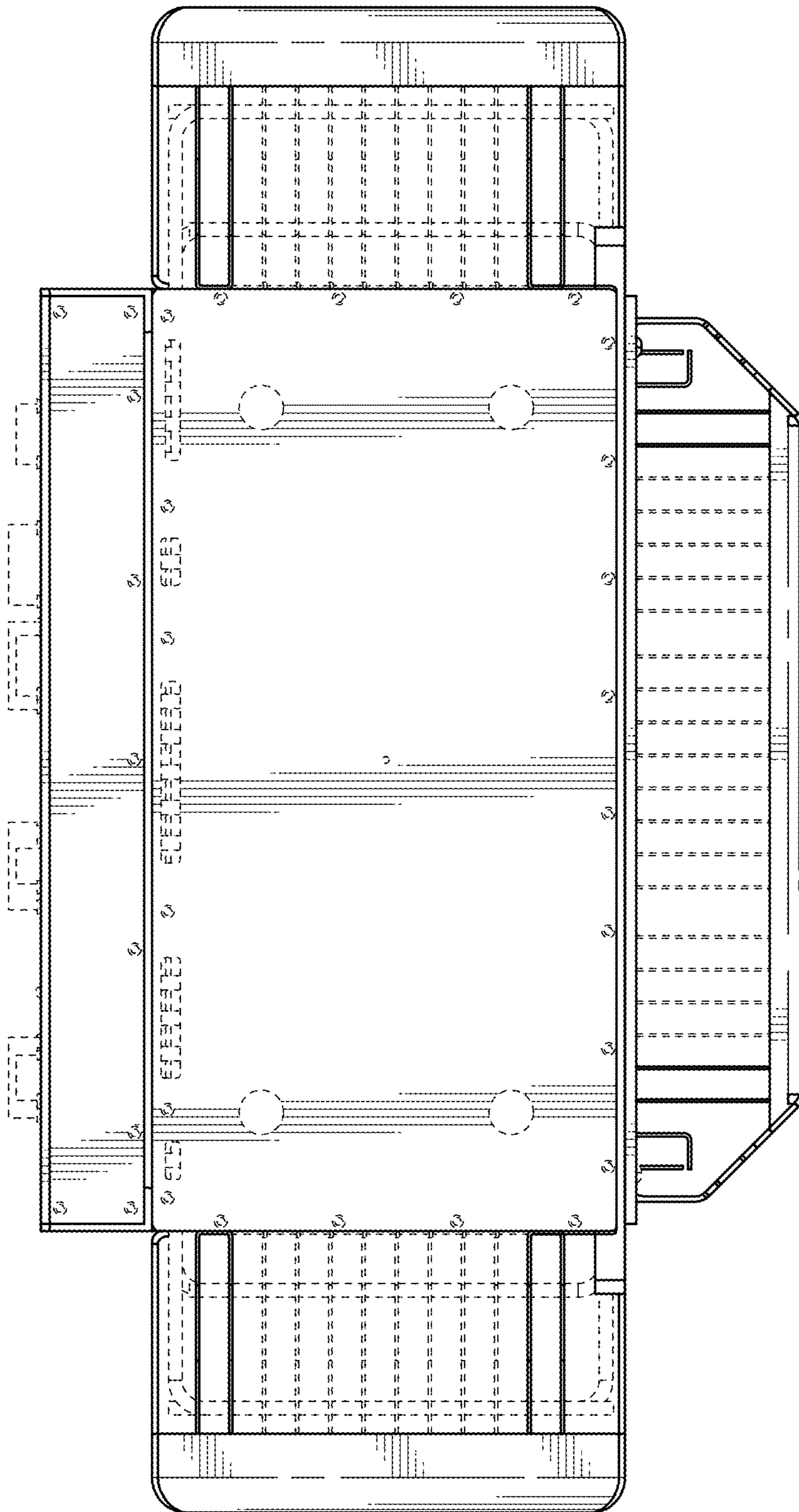


FIG. 7

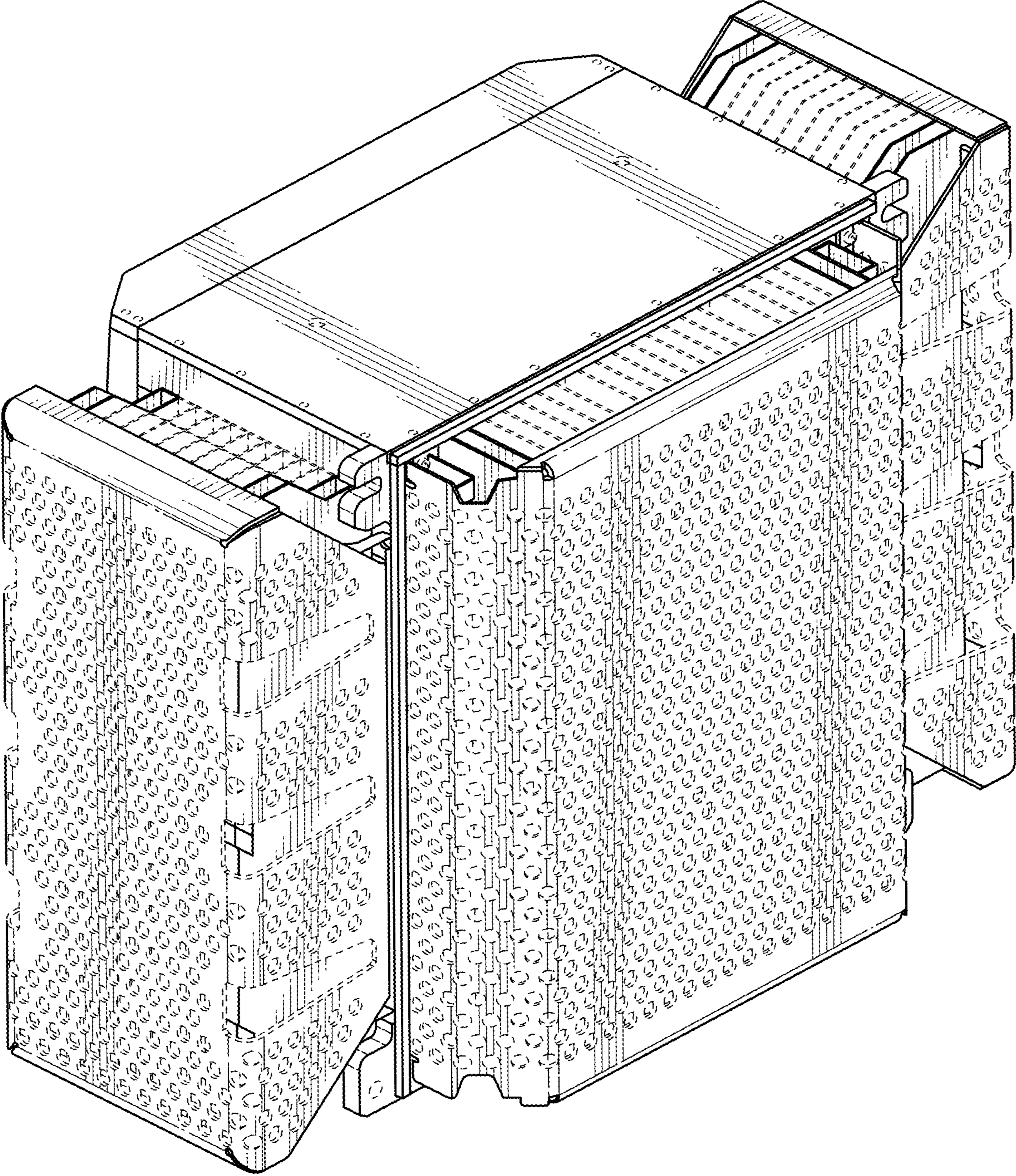


FIG. 8