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(12) **United States Design Patent** (10) **Patent No.:** **US D872,096 S**
Siminoff et al. (45) **Date of Patent:** **** Jan. 7, 2020**

(54) **DEVICE MOUNT**

(71) Applicant: **Amazon Technologies, Inc.**, Seattle, WA (US)

(72) Inventors: **Mark Siminoff**, Mountain View, CA (US); **Matthew J. England**, Santa Monica, CA (US)

(73) Assignee: **Amazon Technologies, Inc.**, Seattle, WA (US)

(**) Term: **15 Years**

(21) Appl. No.: **29/648,047**

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(51) **LOC (12) Cl.** **08-07**

(52) **U.S. Cl.**
USPC **D14/447**

(58) **Field of Classification Search**
USPC D14/447, 432, 434, 439, 440, 451, 452, D14/457, 239, 250, 253, 203.7, 336, 337; D8/363, 373, 380; D6/406.3, 406.4, D6/406.5, 406.6; D12/415; D21/330, D21/331; D3/218
CPC A47B 21/04; A47B 2097/006; A47B 2097/005; A47B 2023/049; A45C 2011/002; A45C 2011/003; F16M 2200/00; F16M 13/00
See application file for complete search history.

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Primary Examiner — Angela J Lee

(74) *Attorney, Agent, or Firm* — Lee & Hayes, P.C.

(57) **CLAIM**

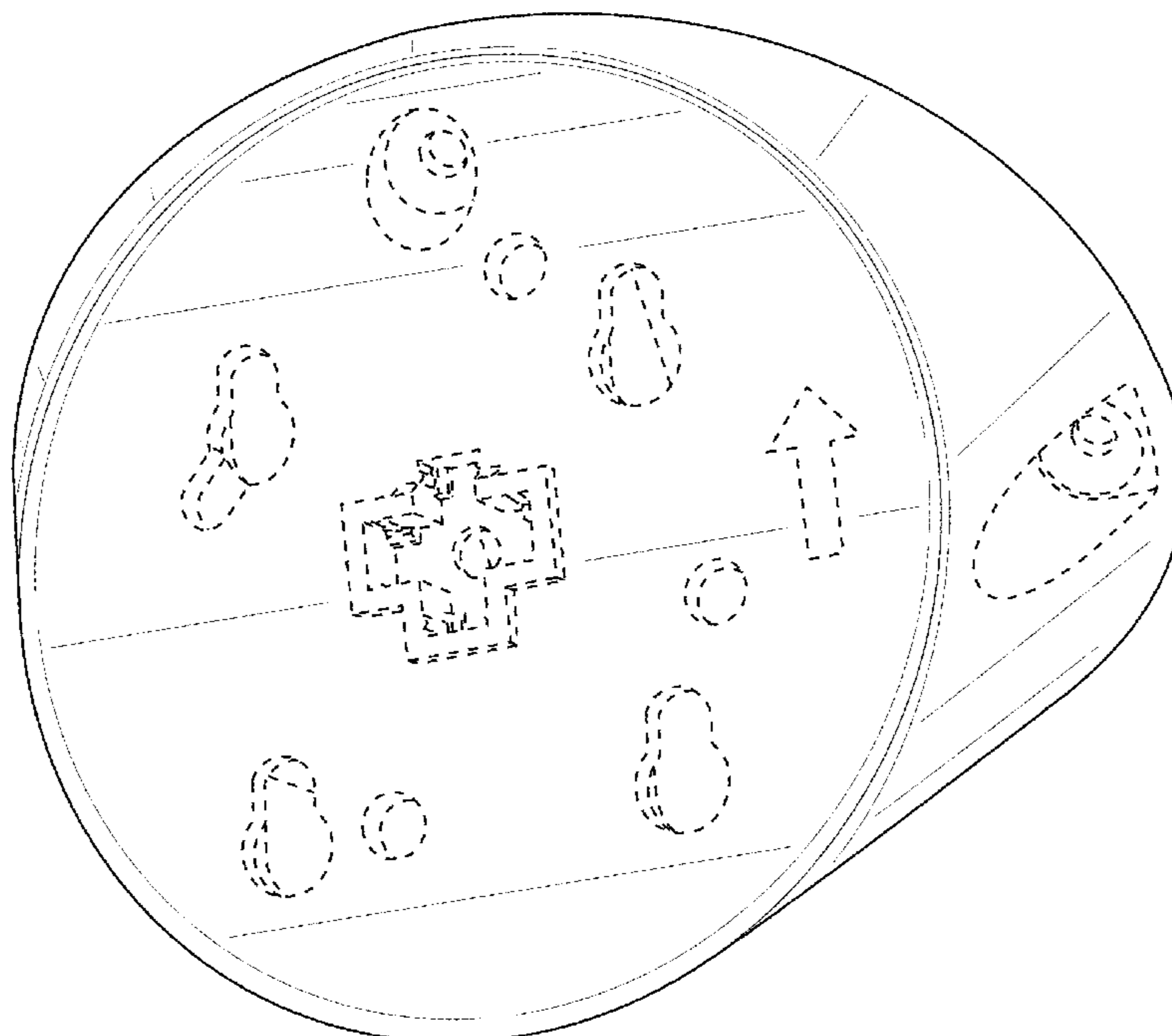
The ornamental design for a device mount, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of a device mount;
FIG. 2 is a front elevational view thereof;
FIG. 3 is a rear elevational view thereof;
FIG. 4 is a left-side elevational view thereof;
FIG. 5 is a right-side elevational view thereof;
FIG. 6 is a bottom plan view thereof; and,
FIG. 7 is a top plan view thereof.

The dashed broken lines depict portions of the device mount that form no part of the claimed design. The dot-dashed broken lines represent boundaries of the claimed device mount and form no part of the claimed design.

1 Claim, 7 Drawing Sheets



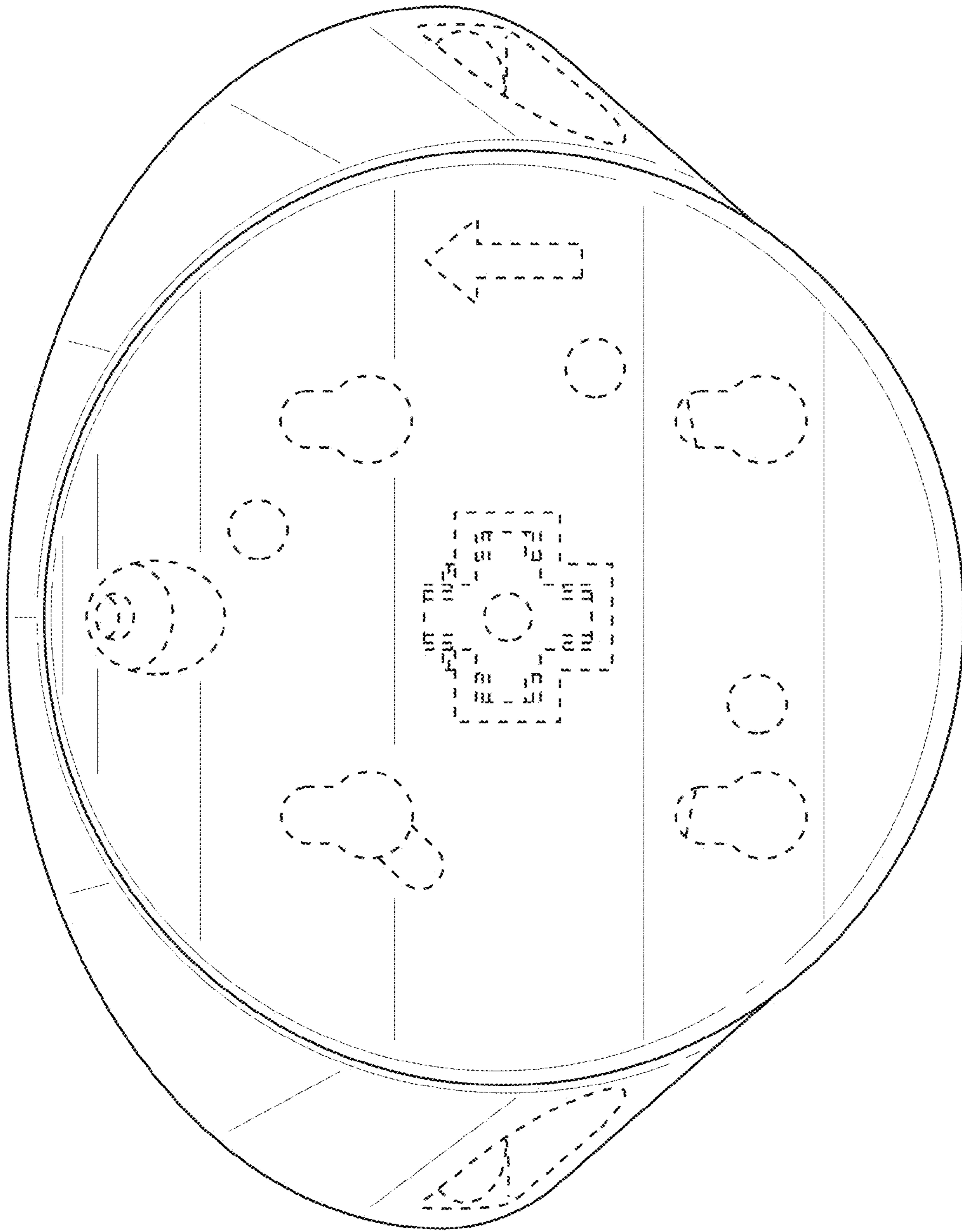


FIG. 2

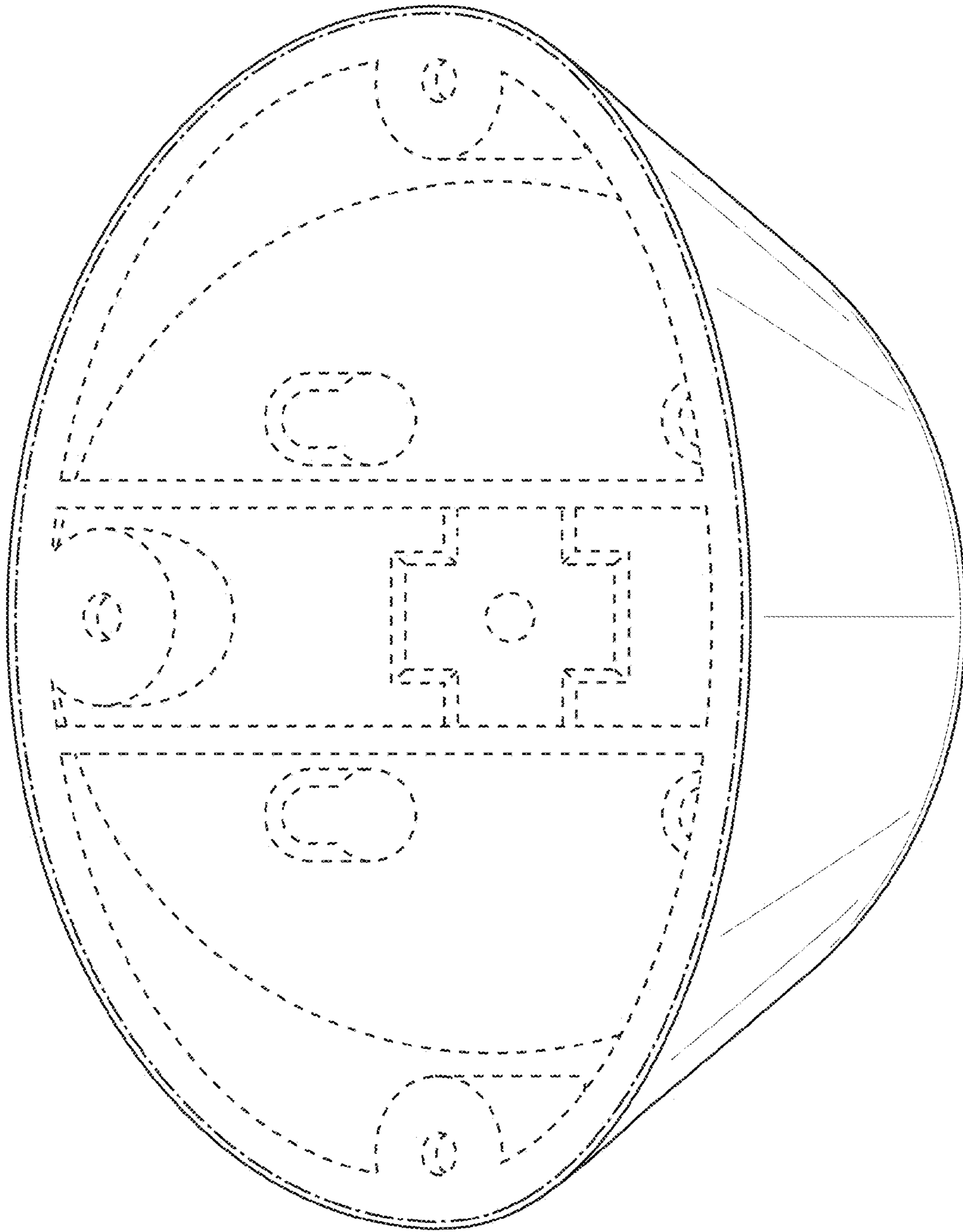


FIG. 3

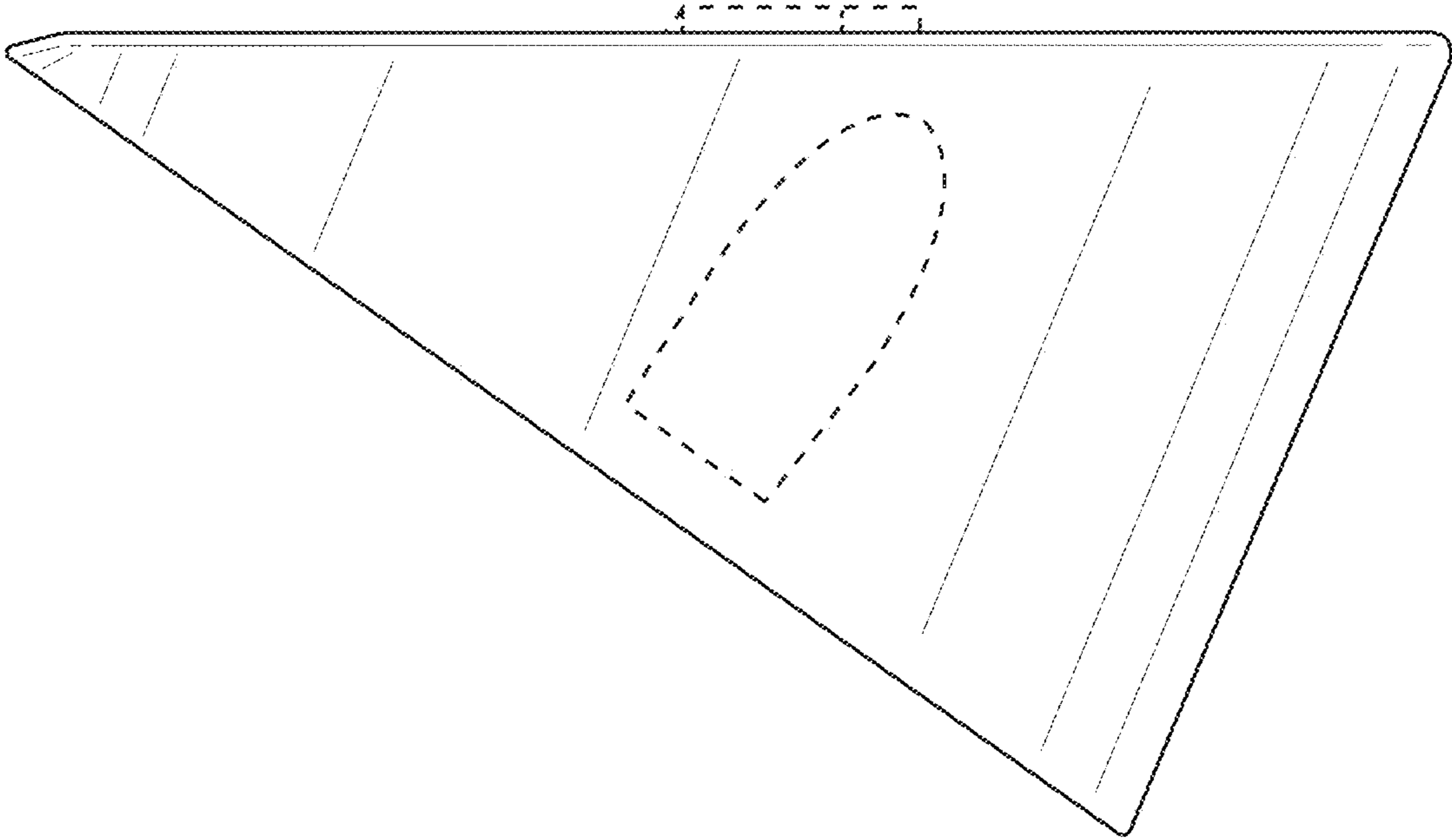


FIG. 4

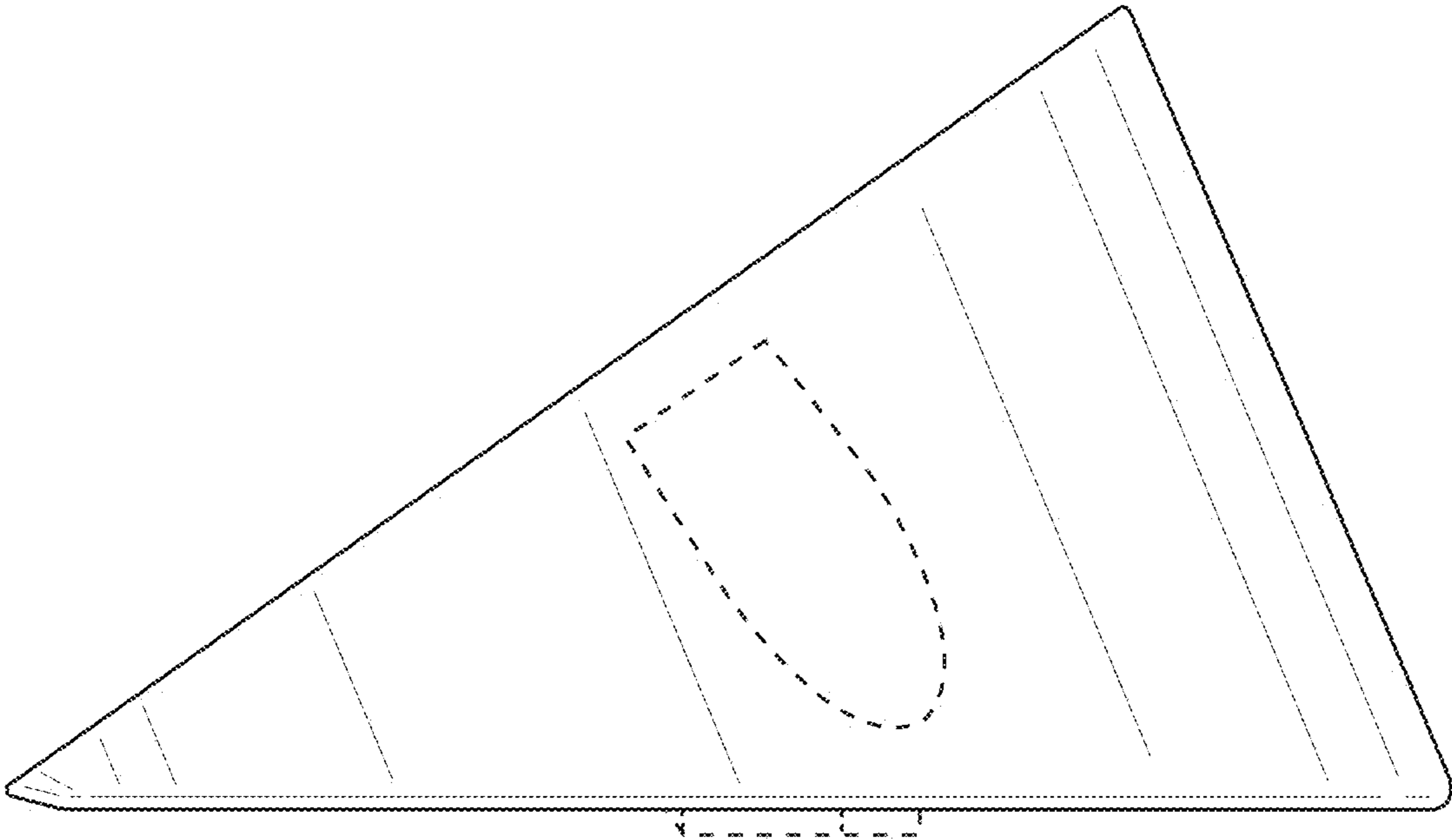


FIG. 5

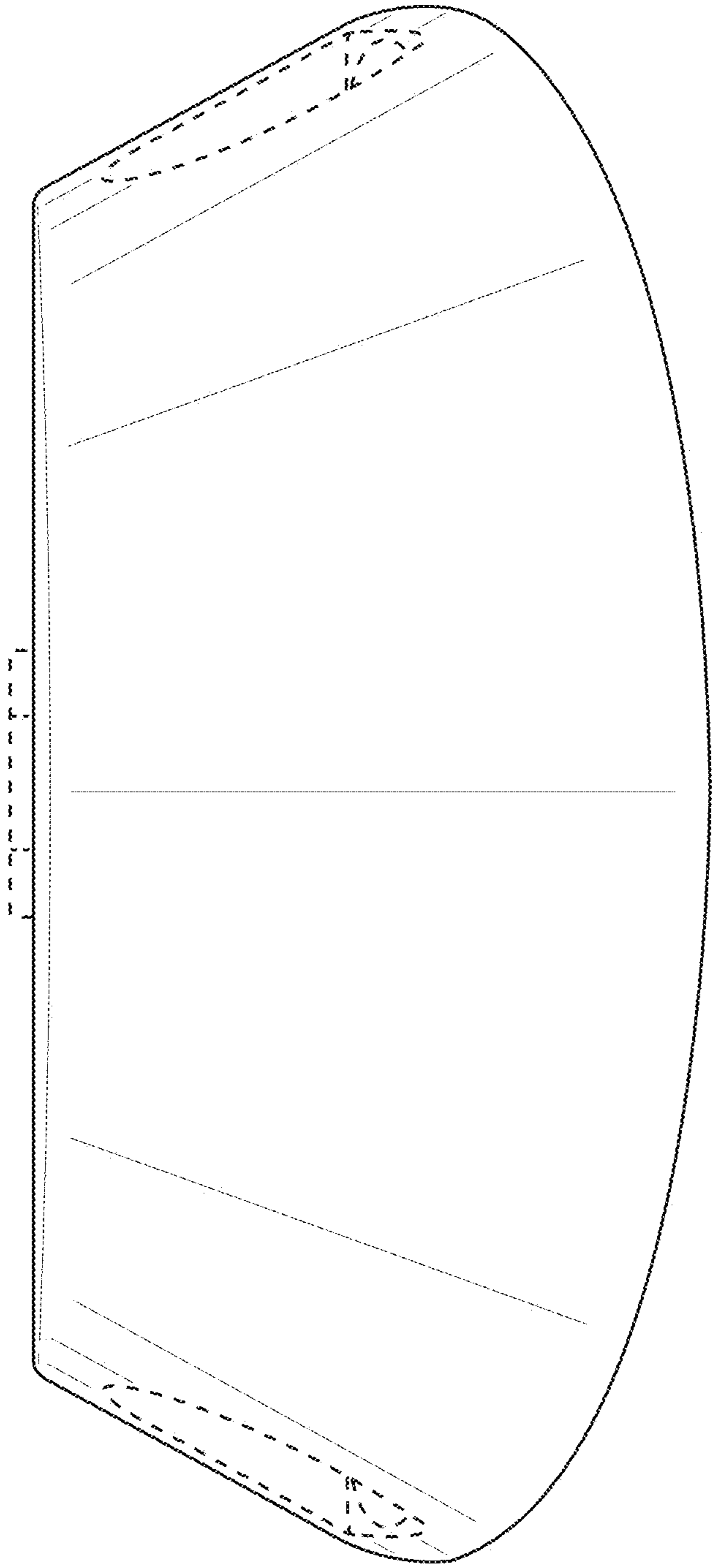


FIG. 6

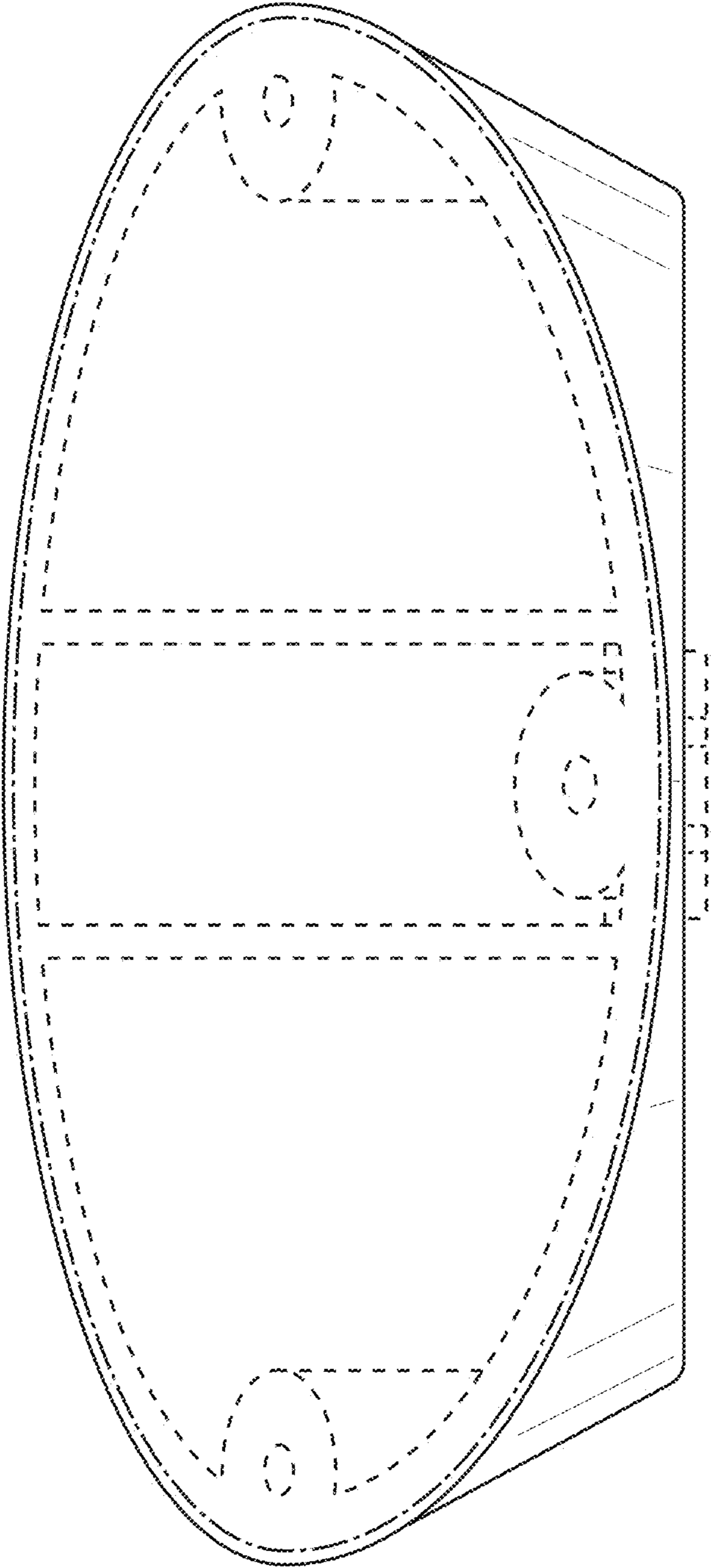


FIG. 7