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(12) **United States Design Patent** (10) **Patent No.:** **US D871,490 S**
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(54) **MOUNT FOR ELECTRONIC DEVICES**

(71) Applicant: **Innovelis, Inc.**, Sammamish, WA (US)

(72) Inventor: **Eric John Wengreen**, Sammamish, WA (US)

(73) Assignee: **Innovelis, Inc.**, Sammamish, WA (US)

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(52) **U.S. Cl.**

USPC **D16/242**; D14/253; D14/447

(58) **Field of Classification Search**

USPC D14/250–254, 217, 224, 224.1, 238.1, D14/299, 434, 447, 451–452, 483; D12/400, 415–426, 426.1, 114, 223; D16/242, 245; D10/74; D3/218; D13/107, 108; D8/363, 382

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See application file for complete search history.

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Primary Examiner — Jeffrey D Asch
Assistant Examiner — Rebekah A Caruso

(57) **CLAIM**

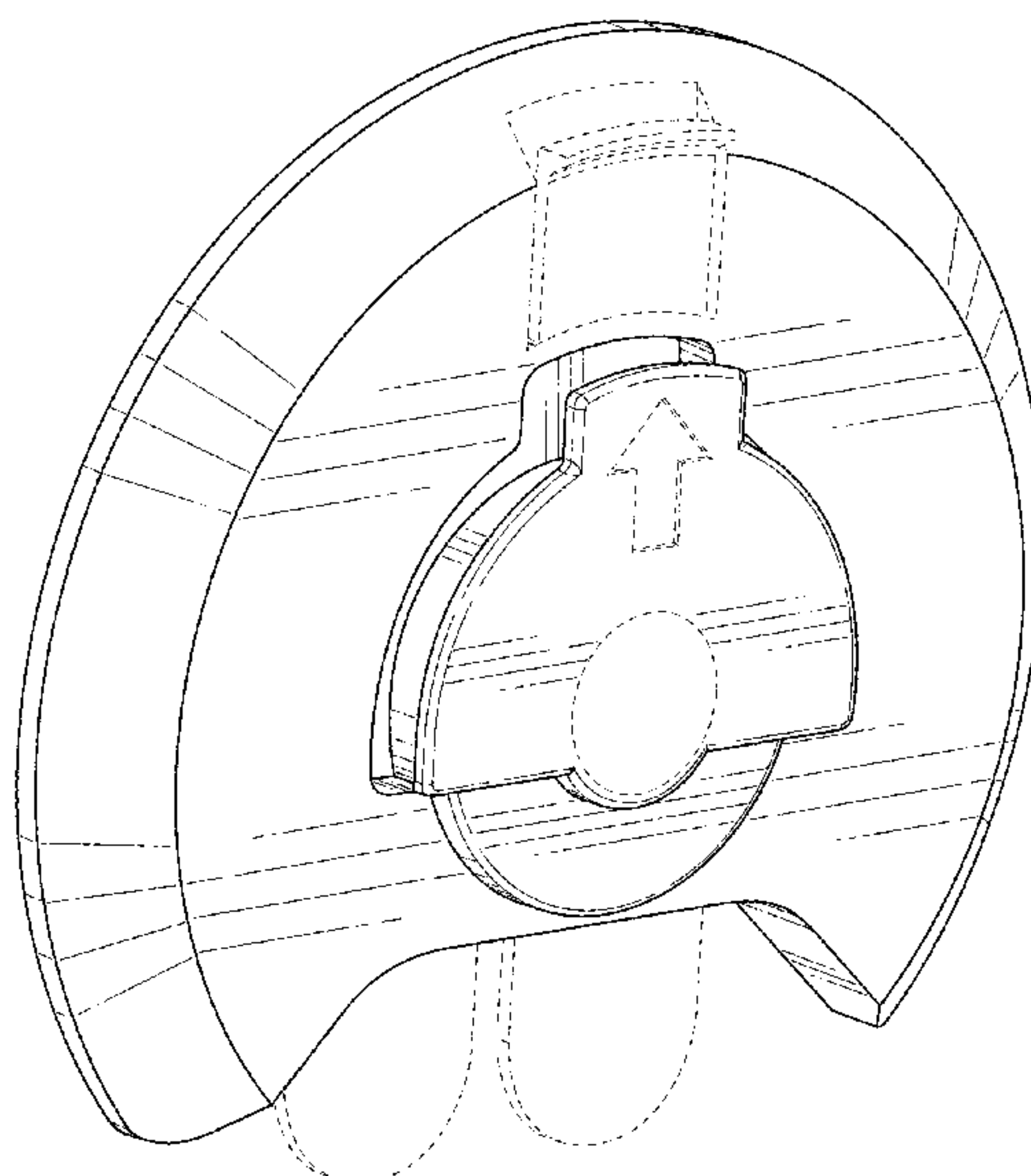
The ornamental design for a mount for electronic devices, as shown and described.

DESCRIPTION

FIG. 1 is a front, top, and first side perspective view of a mount for electronic devices embodying the new design; FIG. 2 is a front, bottom, and second side perspective view thereof; FIG. 3 is a back, bottom, and second side perspective view thereof; FIG. 4 is a front view thereof; FIG. 5 is a back view thereof; FIG. 6 is a second side view thereof; FIG. 7 is a first side view thereof; FIG. 8 is a top view thereof; and, FIG. 9 is a bottom view thereof.

The uniform repeating broken lines depict portions of a mount for electronic devices in which the claimed design is embodied and form no part of the claimed design.

1 Claim, 9 Drawing Sheets



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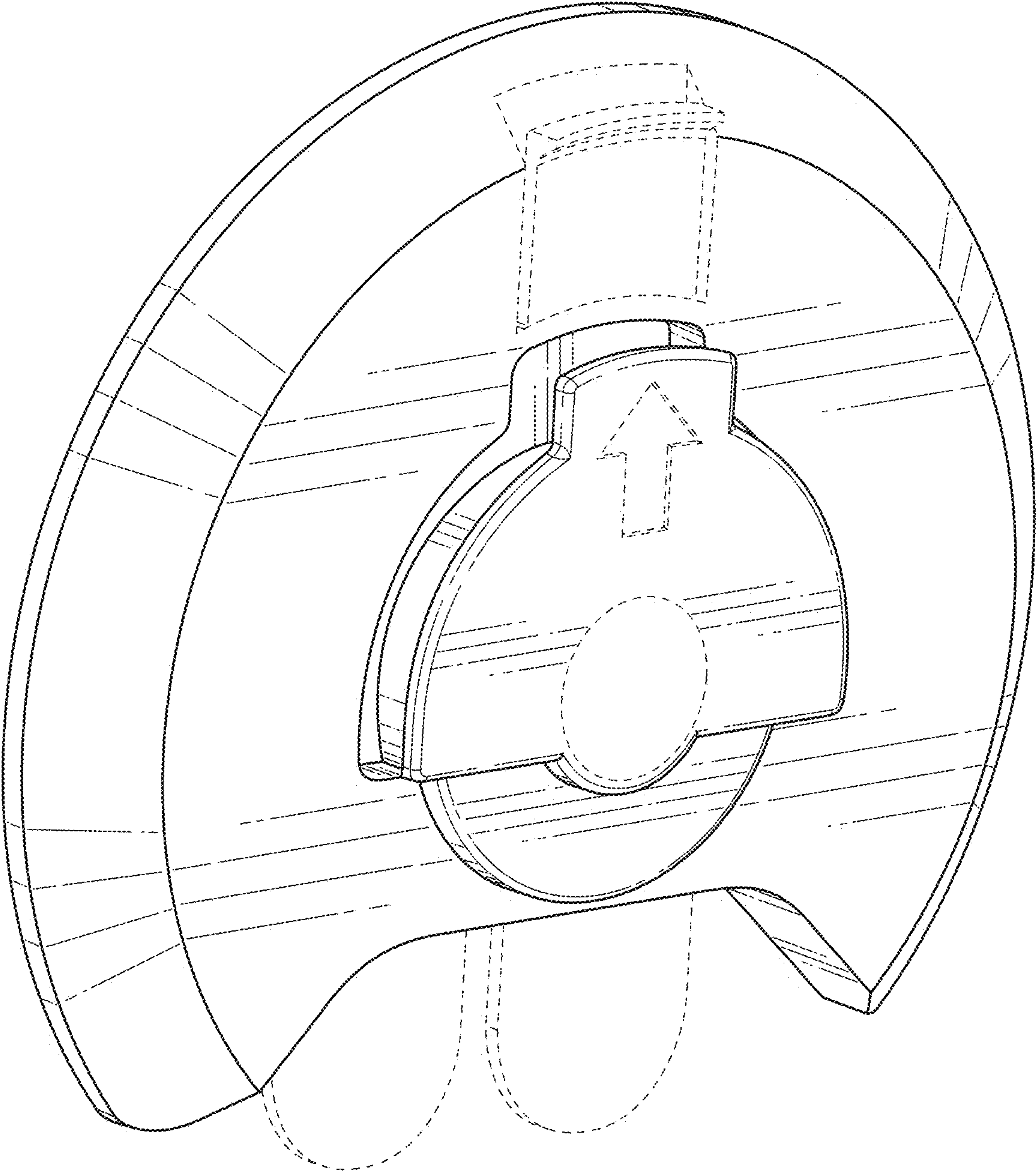


FIG. 1

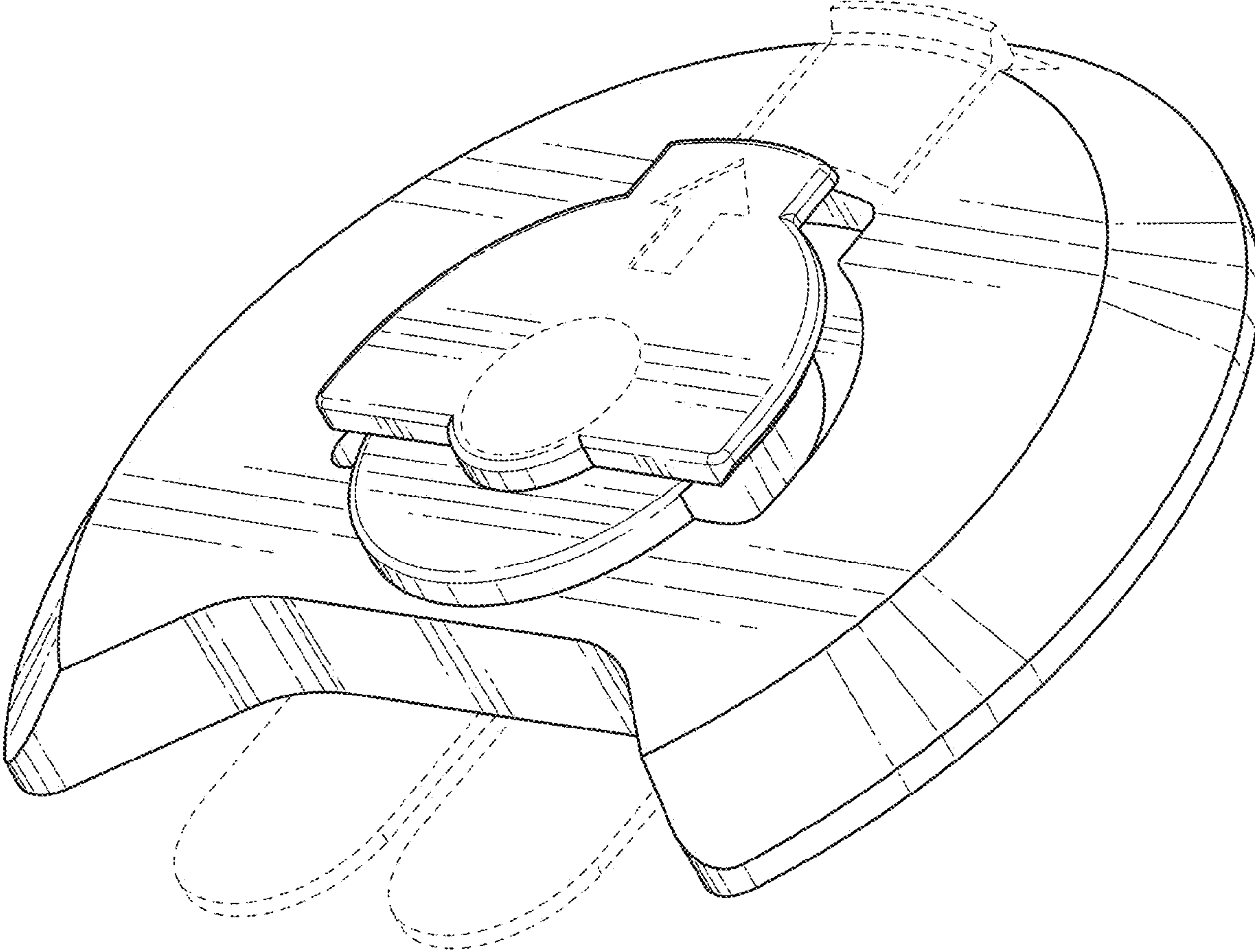


FIG. 2

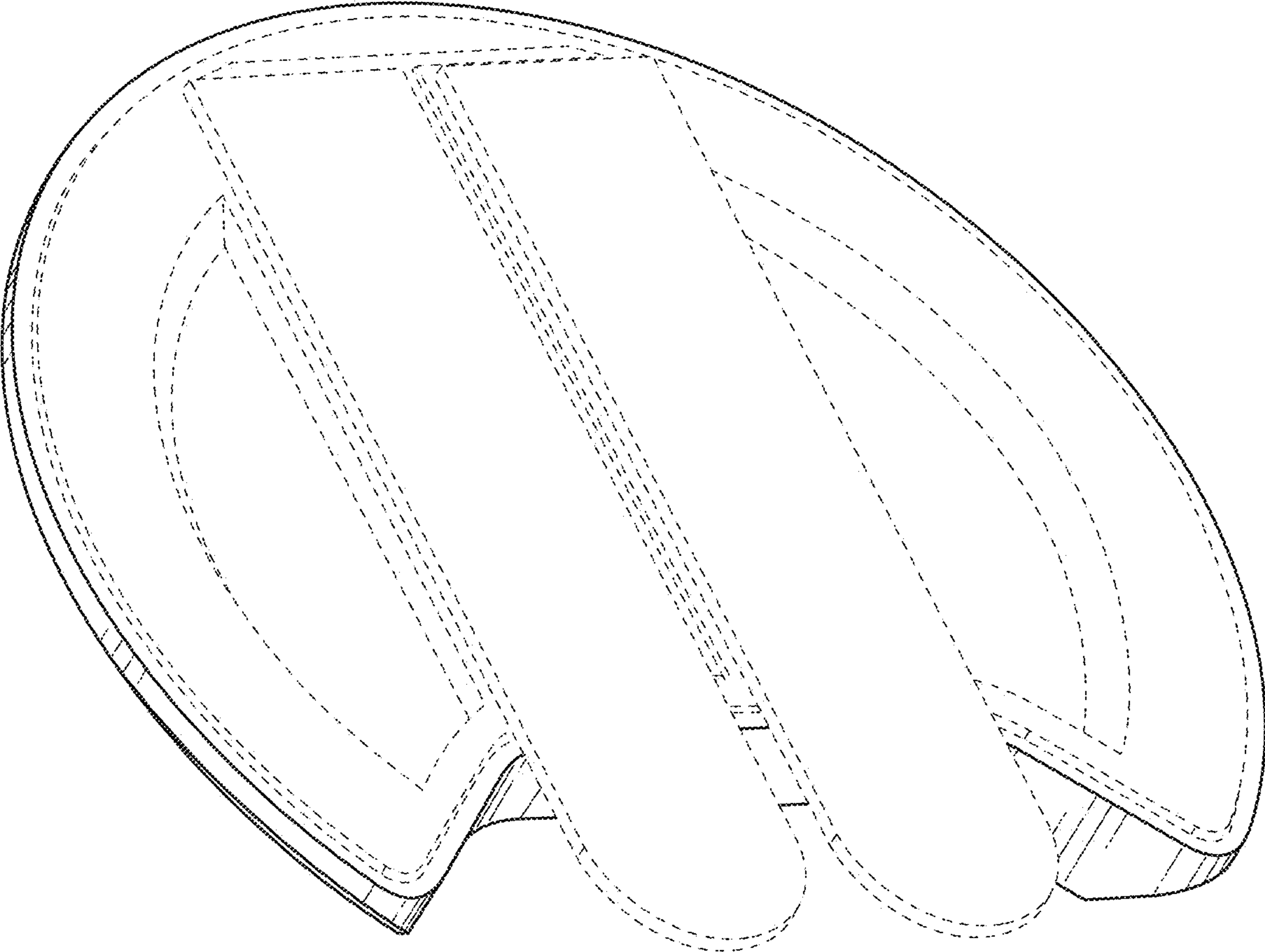


FIG. 3

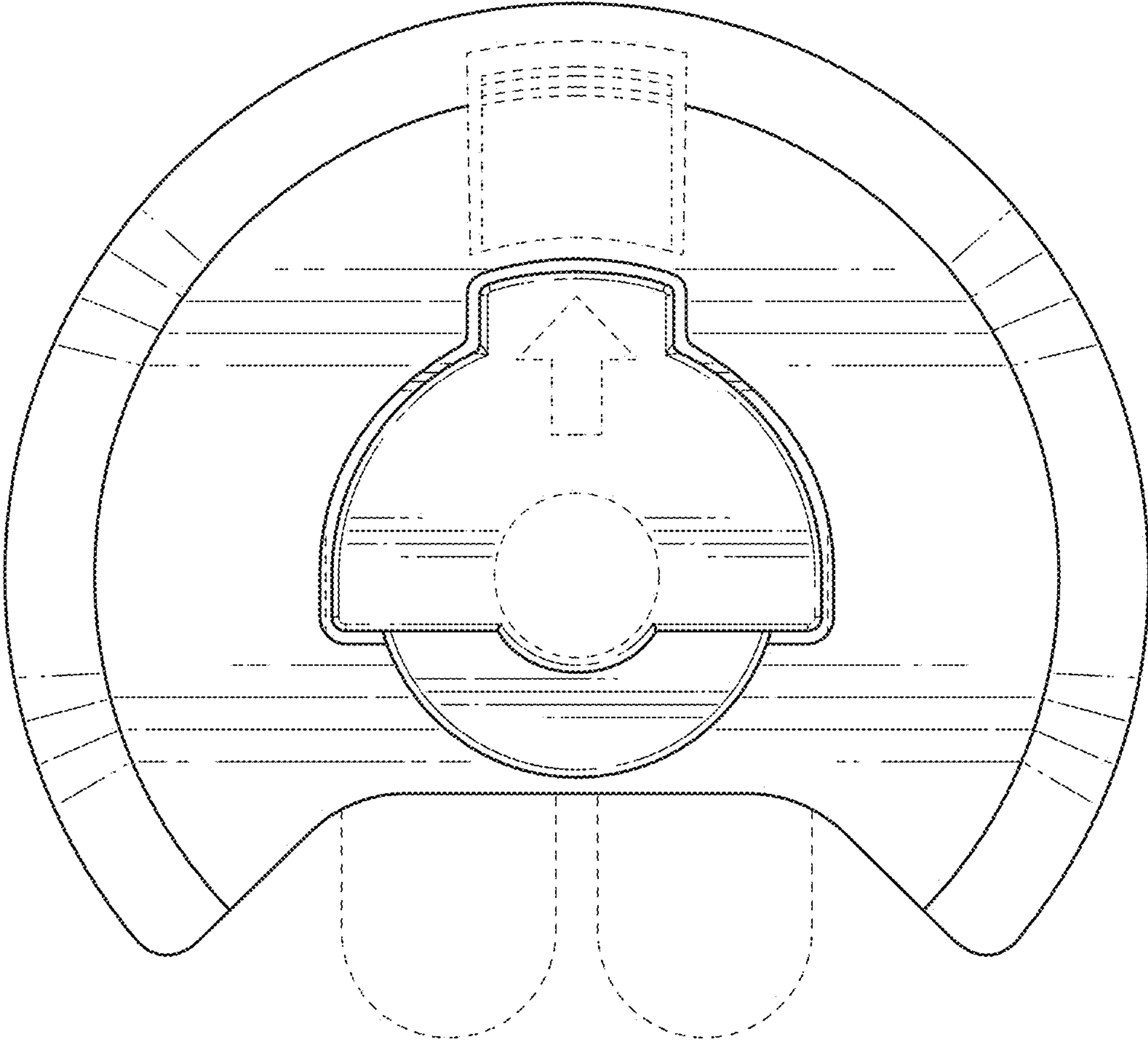


FIG. 4

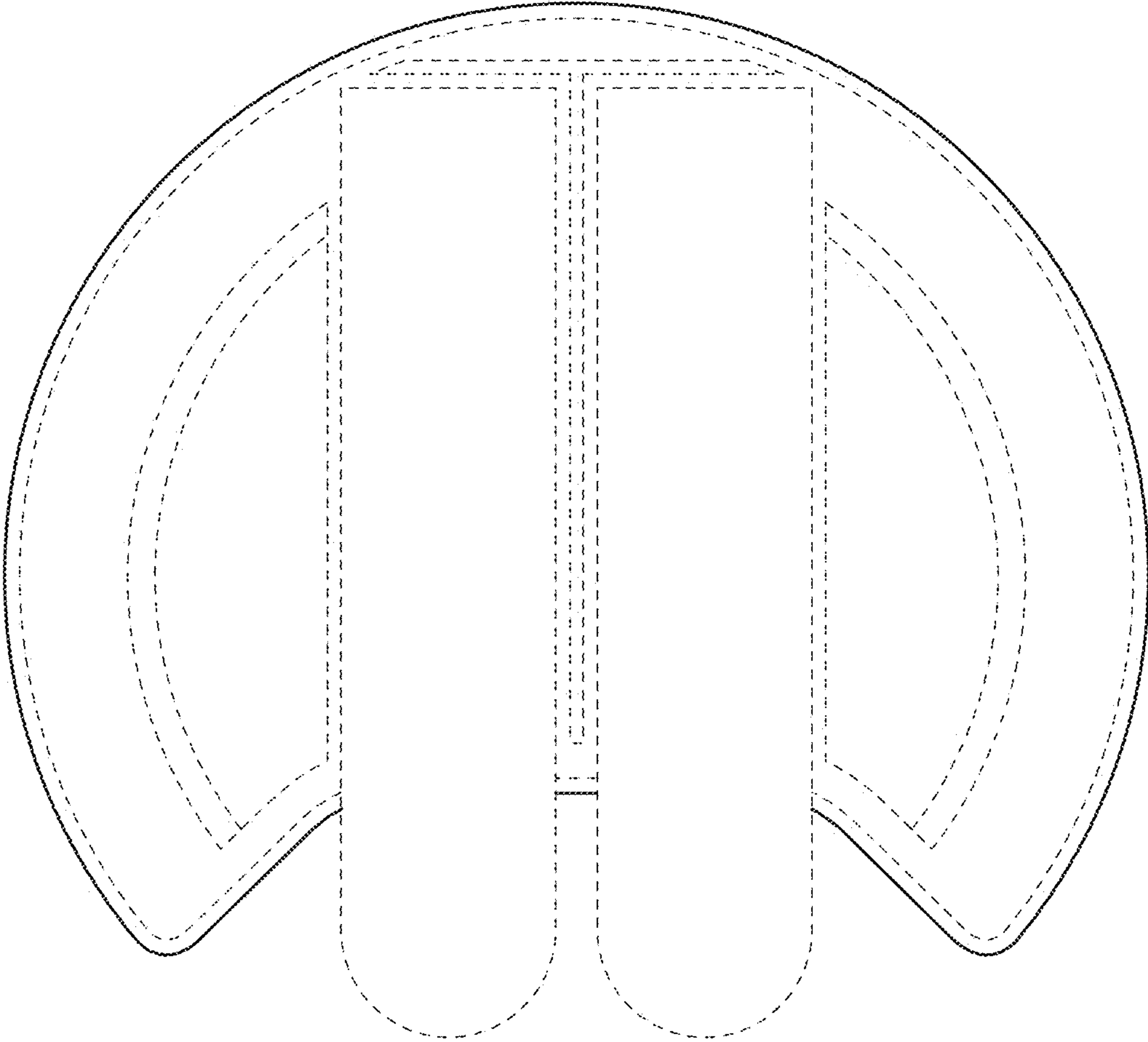


FIG. 5

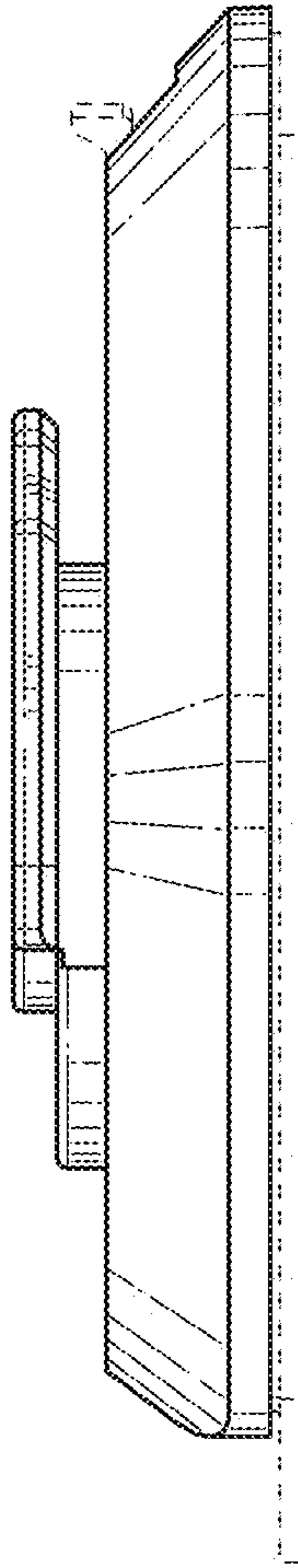


FIG. 6

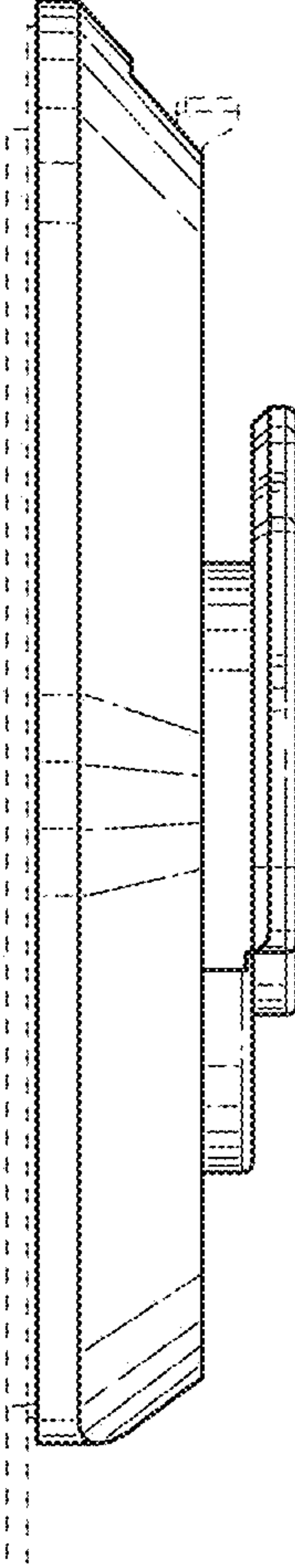


FIG. 7

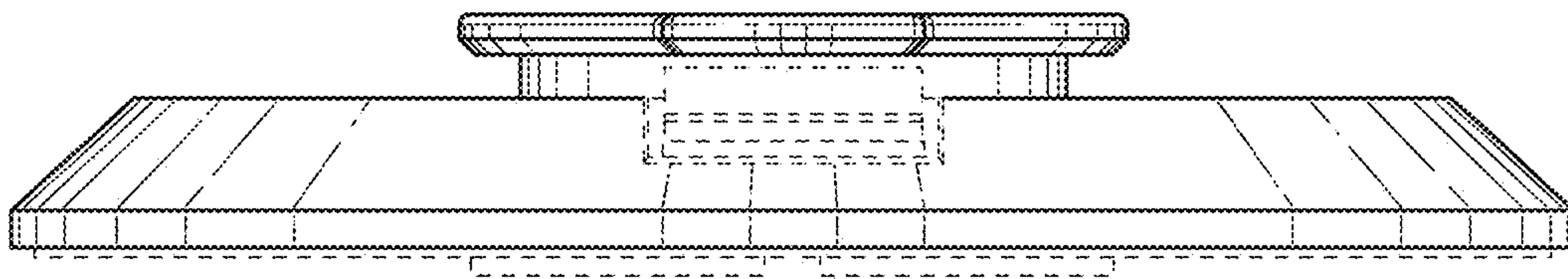


FIG. 8

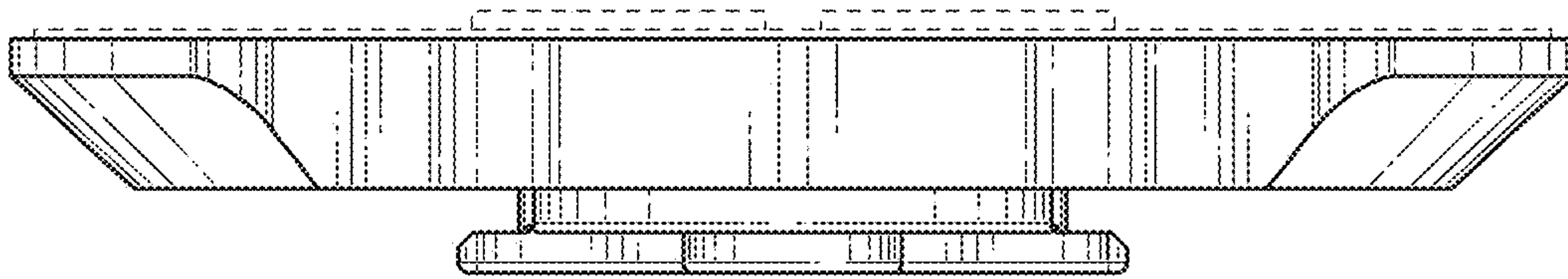


FIG. 9