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(12) **United States Design Patent** (10) **Patent No.:** **US D871,413 S**
Heiss et al. (45) **Date of Patent:** **** Dec. 31, 2019**

(54) **PROTECTIVE BUMPER DESIGN FOR ELECTRONIC DEVICES**

CPC G06K 9/00006; G06K 7/0013; G06K 9/00026; G06F 21/00; G06Q 30/00
See application file for complete search history.

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(**) Term: **15 Years**

* cited by examiner

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Related U.S. Application Data

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10, 2015, now Pat. No. Des. 797,746.

(51) **LOC (12) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/432**

(58) **Field of Classification Search**

USPC D14/300–302, 313, 314, 341, 348–370,
D14/383, 385, 432, 436, 496, 125, 135,
D14/134, 155, 167, 168, 230, 231, 233,
D14/235, 237, 240–242, 299; D10/65,
D10/75, 78; D13/103, 149, 162, 184,
D13/199; D18/4.4–4.6, 12.1, 56;
361/679.31–679.45, 752; 711/100, 115;
382/115, 124; 356/71; 235/380, 461,
235/382, 472.01, 472.02; D3/201, 273

(57) **CLAIM**

The ornamental design for a protective bumper design for
electronic devices, as shown and described.

DESCRIPTION

FIG. 1 is a bottom plan view of a corner bumper according
to an embodiment of our design.

FIG. 2 is a top plan view thereof.

FIG. 3 is a front plan view thereof.

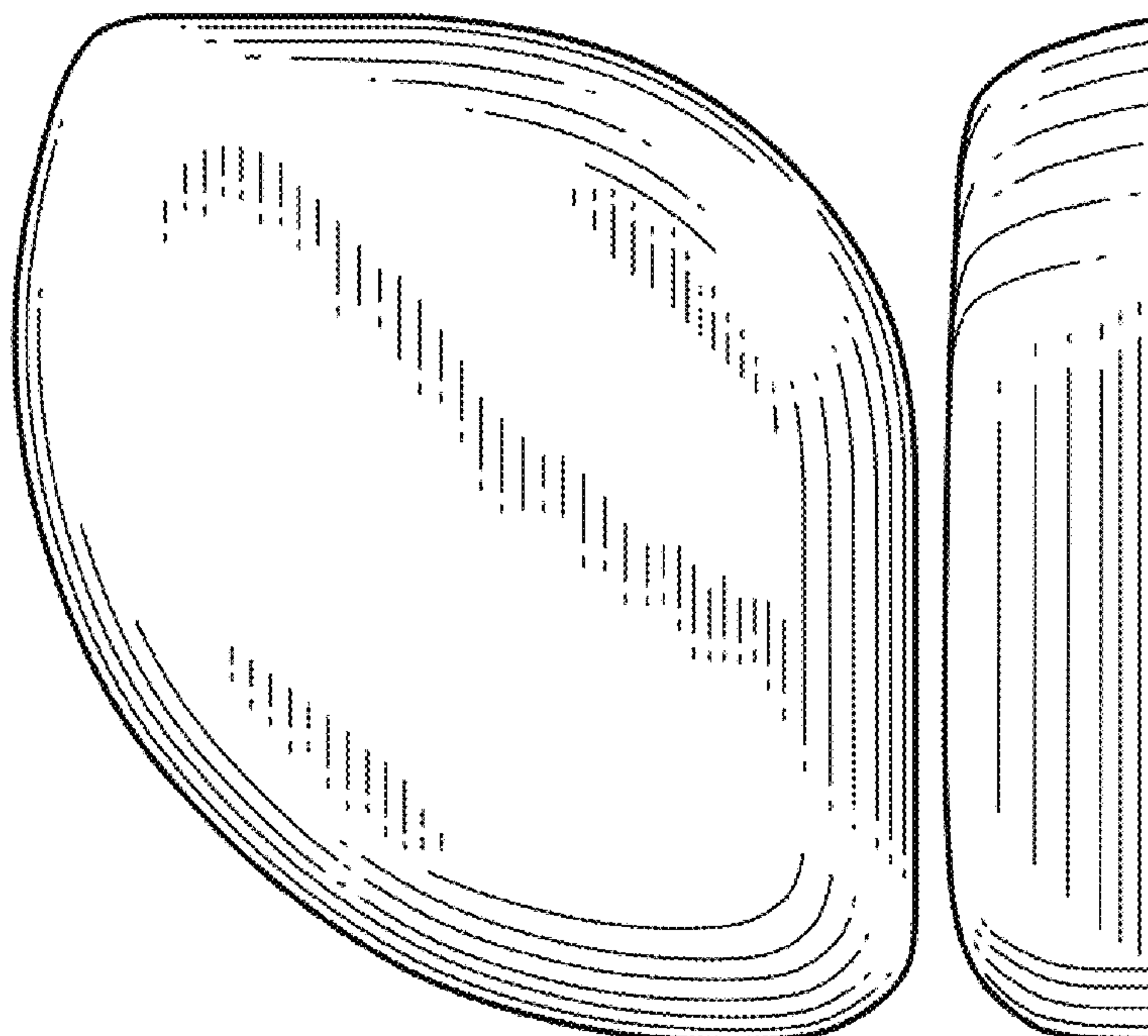
FIG. 4 is a back side plan view thereof.

FIG. 5 is a left side plan view thereof; and,

FIG. 6 is a right side plan view thereof.

Shading lines and stippling are used to show solid structures
and are not intended to indicate surface decoration.

1 Claim, 1 Drawing Sheet



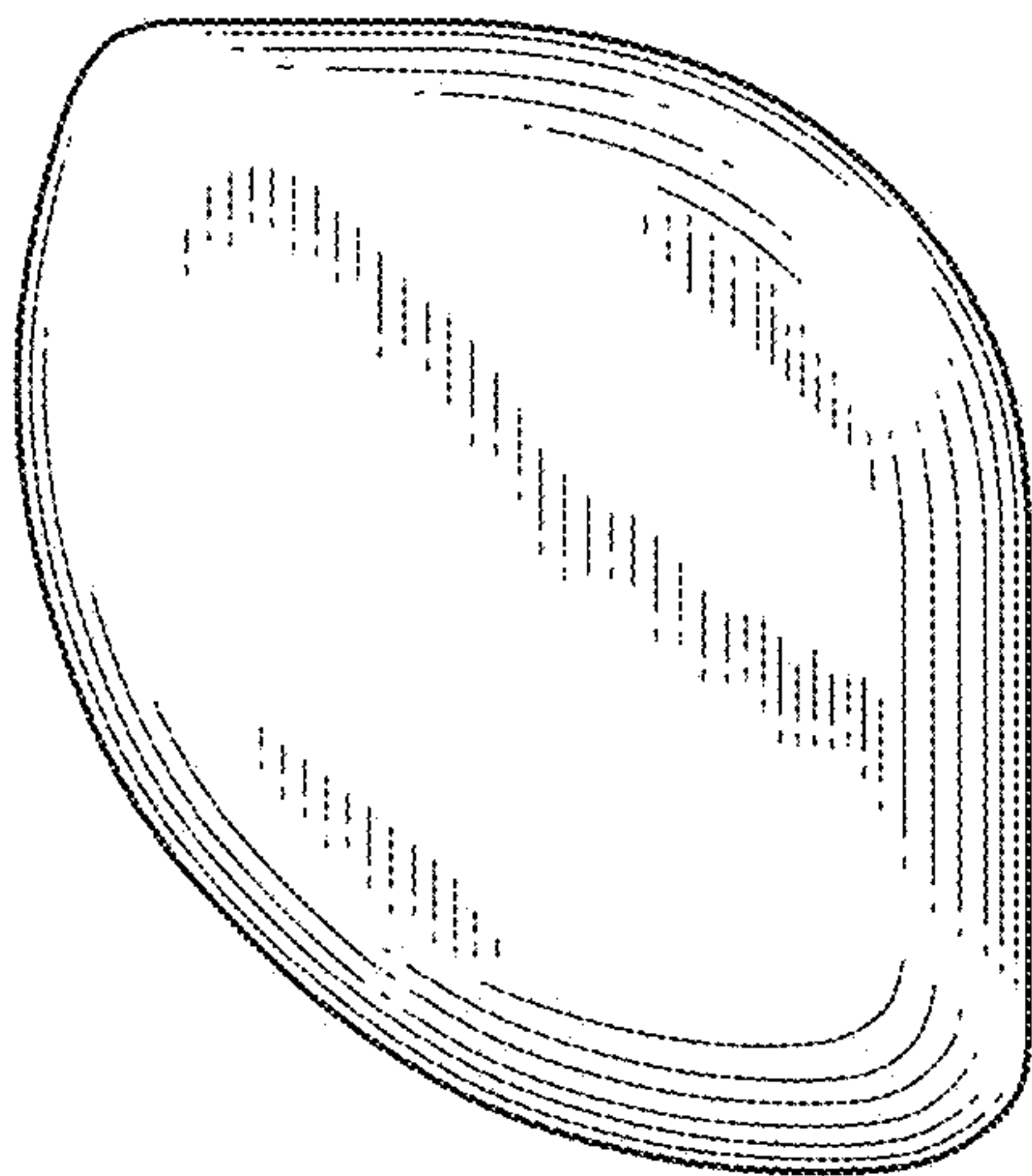


FIG. 1

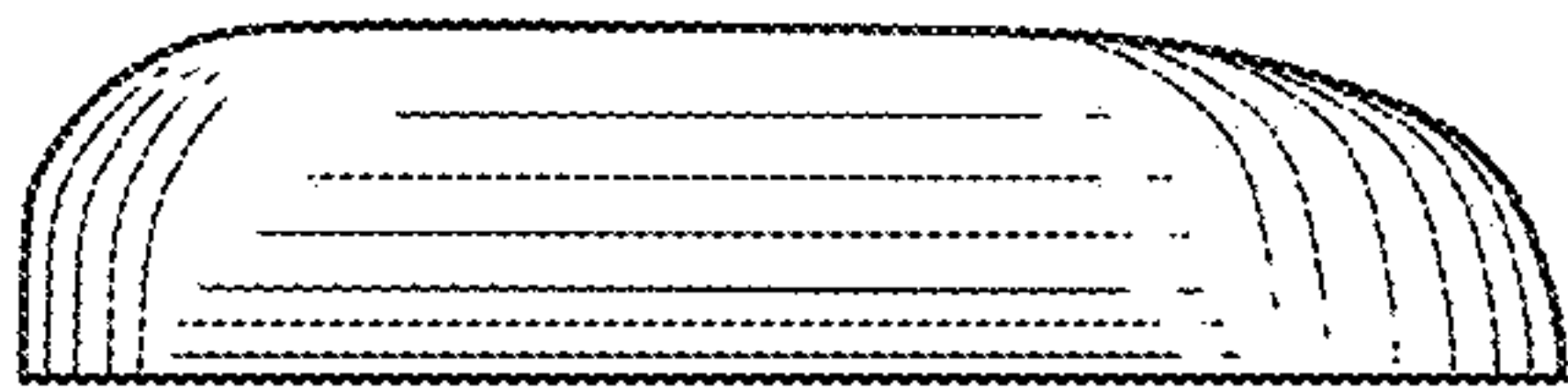


FIG. 3

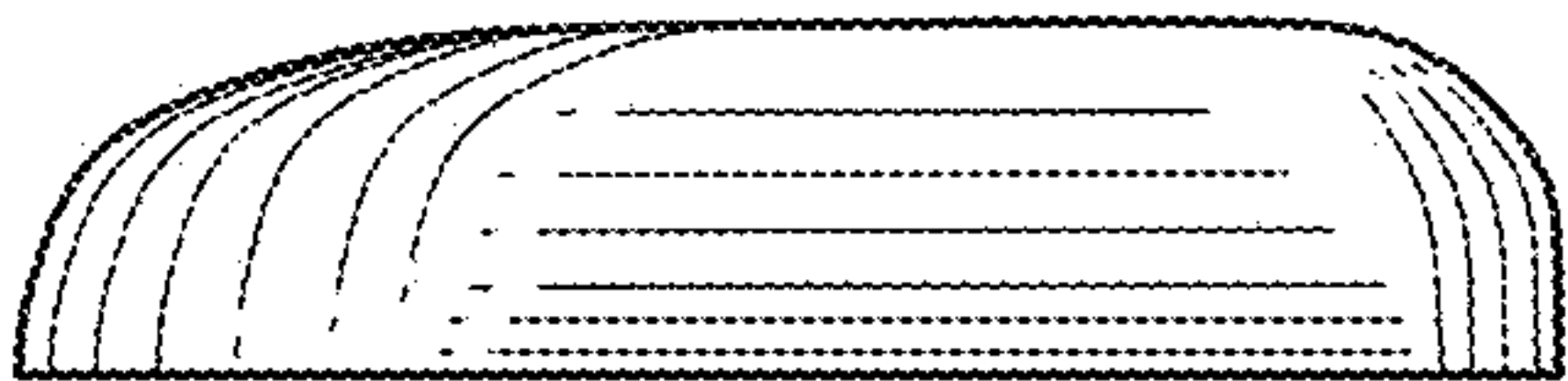


FIG. 4

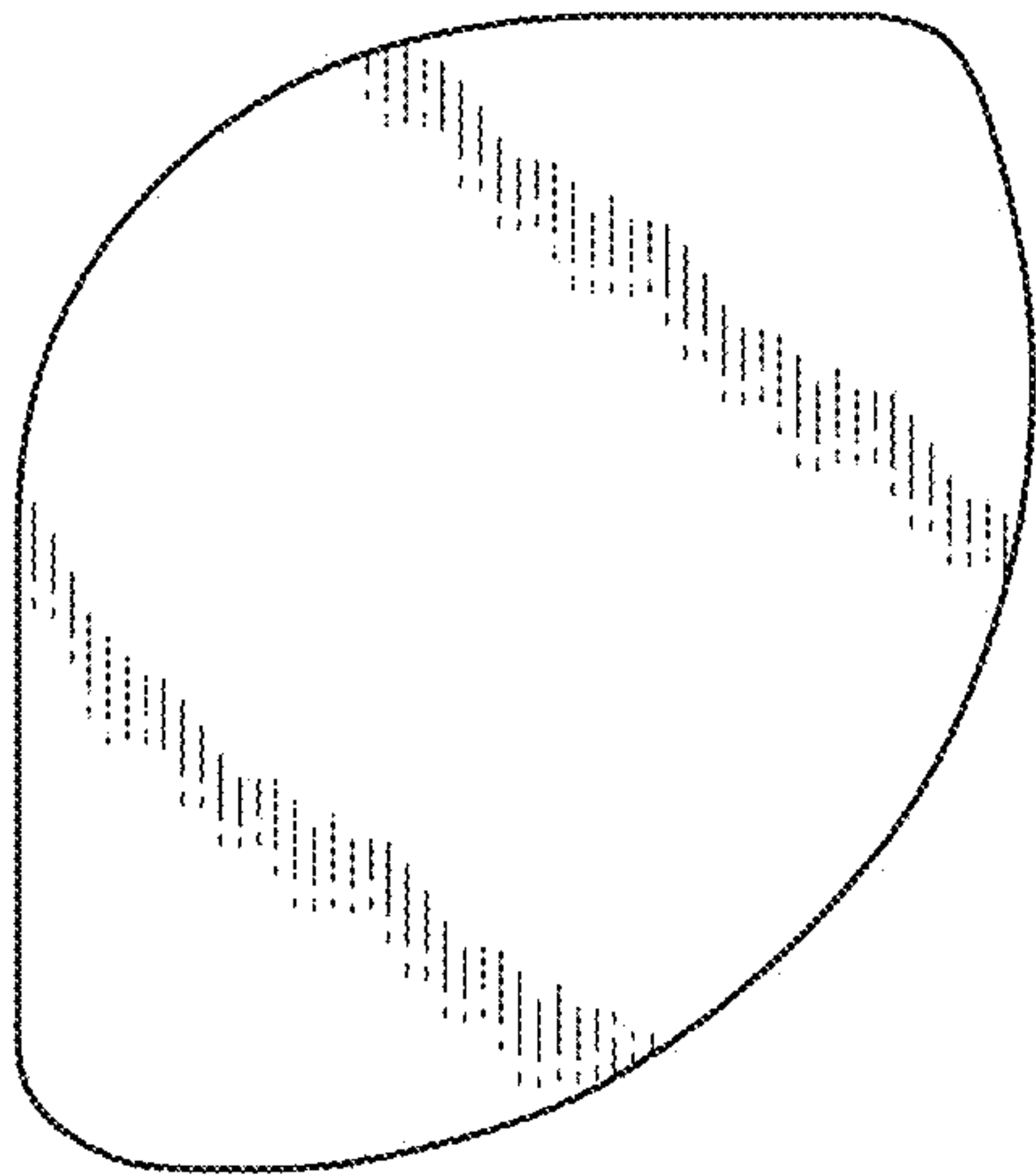


FIG. 2

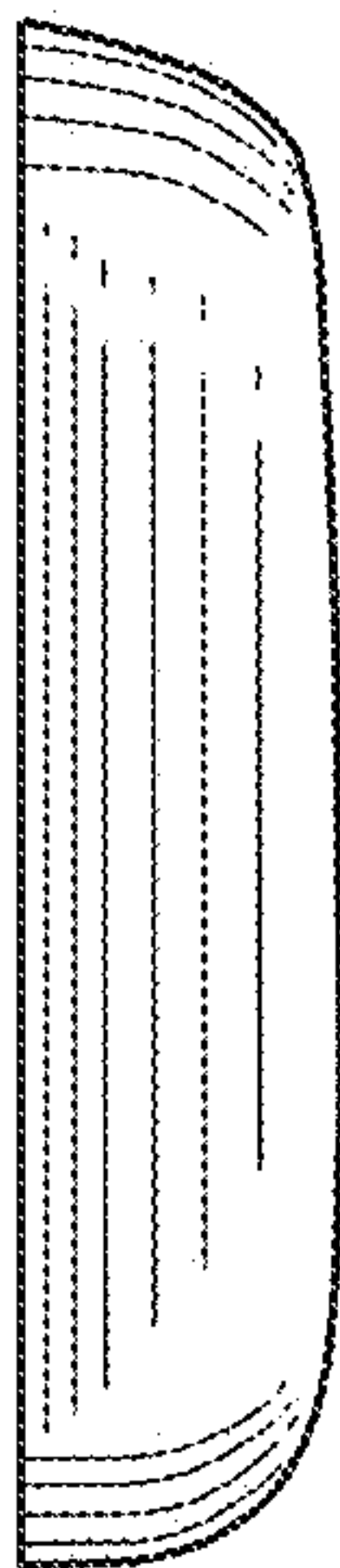


FIG. 5

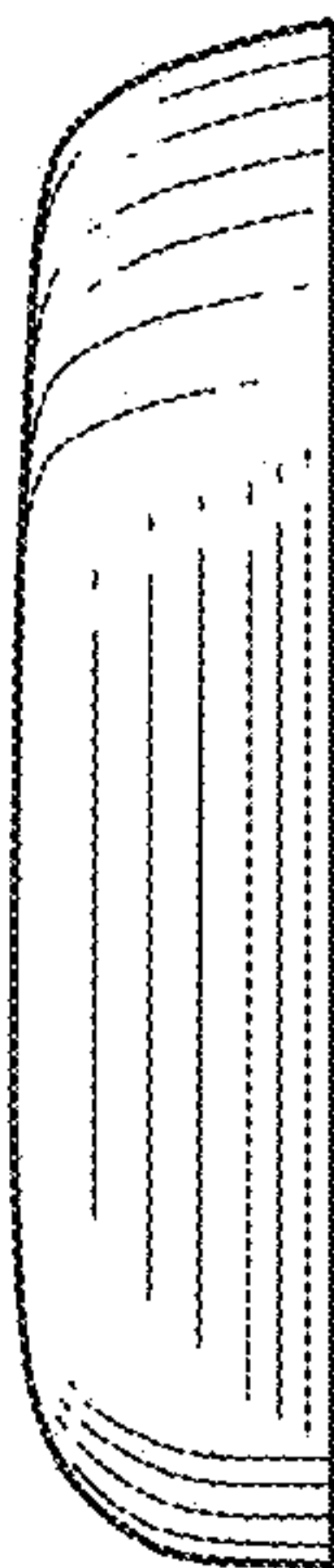


FIG. 6