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(12) **United States Design Patent**
Kobayashi

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(54) **DISPLAY MODULE**

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(73) Assignee: **SONY SEMICONDUCTOR SOLUTIONS CORPORATION**, Tokyo (JP)

(**) Term: **15 Years**

(21) Appl. No.: **29/553,148**

(22) Filed: **Jan. 28, 2016**

(51) **LOC (12) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/372**

(58) **Field of Classification Search**
USPC D16/219, 100, 330; D14/356–358, 361, D14/370, 372, 385, 388, 432–439, 447, D14/458, 474, 480.1, 483, 484.1, 217, D14/239, 299; D8/349, 354, 395
CPC G03B 17/02; G03B 19/04
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a display module, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a display module showing my new design;
FIG. 2 is a front elevational view thereof.
FIG. 3 is a rear elevational view thereof;
FIG. 4 is a left side elevational view thereof;
FIG. 5 is a right side elevational view thereof;
FIG. 6 is a top plan view thereof; and,
FIG. 7 is a bottom plan view thereof.
The broken lines showing of unclaimed portions of the display module form no part of the claimed design. The dot-dash broken lines define the boundaries of, but not included in, the claimed design.

1 Claim, 7 Drawing Sheets

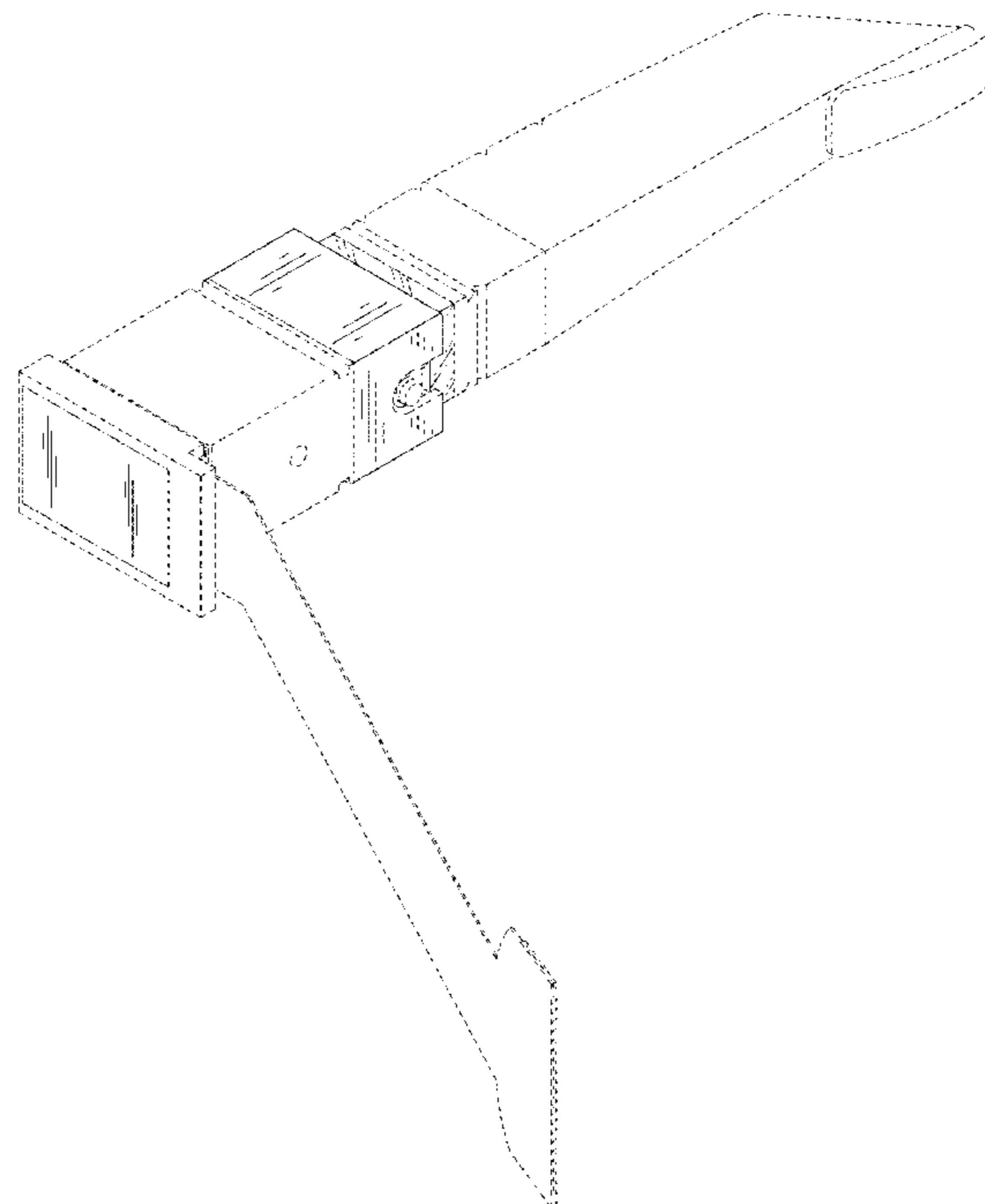


FIG. 1

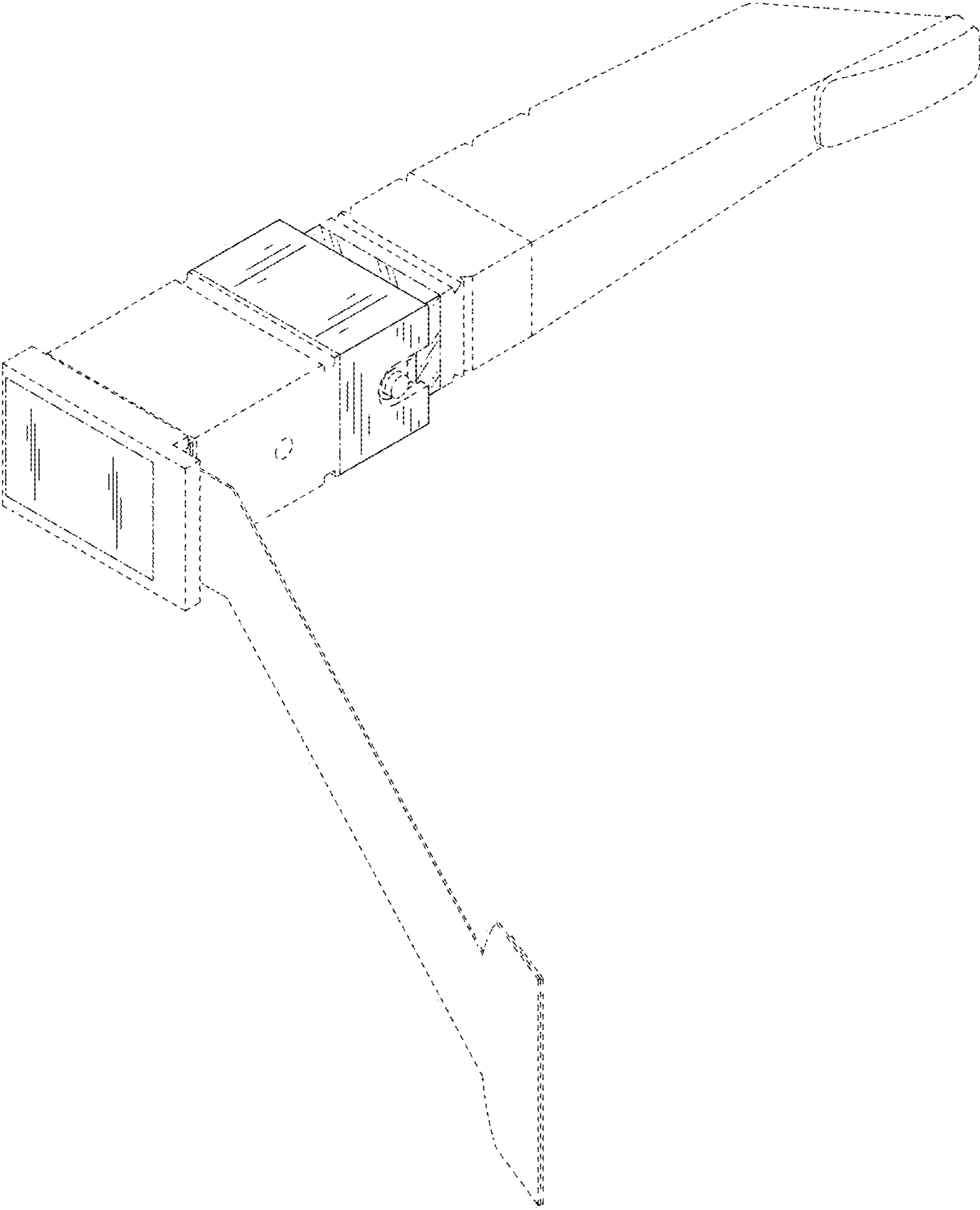


FIG. 2

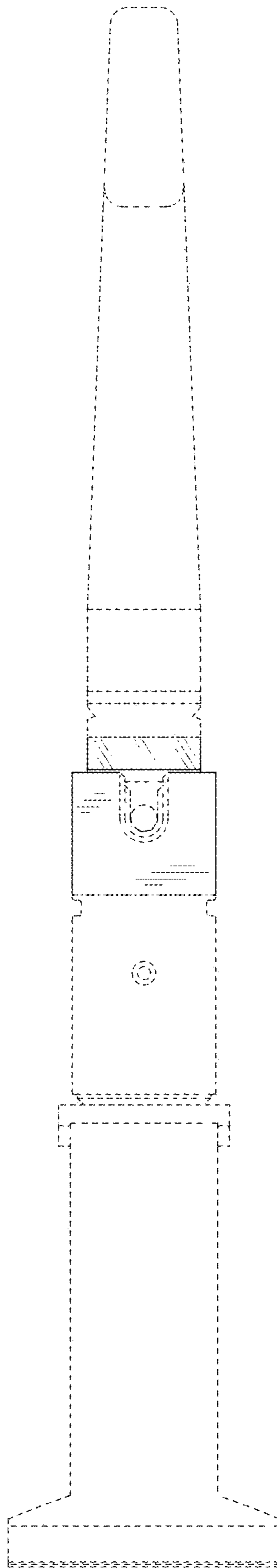


FIG. 3

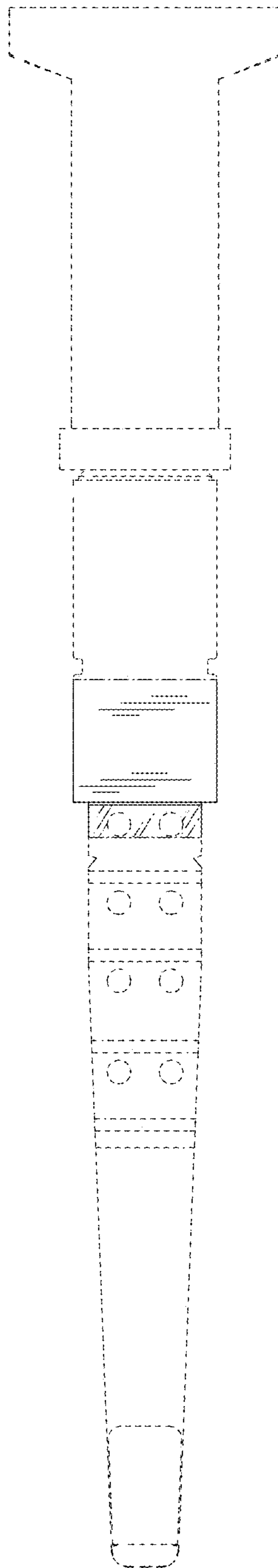


FIG. 4

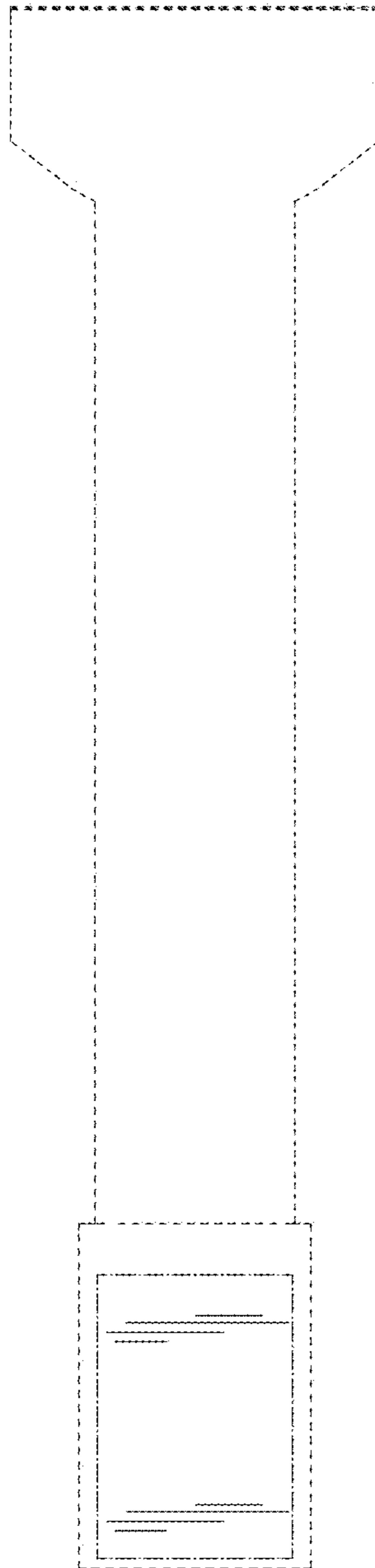
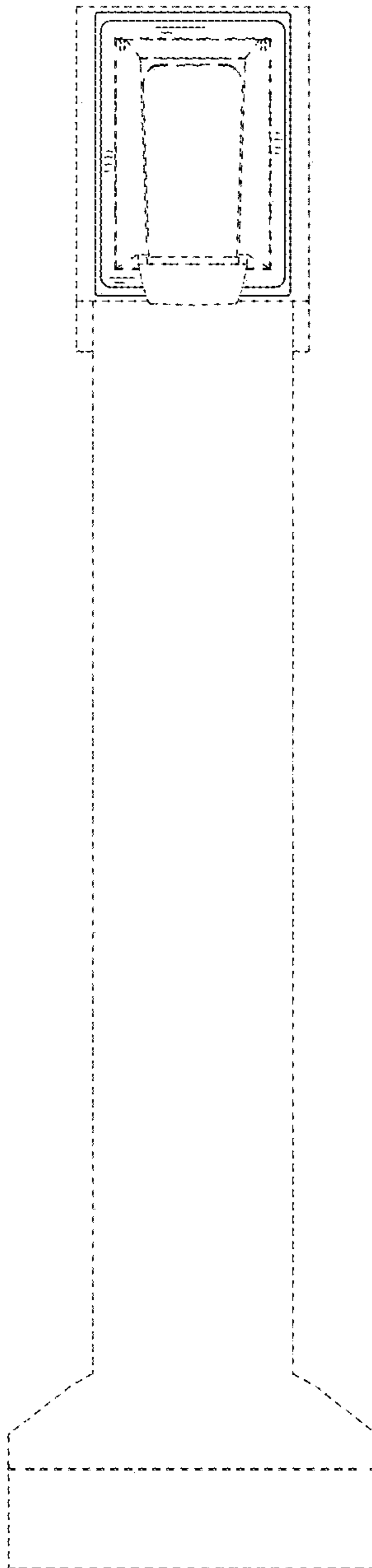


FIG. 5



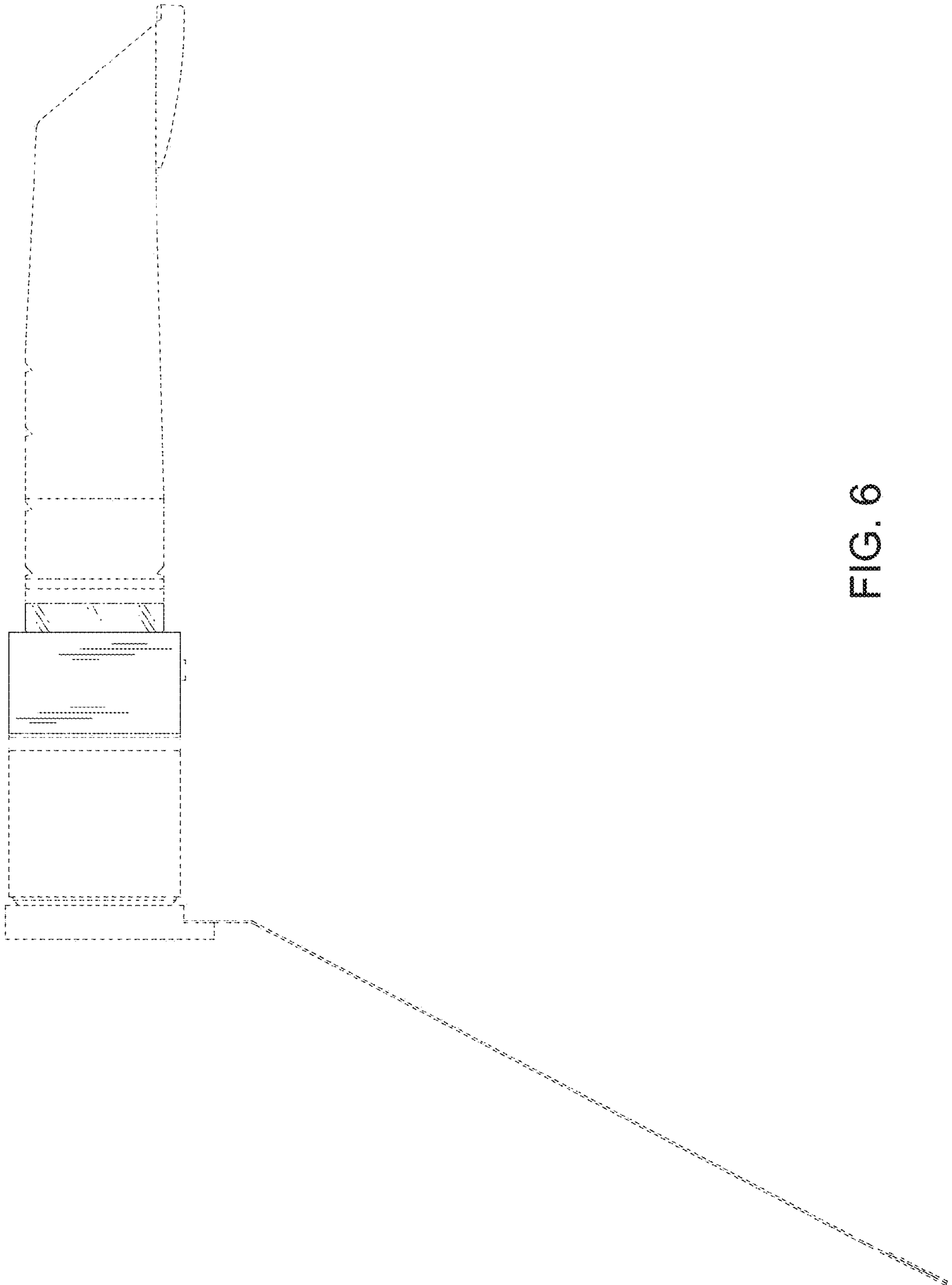


FIG. 7

