



US00D870119S

(12) **United States Design Patent** (10) **Patent No.:** **US D870,119 S**
Jun (45) **Date of Patent:** **** Dec. 17, 2019**

(54) **USB MEMORY**

D831,669 S * 10/2018 Shim D14/480.5
D836,647 S * 12/2018 Wei D14/480.7
D841,010 S * 2/2019 Kong D14/435.1

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FOREIGN PATENT DOCUMENTS

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TW D180297 S 12/2016

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OTHER PUBLICATIONS

(**) Term: **15 Years**

Communication dated Dec. 10, 2018, issued by the Taiwanese Intellectual Property Office in counterpart Taiwanese Patent Application No. 107304488.

(21) Appl. No.: **29/658,694**

* cited by examiner

(22) Filed: **Aug. 2, 2018**

(30) **Foreign Application Priority Data**

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Feb. 2, 2018 (KR) 30-2018-0005877

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(51) **LOC (12) Cl.** **14-03**

(57) **CLAIM**

(52) **U.S. Cl.**

The ornamental design for a USB memory, as shown and described.

USPC **D14/480.7**

DESCRIPTION

(58) **Field of Classification Search**

USPC D14/480.1–480.7, 435.1, 411, 433, 436,
D14/484.1, 137, 155, 203.1–203.8, 240,
D14/242; D13/133, 154

CPC H05K 5/0278; G06K 19/07732
See application file for complete search history.

FIG. 1 is a perspective view of a universal serial bus (USB) memory showing our new design.

FIG. 2 is a front elevation view thereof.

FIG. 3 is a rear elevation view thereof.

FIG. 4 is a left side elevation view thereof.

FIG. 5 is a right side elevation view thereof.

FIG. 6 is a top plan view thereof.

FIG. 7 is a bottom plan view thereof; and,

FIG. 8 is a perspective view of USB memory shown in an example environment.

The features of the USB memory shown in broken lines in FIG. 7 form no part of the claimed design.

The features shown in broken lines in FIG. 8 show environment only and form no part of the claimed design.

(56) **References Cited**

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D798,308 S * 9/2017 Tzeng D14/480.5
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1 Claim, 7 Drawing Sheets

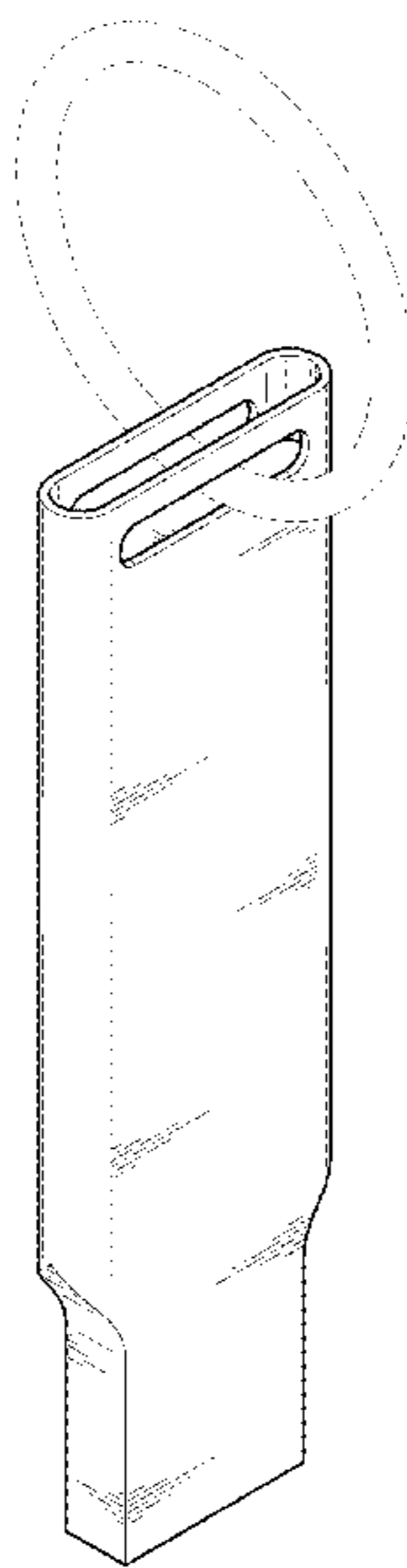


FIG. 1

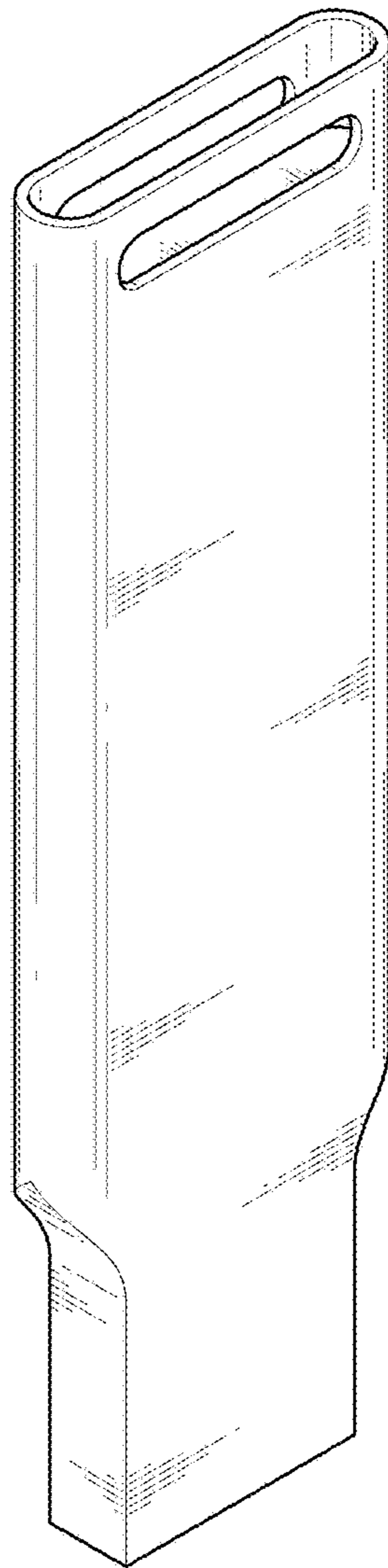


FIG. 2

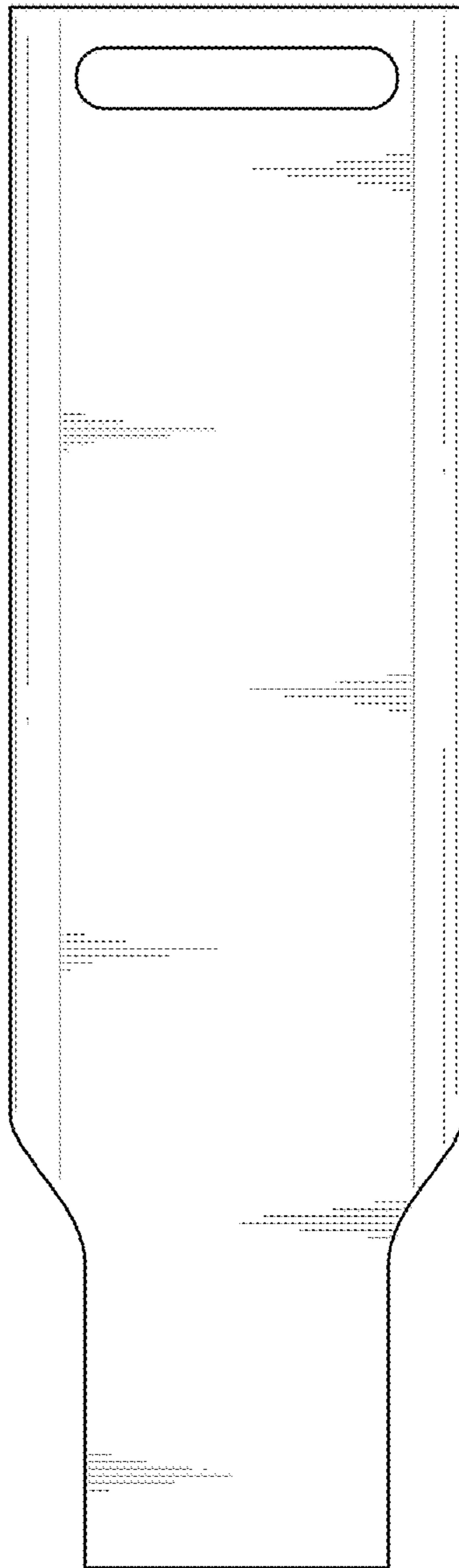


FIG. 3

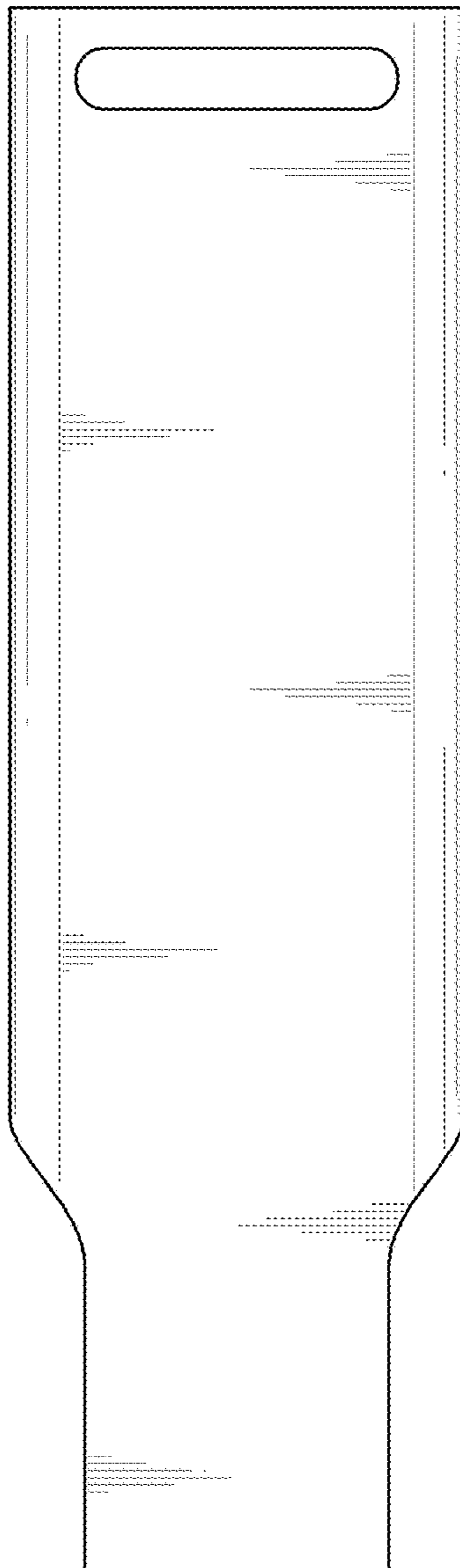


FIG. 4

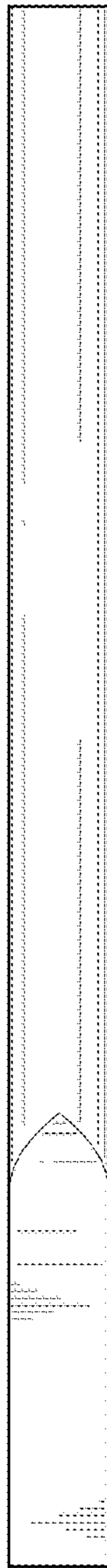


FIG. 5

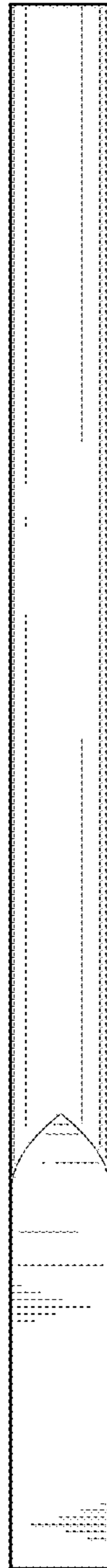


FIG. 6

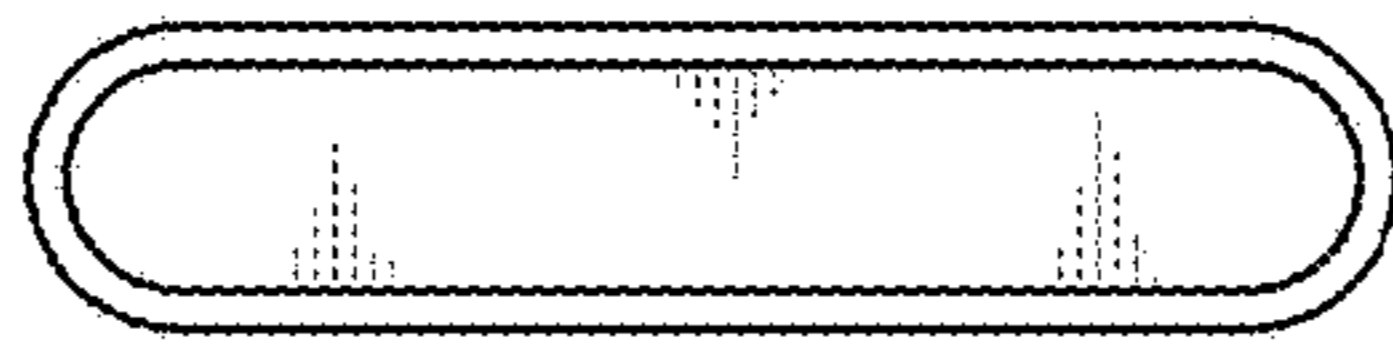


FIG. 7



FIG. 8

