



US00D869404S

(12) **United States Design Patent**
Ueda et al.

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(45) **Date of Patent:** **** Dec. 10, 2019**

(54) **GATEWAY**

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(73) Assignee: **OMRON Corporation**, Kyoto (JP)

(**) Term: **15 Years**

(21) Appl. No.: **29/645,842**

(22) Filed: **Apr. 30, 2018**

(30) **Foreign Application Priority Data**

Nov. 24, 2017 (JP) 2017-026209

(51) **LOC (12) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/162.1**

(58) **Field of Classification Search**
USPC D13/123, 162, 162.1, 184
CPC G05B 9/02; G05B 19/05; G05B 19/054;
G05B 19/056; G06F 1/182; G06F 1/183;
H05K 7/1462; H05K 7/1467; H05K
7/1474

See application file for complete search history.

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(Continued)

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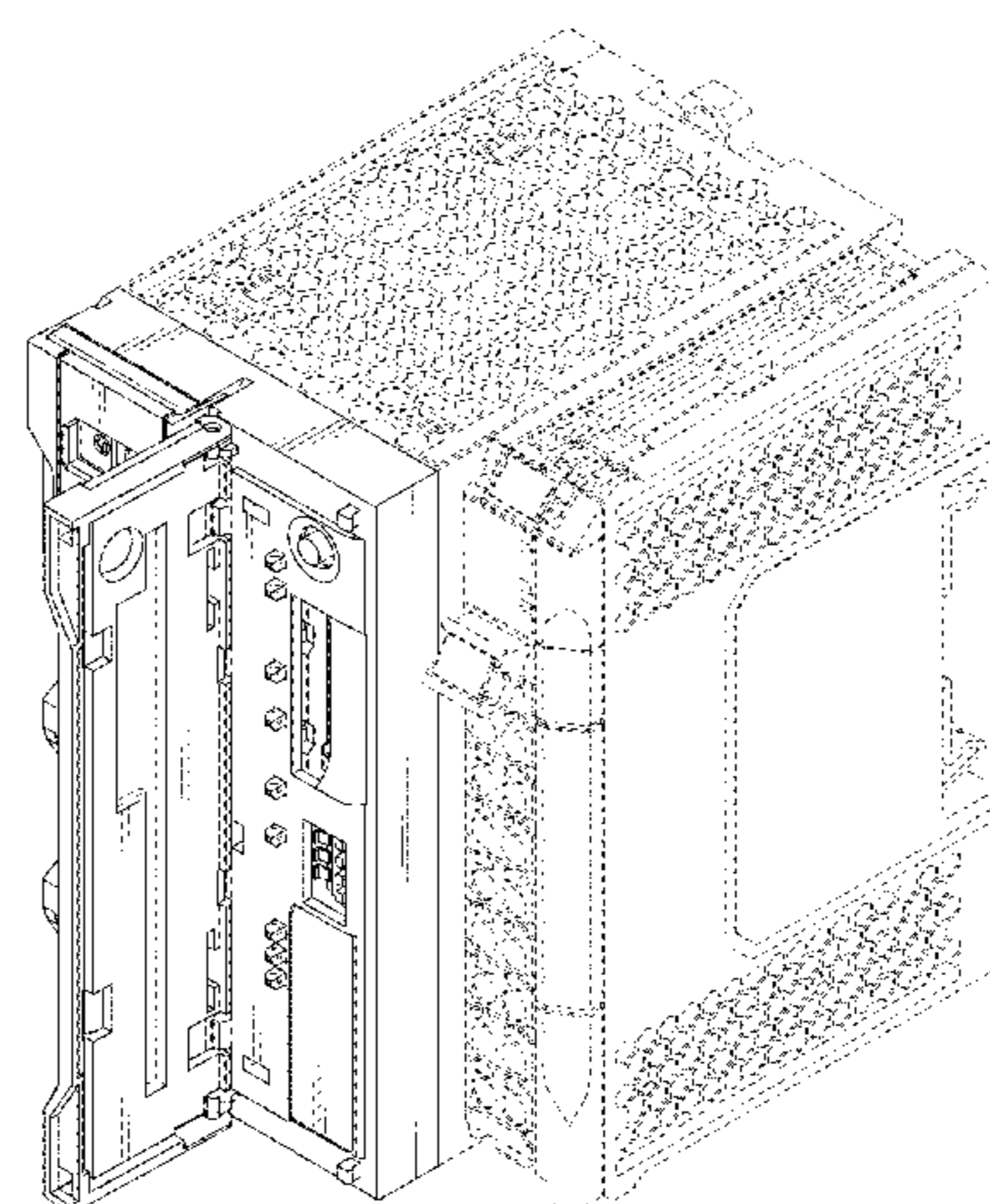
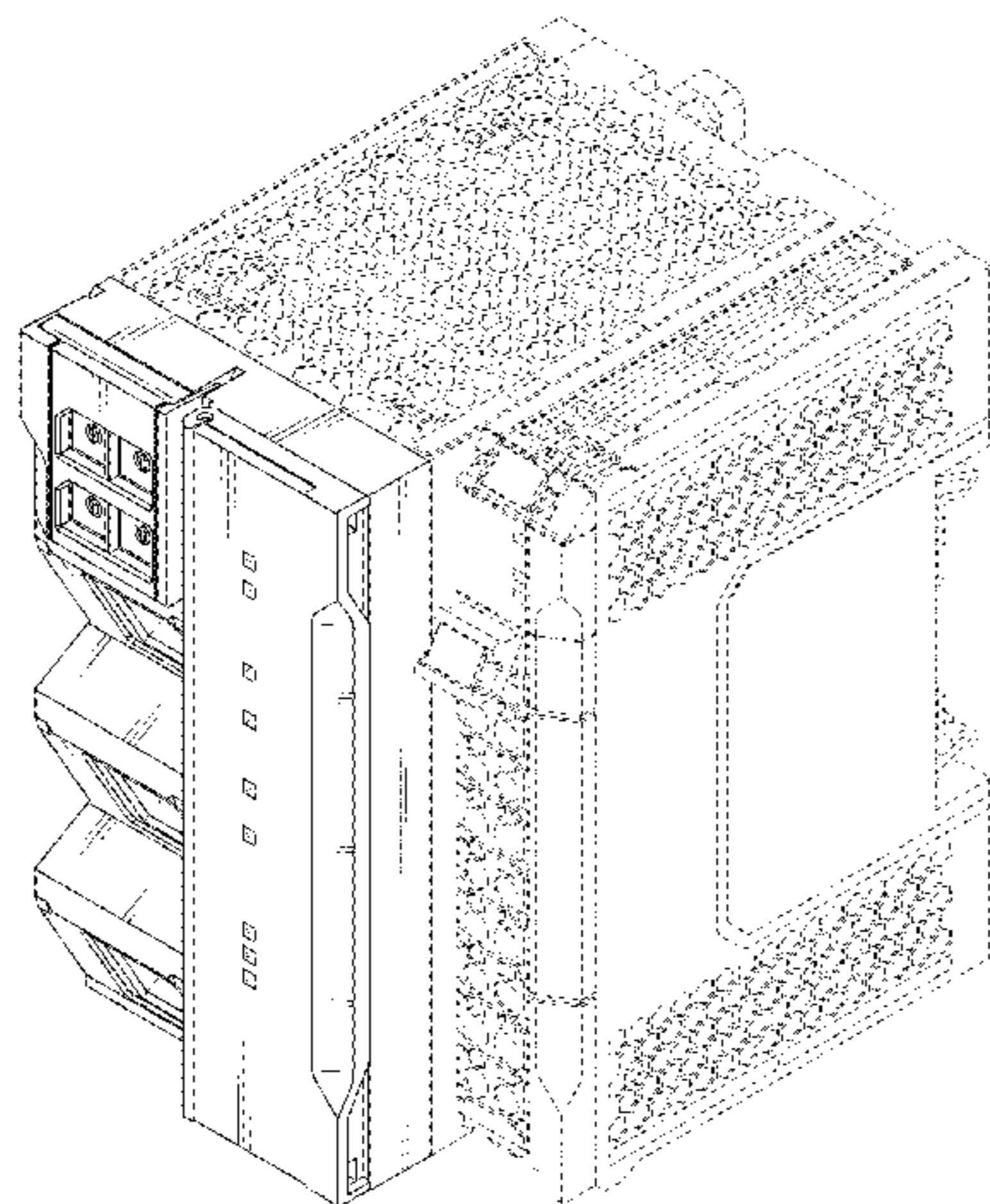
(57) **CLAIM**

The ornamental design for a gateway, as shown and described.

DESCRIPTION

FIG. 1 is a top, front, and right side perspective view of a gateway showing our new design; FIG. 2 is a front view thereof; FIG. 3 is a left side view thereof; FIG. 4 is a right side view thereof; FIG. 5 is a top view thereof; FIG. 6 is a bottom view thereof; and, FIG. 7 is a top, front, and right side perspective view in an opened condition. The broken lines in the figures show portions of the gateway that form no part of the claimed design.

1 Claim, 5 Drawing Sheets



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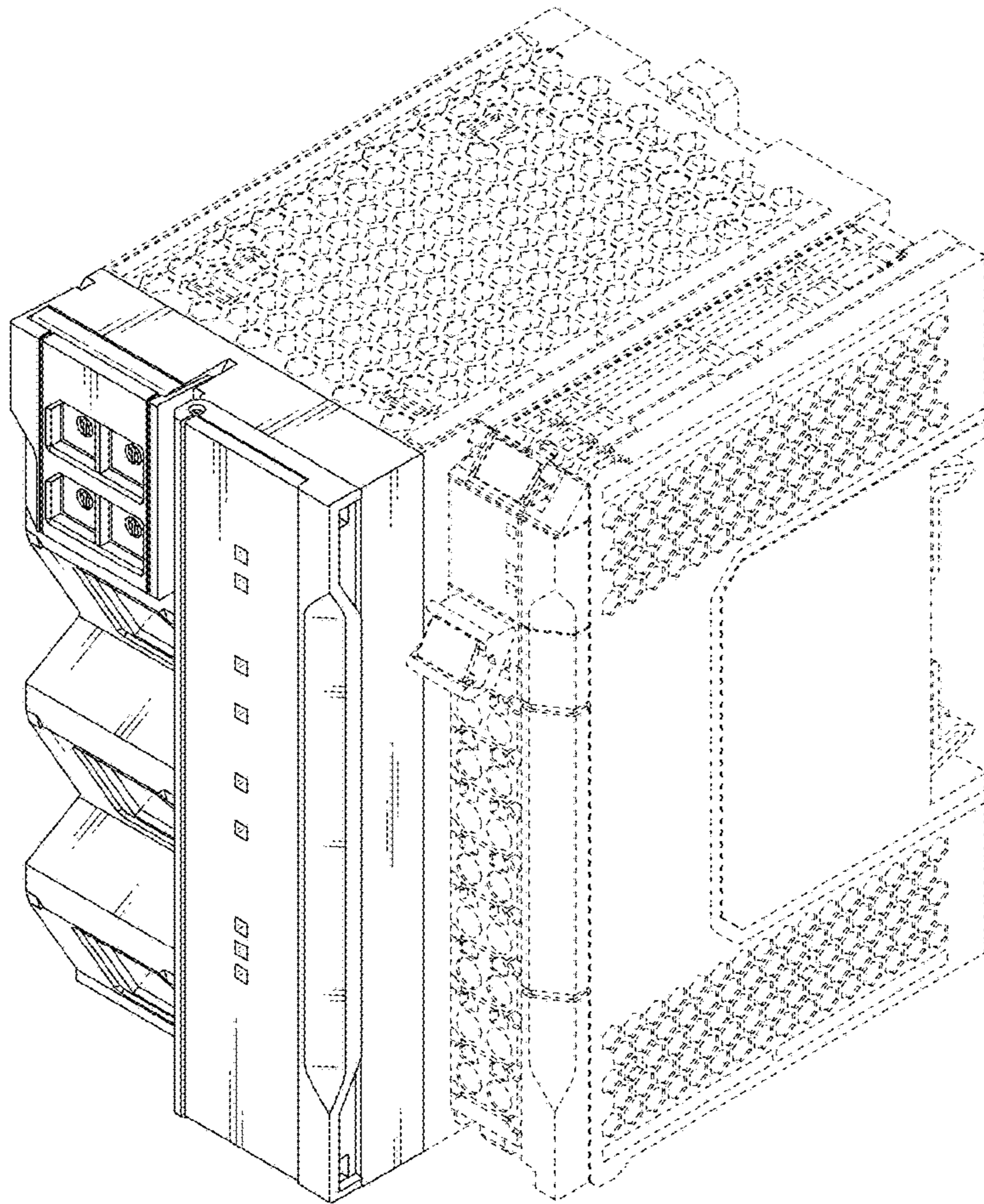


FIG. 1

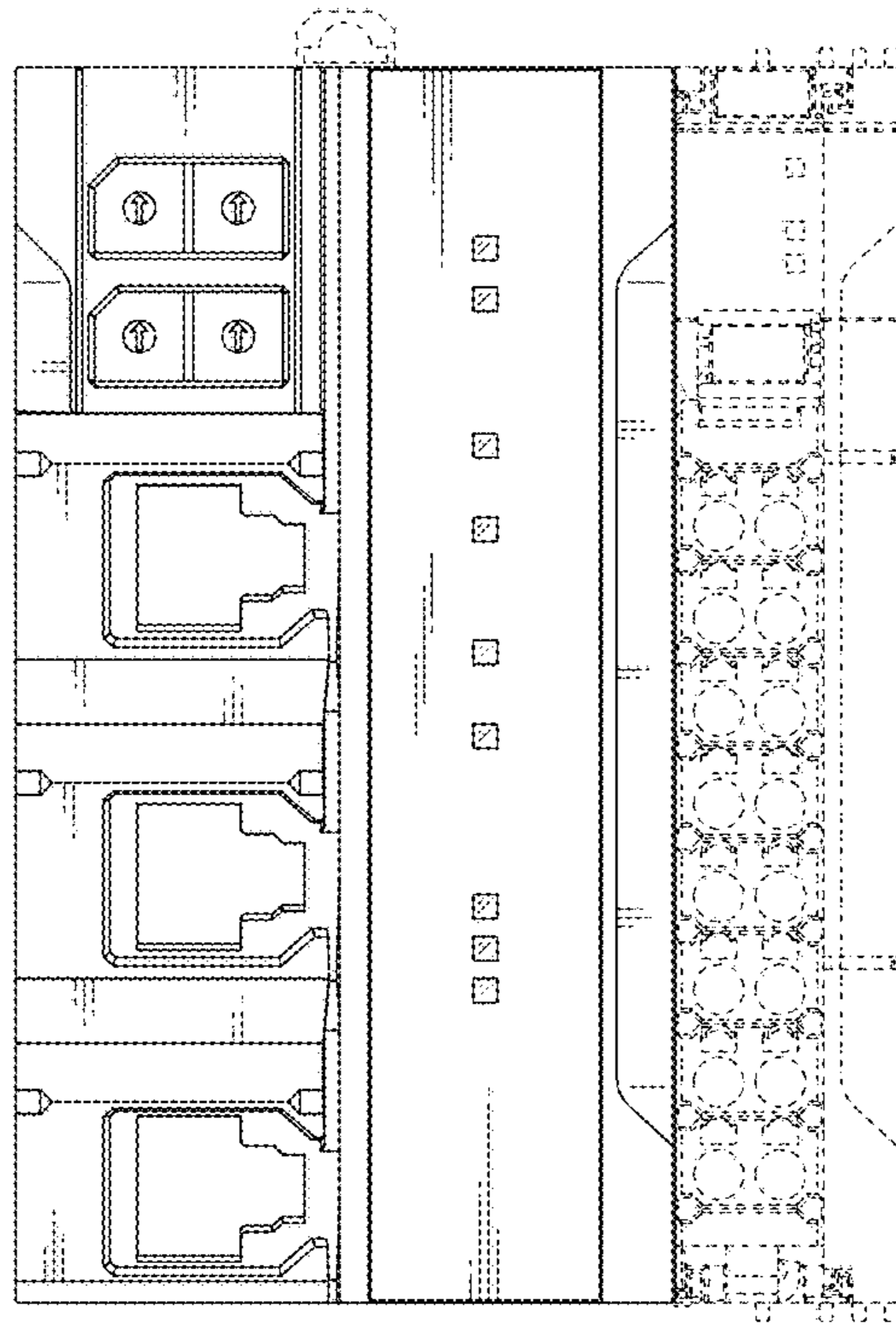


FIG. 2

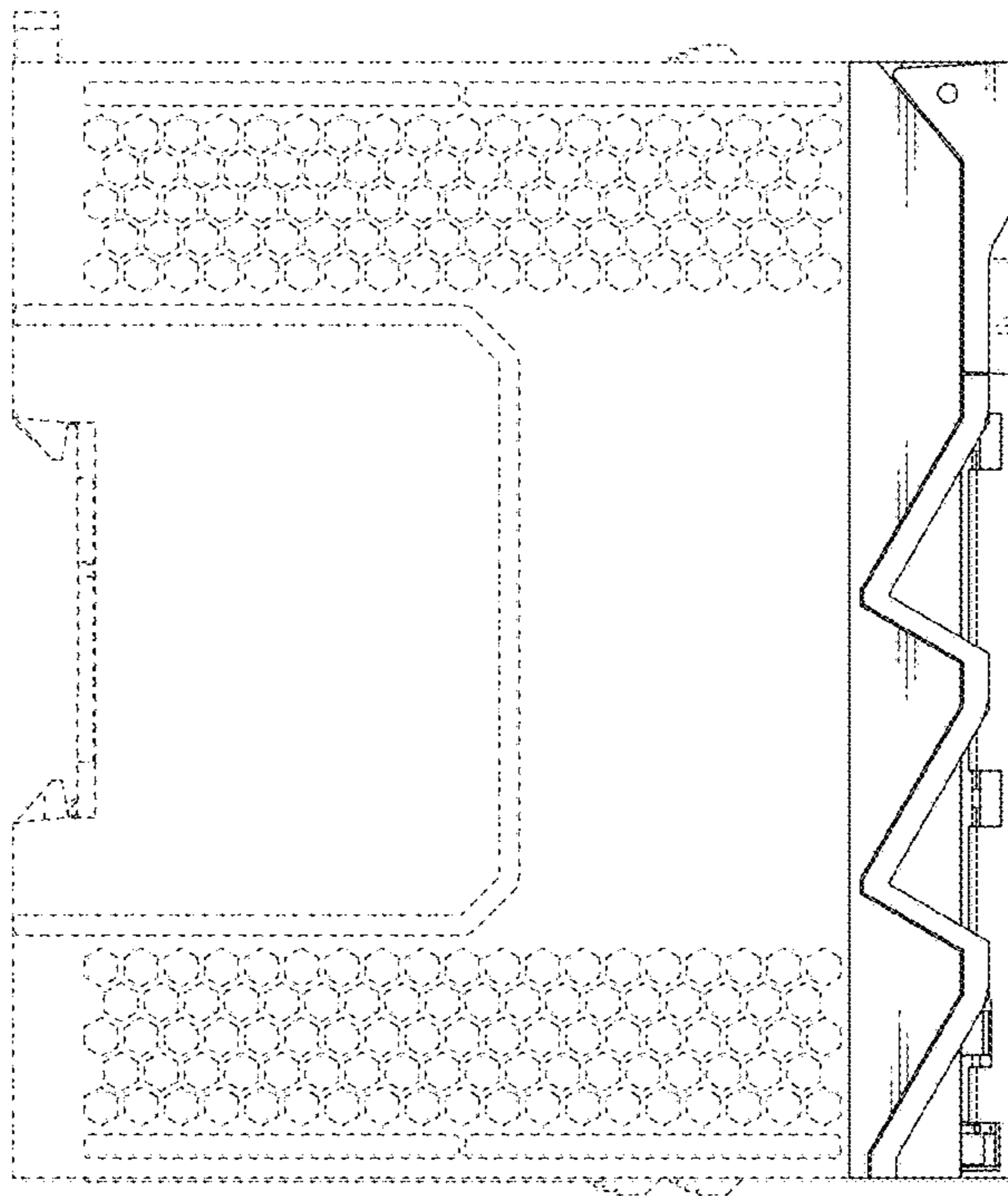


FIG. 3

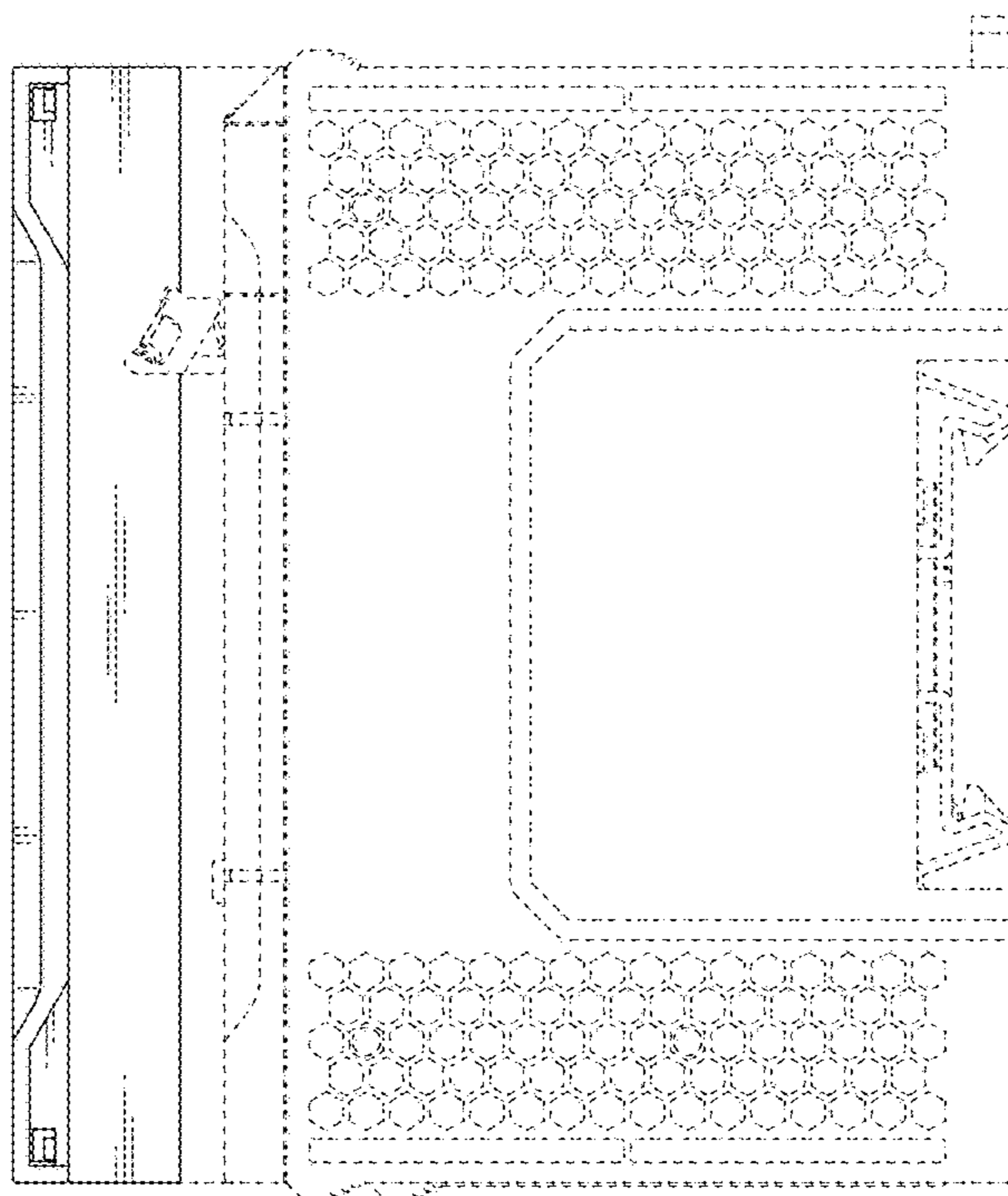


FIG. 4

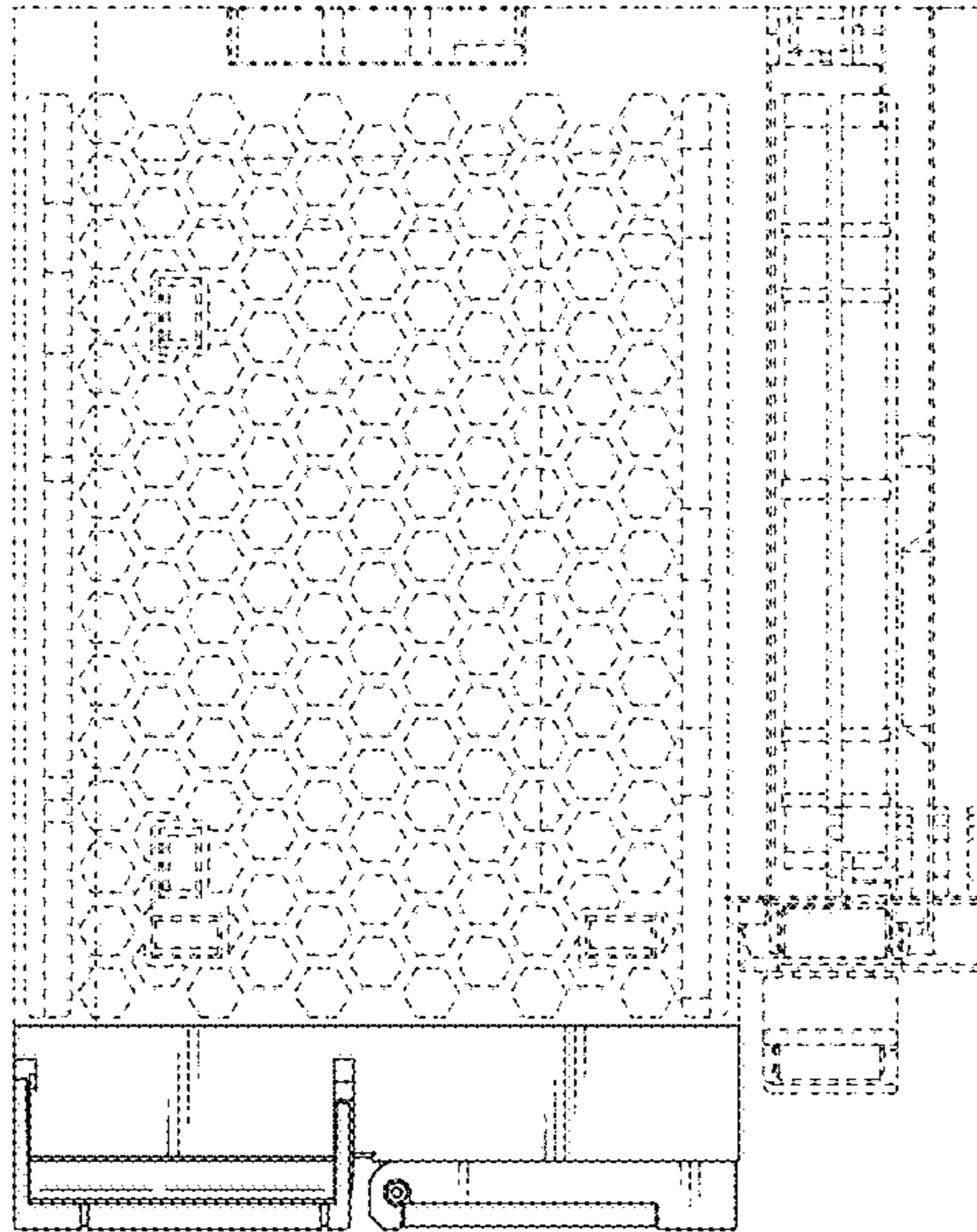


FIG. 5

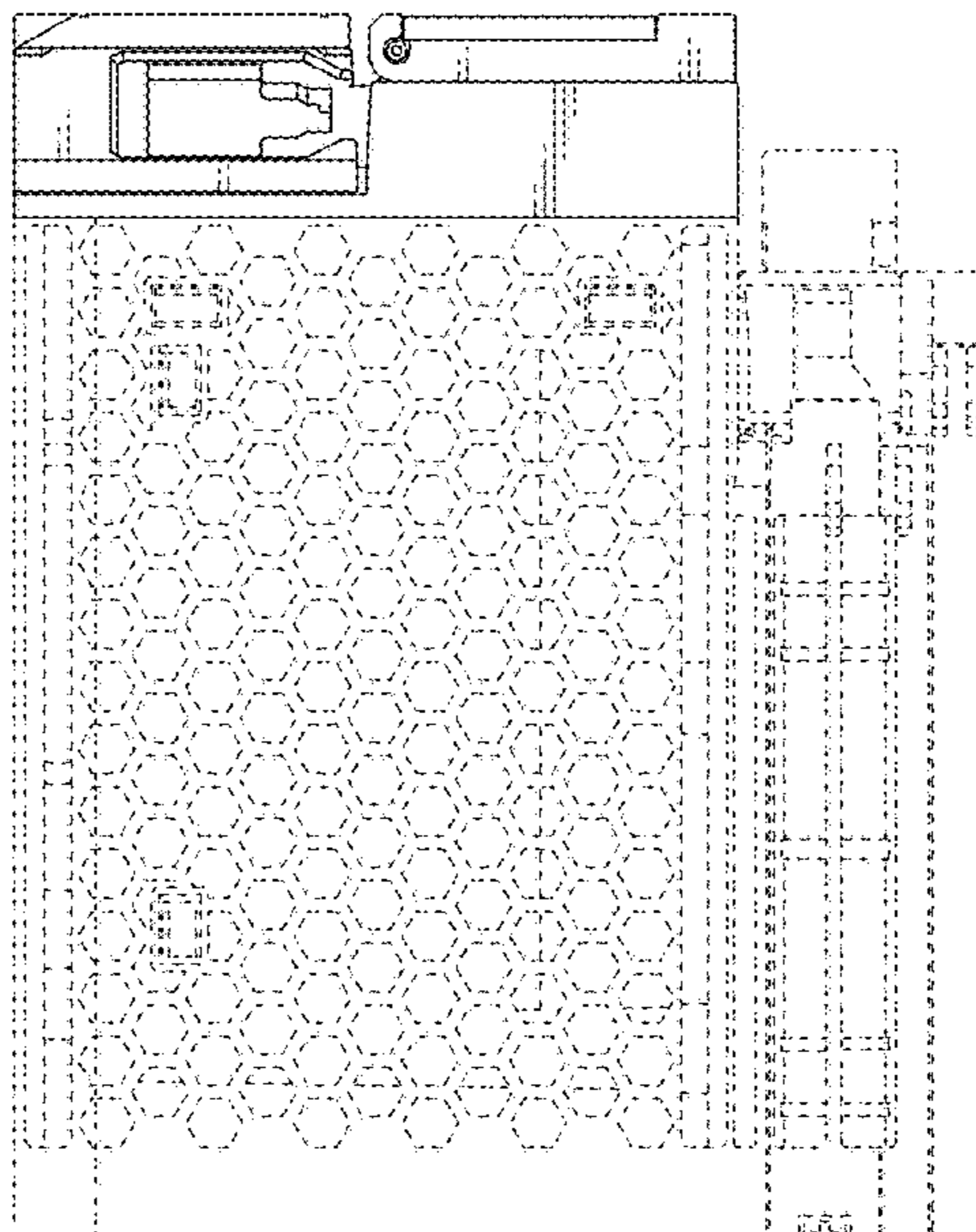


FIG. 6

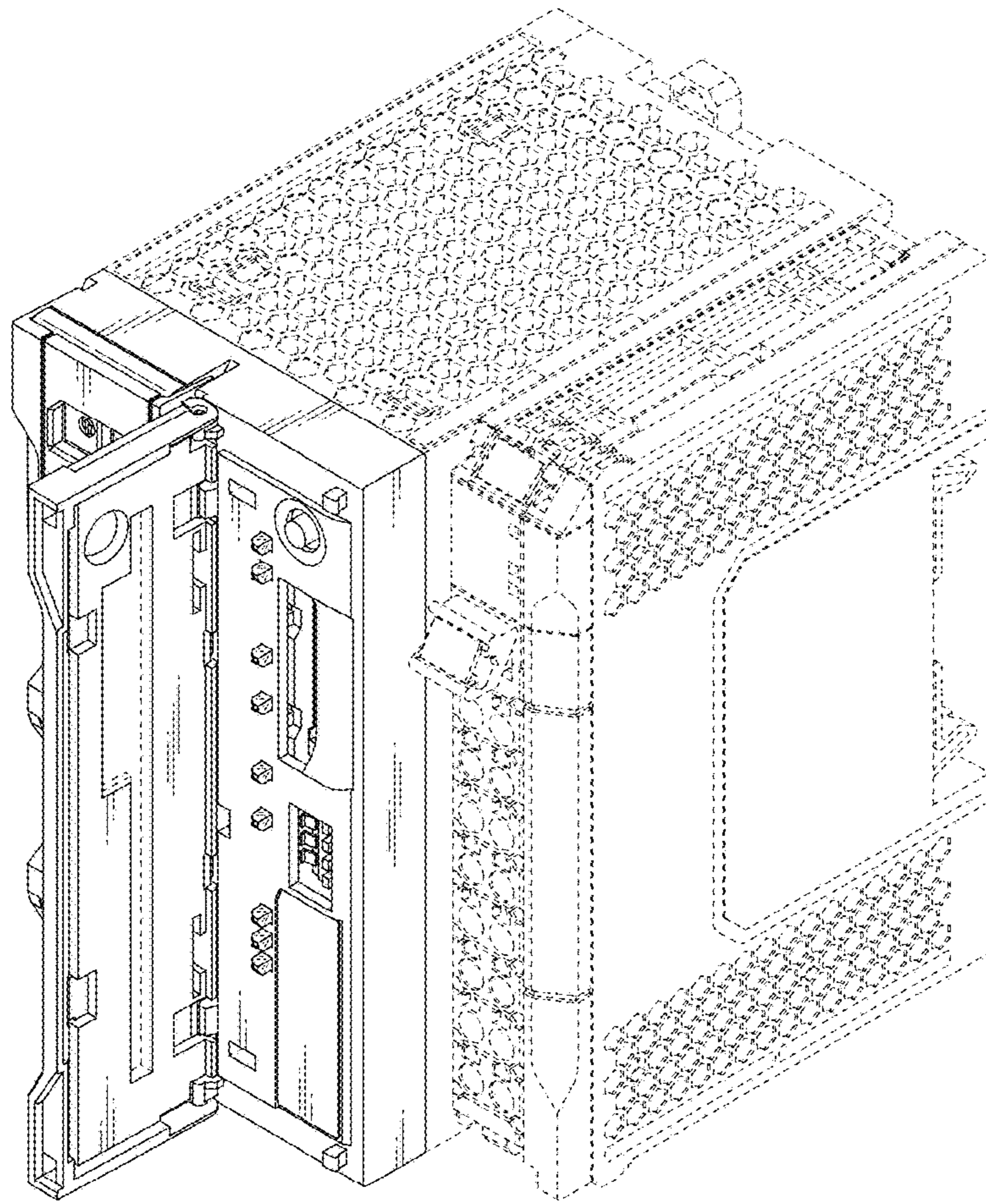


FIG. 7

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : D869,404 S
APPLICATION NO. : 29/645842
DATED : December 10, 2019
INVENTOR(S) : Heroaki Ueda et al.

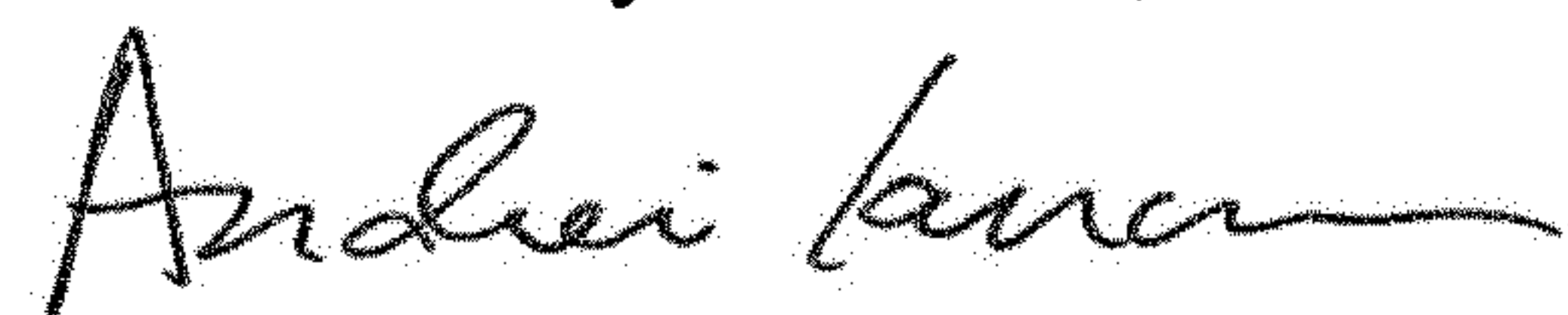
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page

After item (73), insert --(*) Notice: This patent is subject to a terminal disclaimer.--

Signed and Sealed this
Third Day of March, 2020



Andrei Iancu
Director of the United States Patent and Trademark Office