



US00D868796S

(12) **United States Design Patent** (10) **Patent No.:** **US D868,796 S**
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(54) **USB MEMORY**

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(**) Term: **15 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (12) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/480.7**

(58) **Field of Classification Search**
USPC D14/480.1–480.7, 435.1, 411, 433, 436,
D14/484.1, 137, 155, 203.1–203.8, 240,
D14/242; D13/133, 154
CPC H05K 5/0278; G06K 19/07732
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a USB memory, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a universal serial bus (USB) memory showing our new design.

FIG. 2 is a front elevation view thereof.

FIG. 3 is a rear elevation view thereof.

FIG. 4 is a left side elevation view thereof.

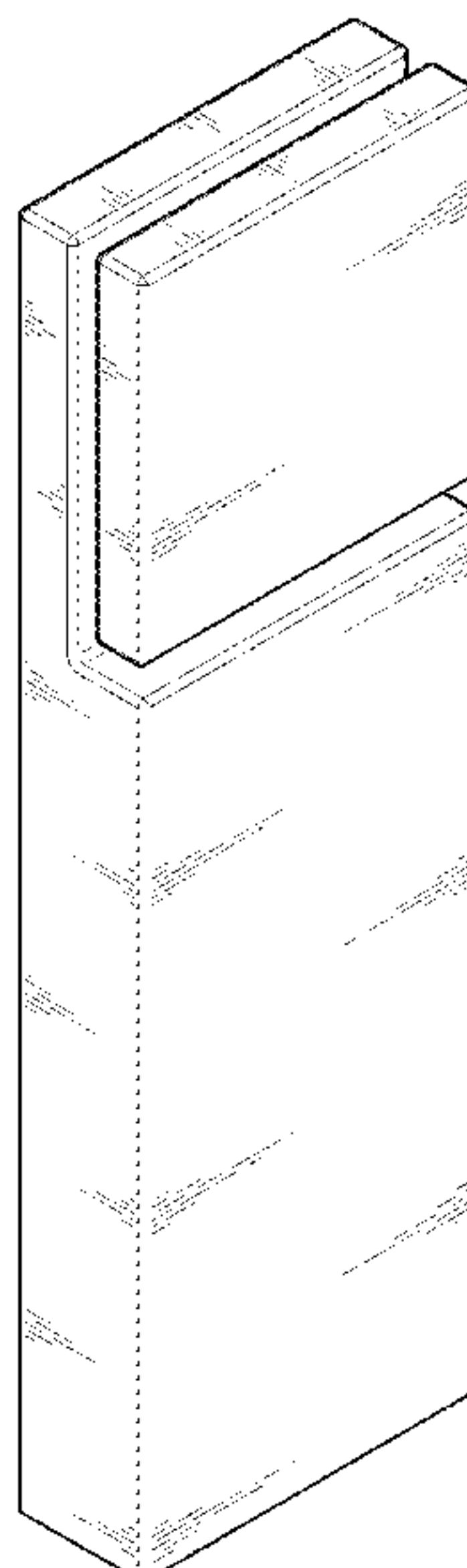
FIG. 5 is a right side elevation view thereof.

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

The features of the USB memory shown in broken lines in FIGS. 3 and 7 form no part of the claimed design.

1 Claim, 6 Drawing Sheets



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FIG. 1

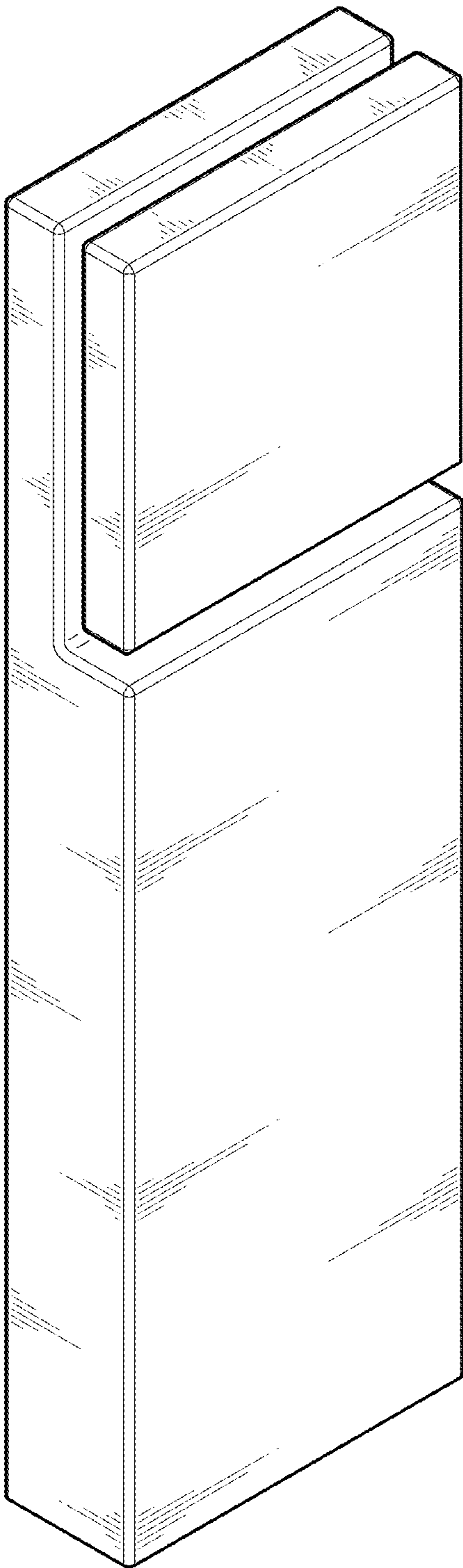


FIG. 2

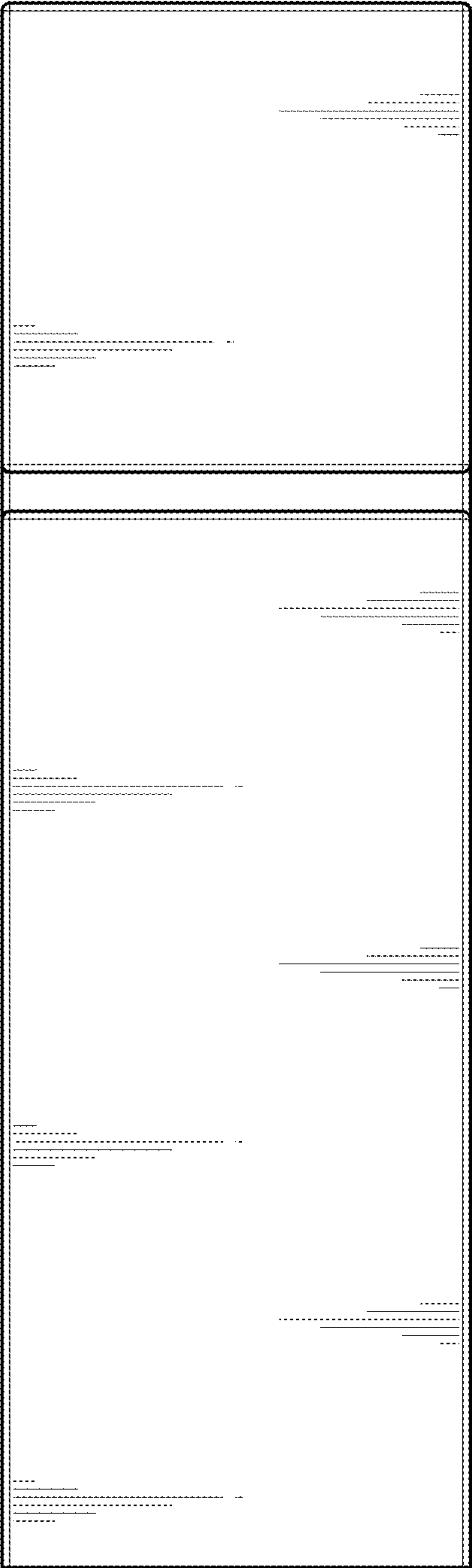


FIG. 3

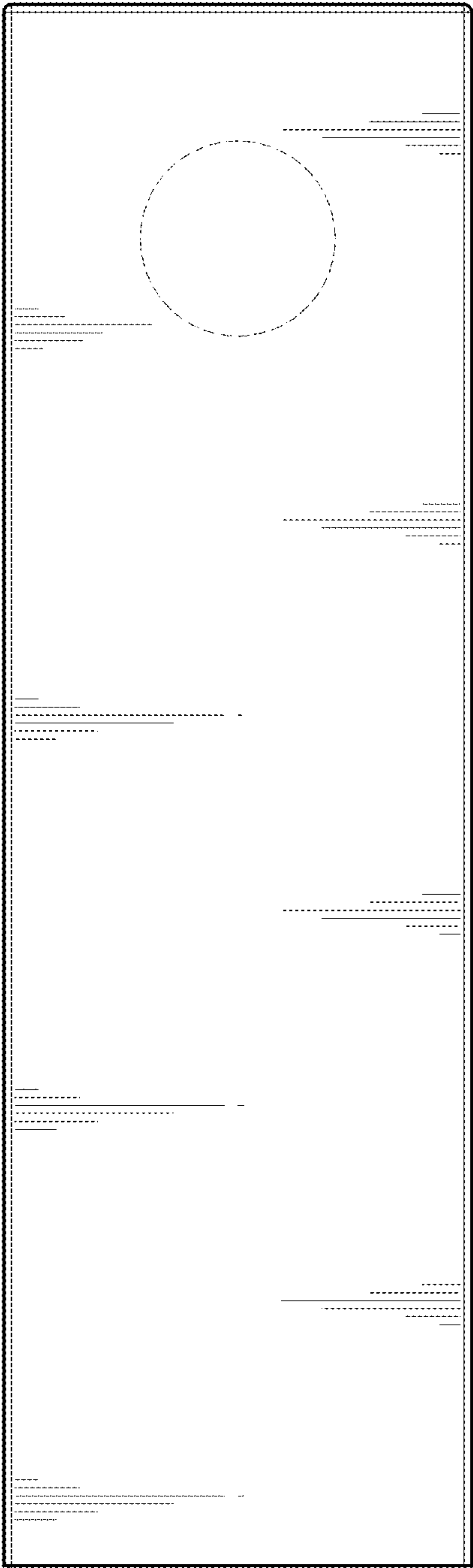


FIG. 4

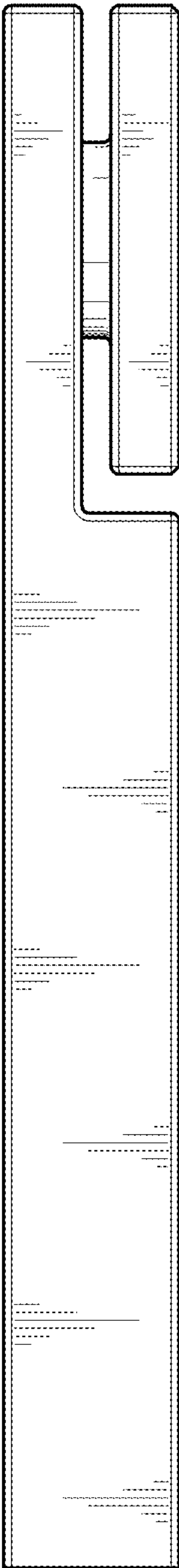


FIG. 5

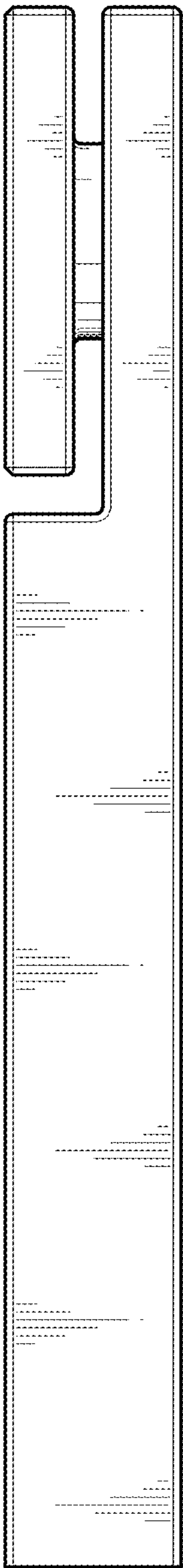


FIG. 6

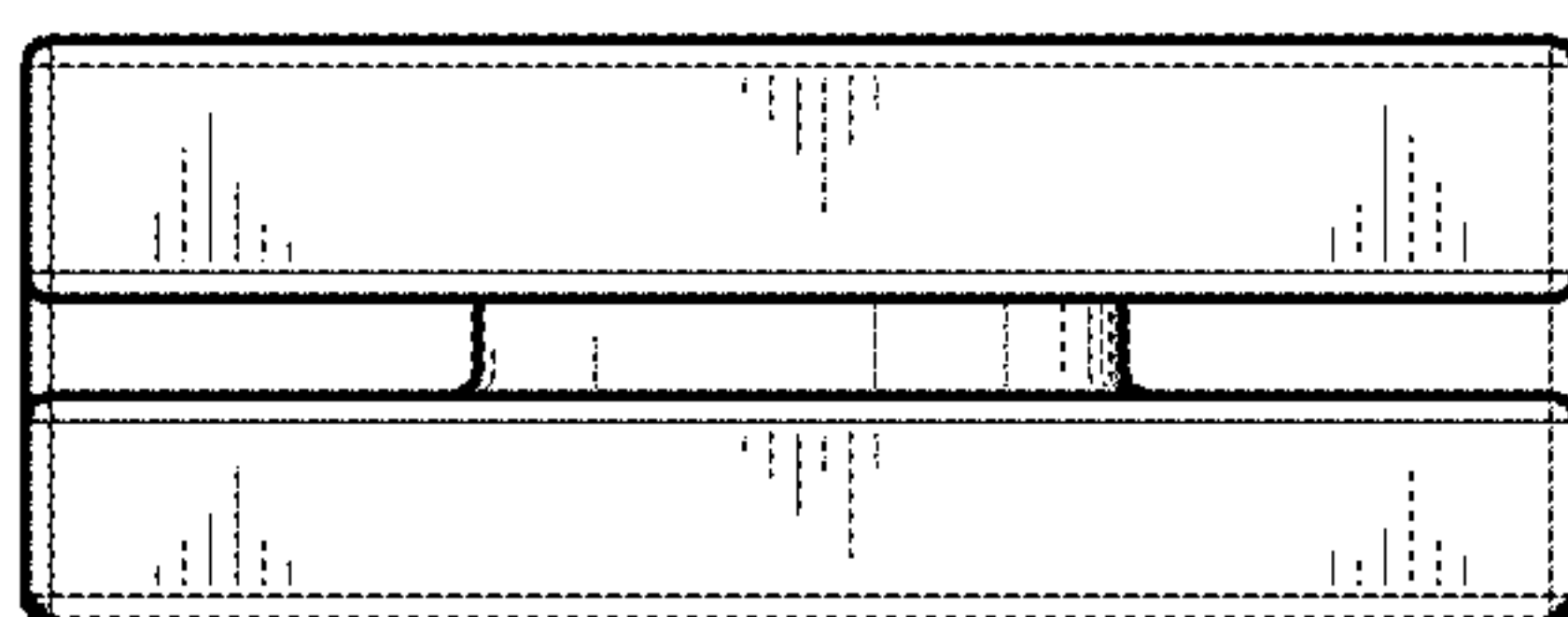


FIG. 7

