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(12) **United States Design Patent** (10) **Patent No.:** **US D868,787 S**  
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(54) **PERIPHERAL COMPONENT INTERCONNECT CARD WITH BRACKET**

(71) Applicant: **INTEL CORPORATION**, Santa Clara, CA (US)

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(58) **Field of Classification Search**  
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CPC ..... G06F 2213/0004; G06F 2213/0024; G06F 2213/0026; G06F 13/00; G06F 13/14; G06F 13/4221; G06F 1/1656; G06F 1/181; G06F 1/186; G06F 11/3041; G06F 3/00; H05K 5/00; H05K 5/0047; H05K 5/0052; H05K 5/006; H05K 5/02; H05K 5/0204; H05K 5/0269; H05K 5/03; H05K 5/04; H05K 1/141; H05K 7/14; H05K 7/1402; H05K 7/1405; H05K 7/1407; H05K 7/1427; H05K 7/1428; H05K 7/1431; H05K 7/1411; H01L 24/00

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D186,981 S \* 1/1960 Hughes ..... D14/356  
D257,345 S \* 10/1980 Levy ..... D14/435  
D268,931 S \* 5/1983 Ellis ..... D14/433  
D287,594 S \* 1/1987 Perissinotto ..... D14/387

(Continued)

OTHER PUBLICATIONS

Intel Xeon Phi Coprocessor adds 60 cores through a PCI-Express card. 404techsupport.com. (online) 5 pgs. Printed Dec. 1, 2012 [Retrieved on Mar. 22, 2019] <https://www.404techsupport.com/2012/12/01/intel-xeon-phi-coprocessor-adds-60-cores-through-a-pci-express-card/>.\*

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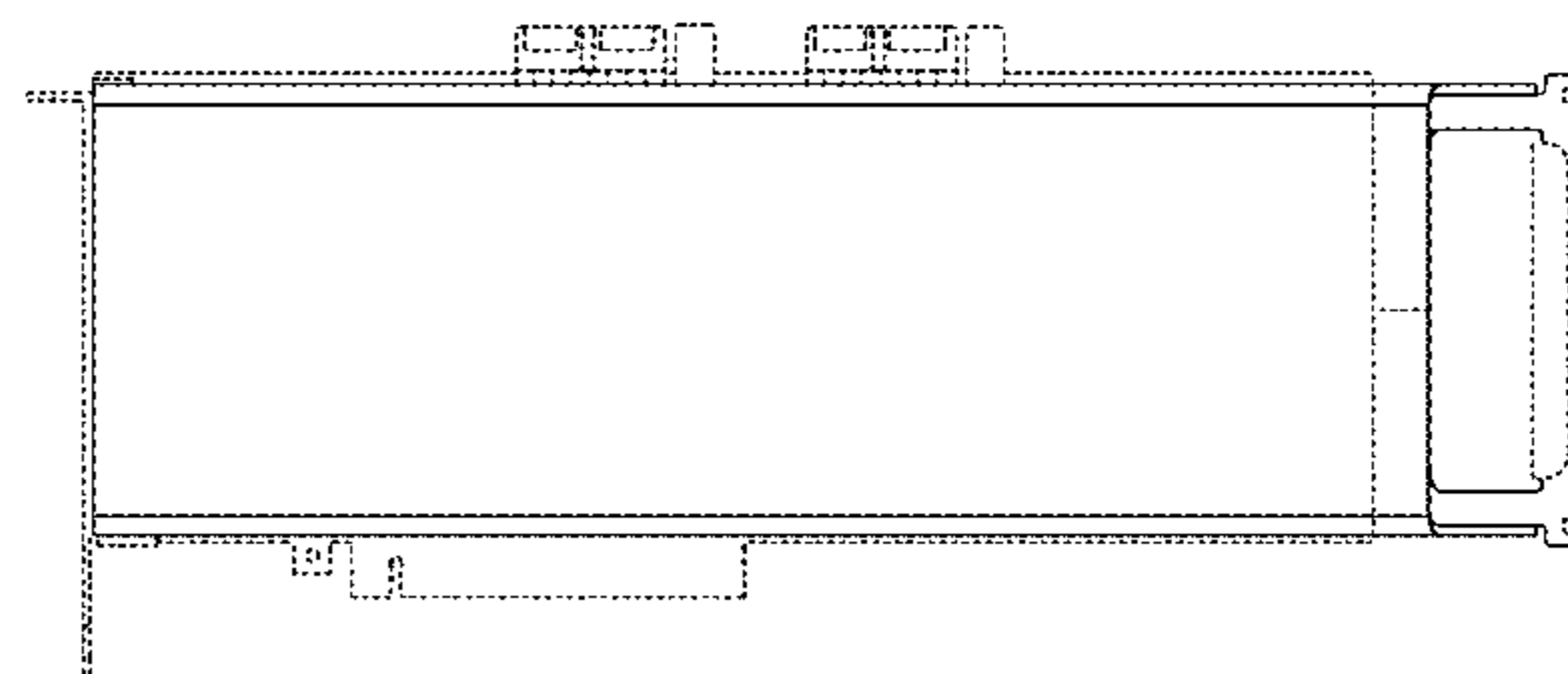
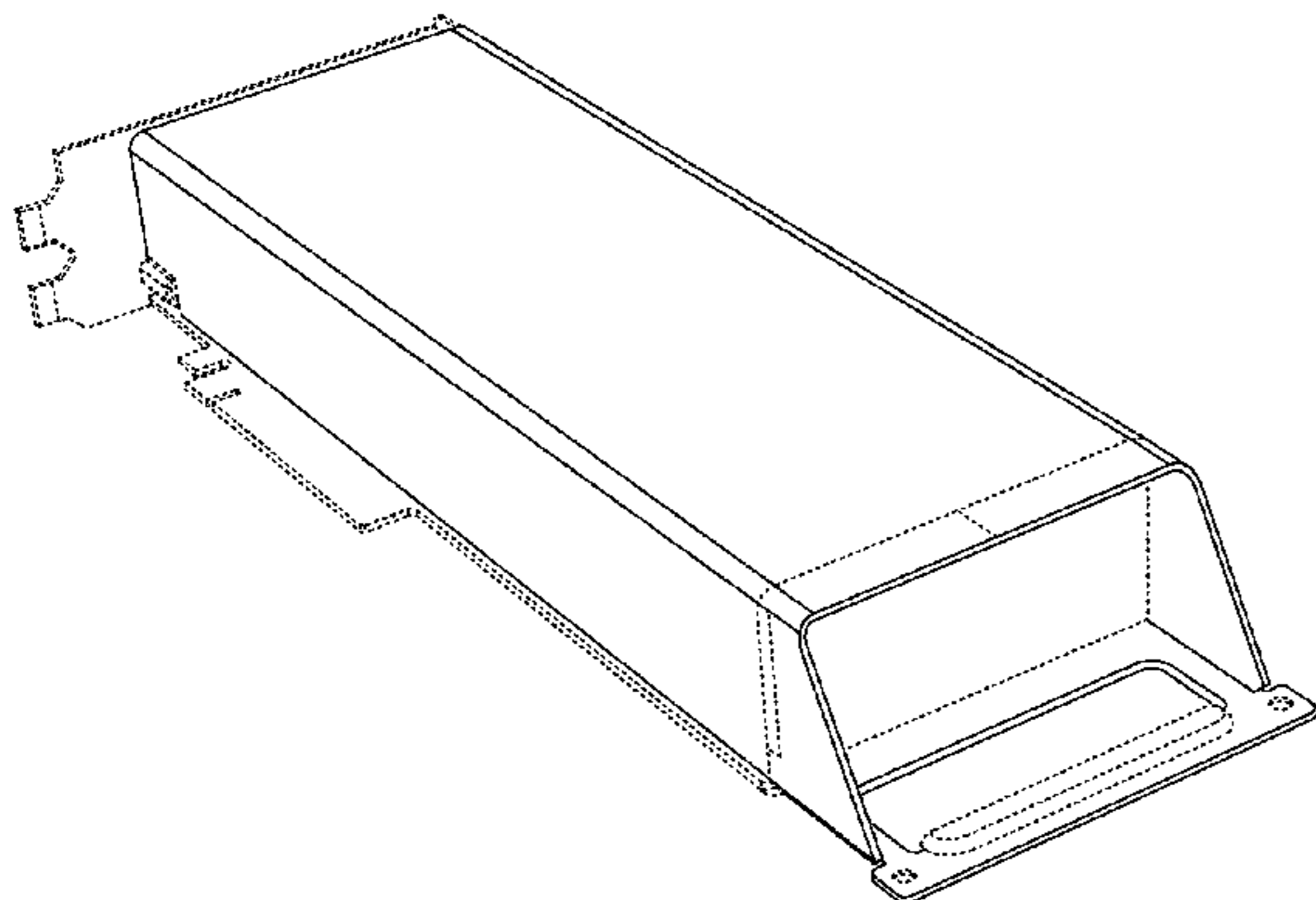
(57) **CLAIM**

The ornamental design for a “peripheral component interconnect card with bracket”, as shown and described in FIGS. 1-7.

**DESCRIPTION**

FIG. 1 is right, front perspective view of a peripheral component interconnect (PCI) card with bracket; FIG. 2 a front view of the PCI card with bracket of FIG. 1 shown enlarged for clarity; FIG. 3 is a rear view of the PCI card with bracket of FIG. 1 shown enlarged for clarity; FIG. 4 is right side view of the PCI card with bracket of FIG. 1; FIG. 5 is a left side view of the PCI card with bracket of FIG. 1; FIG. 6 is top view of the PCI card with bracket of FIG. 1; and, FIG. 7 is a bottom view of the PCI card with bracket of FIG. 1. The broken lines in the drawings depict portion of the PCI card that form no part of the claimed design.

**1 Claim, 6 Drawing Sheets**



(56)

## References Cited

## U.S. PATENT DOCUMENTS

- 4,941,835 A \* 7/1990 Lasmayoux ..... H05K 5/0208  
439/152  
D311,737 S \* 10/1990 Westwood ..... D14/435  
D313,404 S \* 1/1991 Myers ..... D14/434  
D316,853 S \* 5/1991 Dickey ..... D13/184  
D327,260 S \* 6/1992 Davis ..... D13/184  
D331,749 S \* 12/1992 Mizusugi ..... D14/434  
D360,419 S \* 7/1995 Weber ..... D13/177  
D397,107 S \* 8/1998 Klinker ..... D14/436  
D398,004 S \* 9/1998 Klinker ..... D14/436  
D398,592 S \* 9/1998 Klinker ..... D14/436  
D399,502 S \* 10/1998 Klinker ..... D14/436  
D405,440 S \* 2/1999 Klinker ..... D14/436  
D405,778 S \* 2/1999 Klinker ..... D14/436  
D409,178 S \* 5/1999 Klinker ..... D14/436  
D414,481 S \* 9/1999 Wu ..... D14/436  
6,021,049 A \* 2/2000 Thompson ..... H05K 7/1405  
211/41.17  
D423,479 S \* 4/2000 Alo ..... D14/260.1  
6,215,666 B1 \* 4/2001 Hileman ..... G11B 33/124  
361/679.32  
D451,501 S \* 12/2001 Sward ..... D14/242  
D452,494 S \* 12/2001 Sodling ..... D14/242  
6,556,451 B1 \* 4/2003 Feightner ..... H05K 7/1429  
361/679.32  
D475,701 S \* 6/2003 Kasuya ..... D14/242  
D475,972 S \* 6/2003 Gershfeld ..... D13/139.4  
D477,279 S \* 7/2003 Gershfeld ..... D13/139.4  
D477,282 S \* 7/2003 Gershfeld ..... D13/139.4  
D483,756 S \* 12/2003 Tanaka ..... D14/356  
D486,482 S \* 2/2004 Huang ..... D14/242  
D489,714 S \* 5/2004 Yin ..... D14/242  
D489,725 S \* 5/2004 Hu ..... D13/147  
D491,191 S \* 6/2004 Wen ..... D14/496  
D513,252 S \* 12/2005 Wang ..... D14/357  
D523,860 S \* 6/2006 Xu ..... D14/436  
D539,286 S \* 3/2007 Phillips ..... D14/435  
D548,739 S \* 8/2007 Nakamura ..... D14/433  
D549,708 S \* 8/2007 Phillips ..... D14/435  
D562,818 S \* 2/2008 Chen ..... D14/385  
D565,571 S \* 4/2008 Trifilio ..... D14/356  
D578,122 S \* 10/2008 Leung ..... D14/385  
D579,873 S \* 11/2008 Gershfeld ..... D13/139.4  
D607,885 S \* 1/2010 Otani ..... D14/436  
D618,680 S \* 6/2010 Marchand ..... D14/357  
D623,178 S \* 9/2010 Lu ..... D14/242  
D631,885 S \* 2/2011 Maier ..... D14/435.1  
D664,544 S \* 7/2012 Yi ..... D14/436  
D678,289 S \* 3/2013 Searby ..... D14/439  
D691,557 S \* 10/2013 Helosvuori ..... D13/123  
D700,894 S \* 3/2014 Feldstein ..... D13/154  
D703,618 S \* 4/2014 Kodama ..... D13/154  
D721,706 S \* 1/2015 Rooyakkers ..... D13/110  
D721,707 S \* 1/2015 Rooyakkers ..... D13/110  
D723,566 S \* 3/2015 Ferguson ..... D14/435.1  
D740,828 S \* 10/2015 Bucsa ..... D14/433  
D745,523 S \* 12/2015 Magi ..... D14/433  
D749,589 S \* 2/2016 Ferguson ..... D14/435.1  
D768,129 S \* 10/2016 Chen ..... D14/349  
D774,035 S \* 12/2016 Kao ..... D14/435.1  
D780,183 S \* 2/2017 Ferguson ..... D14/435.1  
9,600,040 B1 \* 3/2017 Han ..... G06F 1/186  
9,606,589 B2 \* 3/2017 Gallina ..... G06F 1/185  
D801,298 S \* 10/2017 Katsuno ..... D14/157  
D820,264 S \* 6/2018 Lai ..... D14/433  
D827,594 S \* 9/2018 Shim ..... D13/184  
D827,649 S \* 9/2018 Shim ..... D14/435.1  
D830,364 S \* 10/2018 Jhun ..... D14/432  
D831,655 S \* 10/2018 Jhun ..... D14/432  
D832,257 S \* 10/2018 Jhun ..... D14/432  
2008/0101042 A1 \* 5/2008 Huang ..... H05K 5/0269  
361/737  
2012/0026709 A1 \* 2/2012 Chuang ..... H05K 1/141  
361/803  
2013/0021743 A1 \* 1/2013 Hu ..... G06F 1/186  
361/679.32  
2014/0363999 A1 \* 12/2014 Lin ..... G06F 1/185  
439/345  
2015/0062798 A1 \* 3/2015 Kannler ..... G06F 1/20  
361/679.32  
2015/0070837 A1 \* 3/2015 Hsu ..... G06F 1/20  
361/679.47  
2015/0124397 A1 \* 5/2015 Dean ..... H05K 1/141  
361/679.46  
2015/0138718 A1 \* 5/2015 Liu ..... G06F 1/185  
361/679.32  
2016/0327995 A1 \* 11/2016 Sun ..... G06F 1/185  
2017/0153675 A1 \* 6/2017 Yang ..... G06F 1/186  
2017/0280578 A1 \* 9/2017 Hesse ..... G06F 1/185  
2018/0059741 A1 \* 3/2018 Huang ..... G06F 1/181  
2019/0079890 A1 \* 3/2019 Matula ..... G06F 13/4045

## OTHER PUBLICATIONS

Intel Xeon Phi Coprocessor 7120P. amazon.com. (online image:81Zg30DUDrL\_SL1500\_.jpg) 1 pg. MFG Mar. 2016 [Retrieved on Mar. 25, 2019] [https://images-na.ssl-images-amazon.com/images/1/81Zg30DUDrL\\_SL1500\\_.jpg](https://images-na.ssl-images-amazon.com/images/1/81Zg30DUDrL_SL1500_.jpg).\*

PCIE Cards. (Design—© Questel) orbit.com.[online PDF] 12 pgs. Print Dates range Jul. 9, 2014 to Feb. 26, 2019. [Retrieved on Mar. 26, 2019] <https://sobjprd.questel.fr/export/QPTUJ214/pdf2/223b587b-e3b3-4324-91df-efeb6f8ef9c3-181408.pdf>.\*

Intel Xeon Phi Coprocessor 5110P. ebayimg.com (online image: s-I1600.jpg) 1 pg. MFG May 2013 [Retrieved on Mar. 26, 2019] <https://i.ebayimg.com/images/g/H0oAAOSw9N1ap41h/s-11600.jpg>.\*

\* cited by examiner

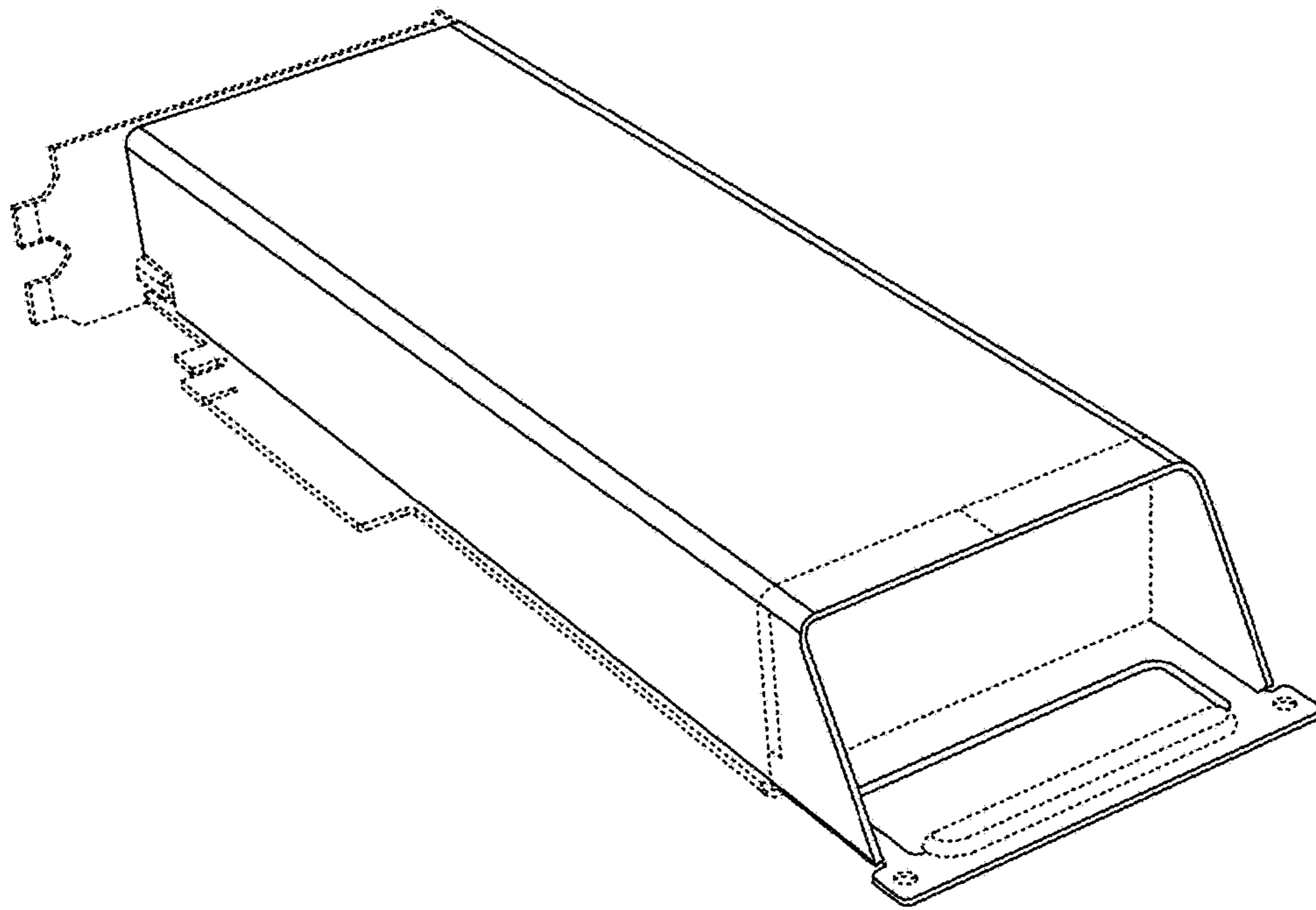


FIG. 1

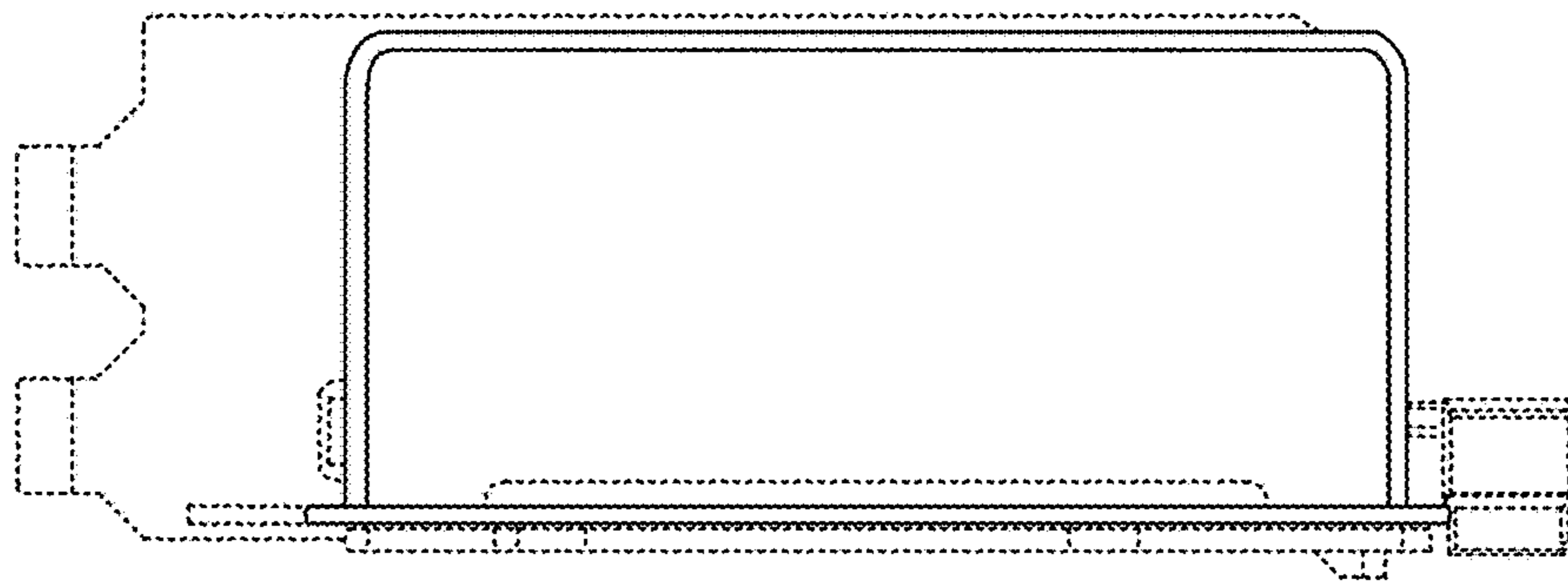


FIG. 2

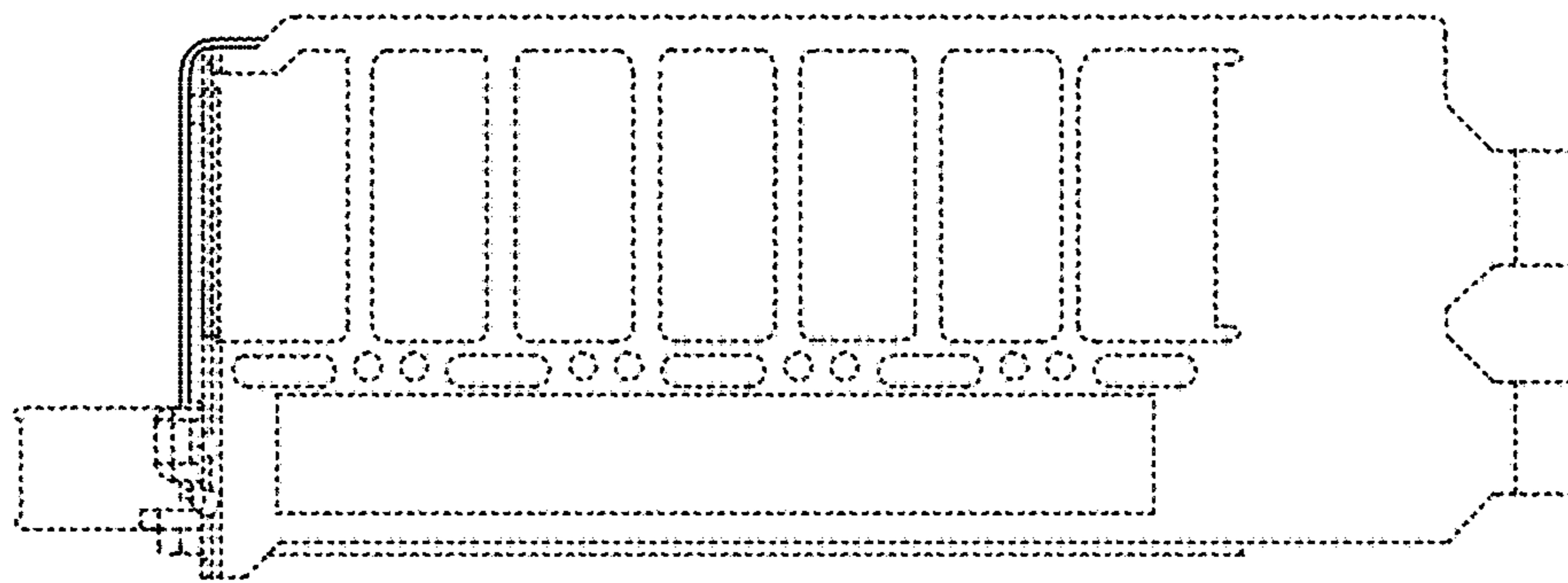


FIG. 3

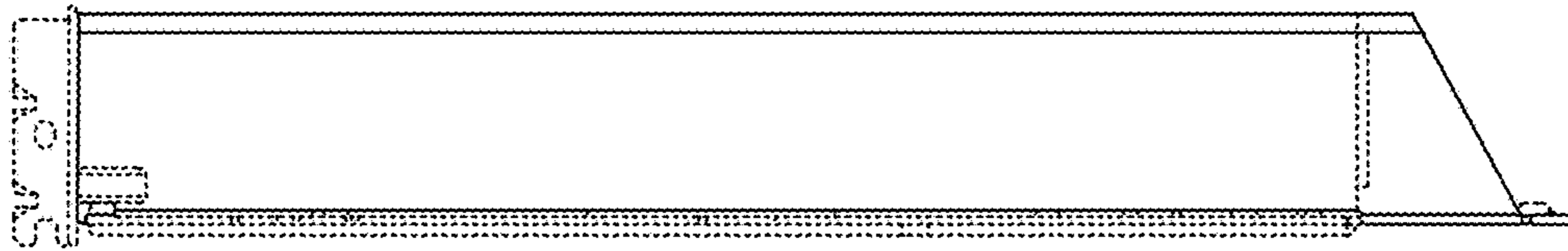


FIG. 4

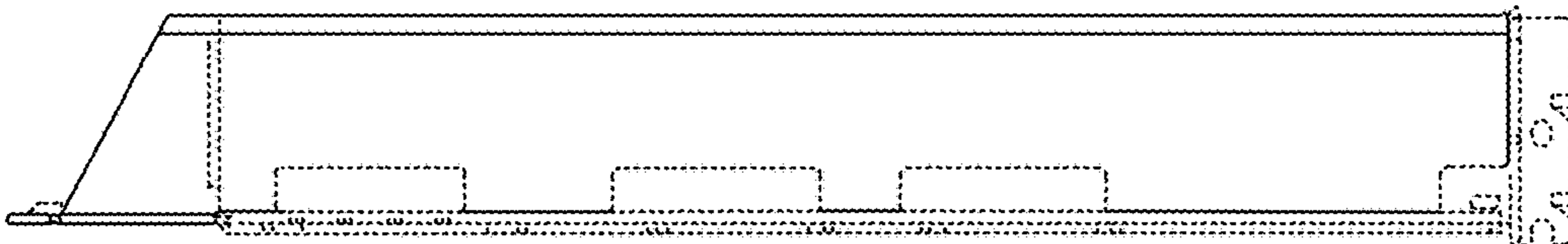


FIG. 5

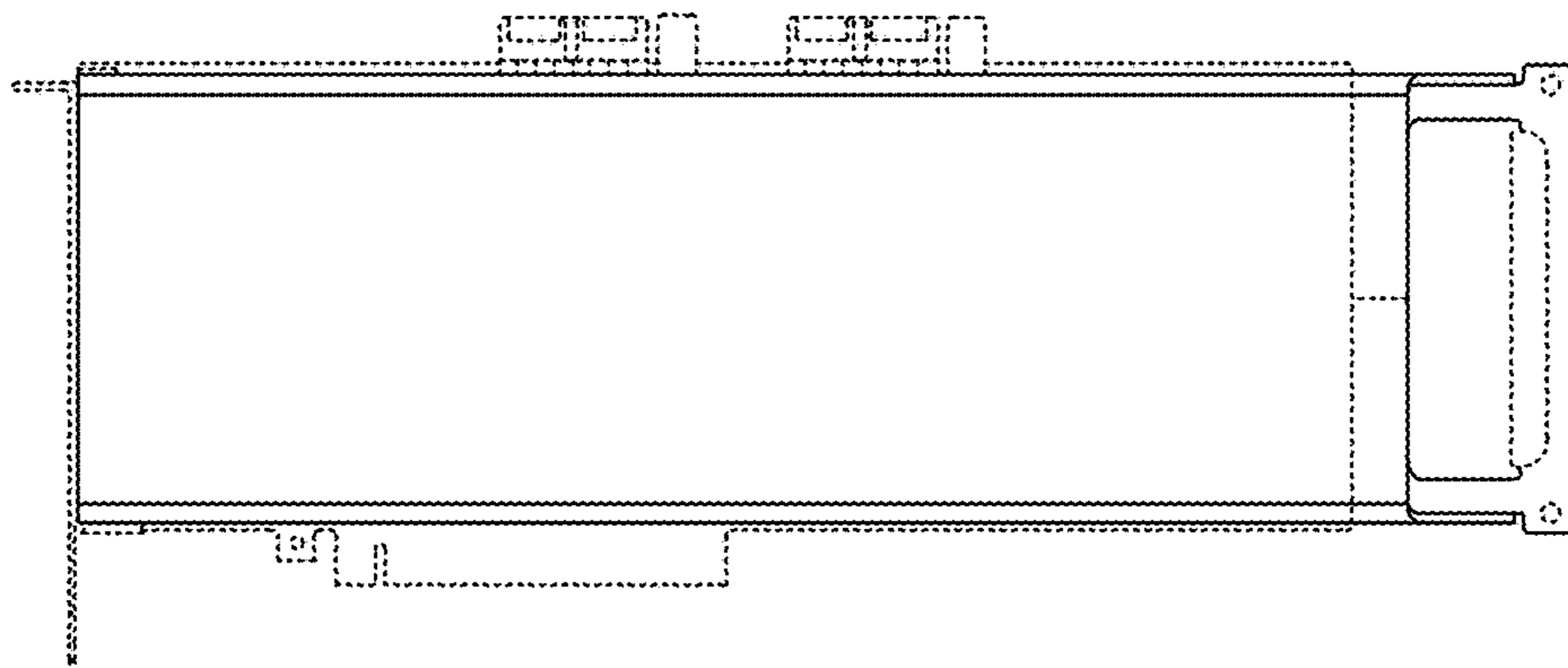


FIG. 6

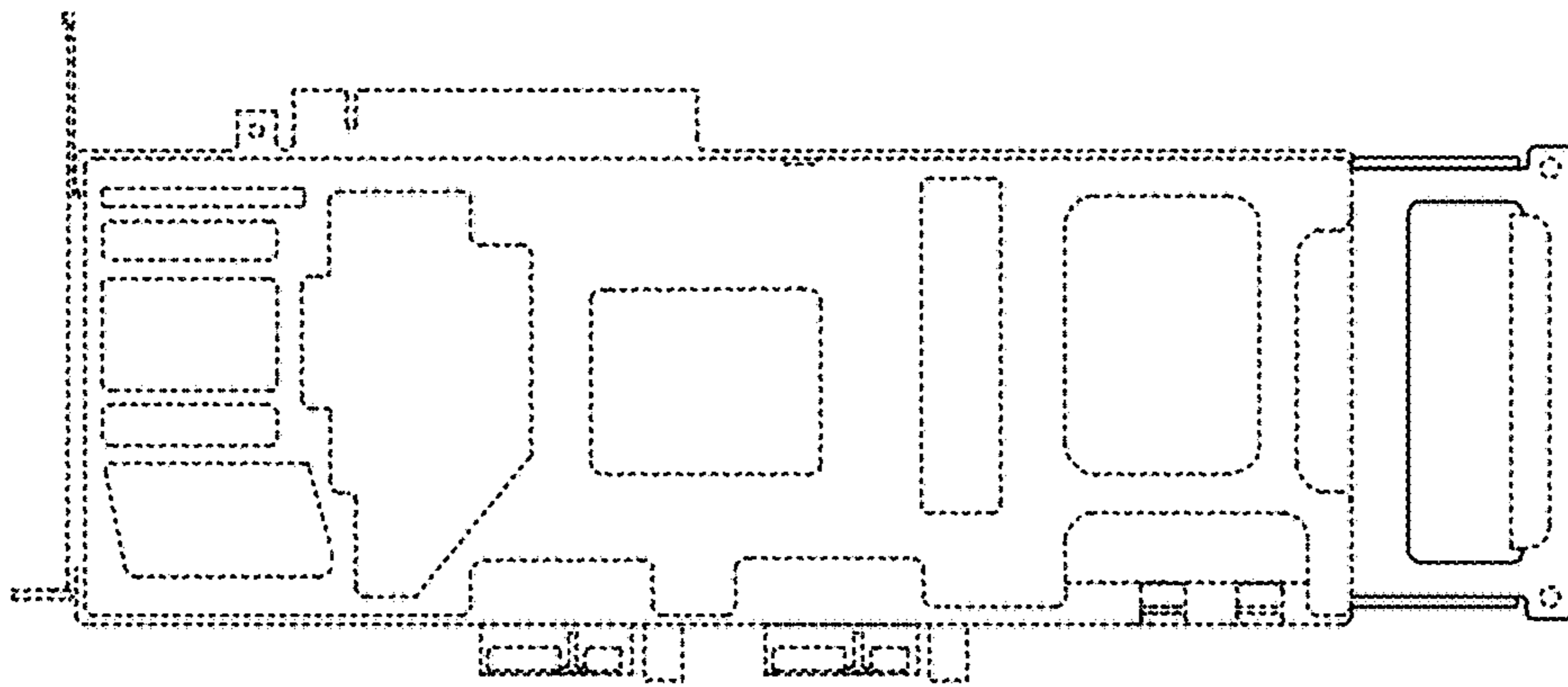


FIG. 7