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(12) **United States Design Patent** (10) **Patent No.:** **US D868,698 S**
Aromin et al. (45) **Date of Patent:** **** Dec. 3, 2019**

(54) **ARC FAULT CIRCUIT INTERRUPTER
 OUTLET ADAPTER**

(71) Applicant: **Tower Manufacturing Corporation,**
 Providence, RI (US)

(72) Inventors: **Victor V Aromin,** West Warwick, RI
 (US); **Daniel R Paquette,** Woonsocket,
 RI (US); **Paul M Piekarski,** Foster, RI
 (US); **Louis J. Shatkin,** East
 Greenwich, RI (US)

(73) Assignee: **Tower Manufacturing Corp.,**
 Providence, RI (US)

(**) Term: **15 Years**

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Related U.S. Application Data

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 filed on Jun. 7, 2017, which is a continuation-in-part
 of application No. 29/600,808, filed on Apr. 16, 2017,
 now abandoned.

(51) **LOC (12) Cl.** **13-03**

(52) **U.S. Cl.**
 USPC **D13/137.1**

(58) **Field of Classification Search**
 USPC D13/107, 108, 110, 123, 133,
 D13/137.1–137.4, 138.1–138.2,
 D13/139.1–139.8, 152, 154, 184
 CPC H01R 11/00; H01R 9/00; H01R 13/00;
 H01R 13/04; H01R 13/10; H01R
 13/6666; H01R 13/6675; H01R 25/00;
 H01R 25/006; H01H 2207/00; H01H
 2207/022

See application file for complete search history.

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 (Year: 2017).*

Primary Examiner — Rosemary K Tarcza
Assistant Examiner — Christy M Nemeth

(57) **CLAIM**

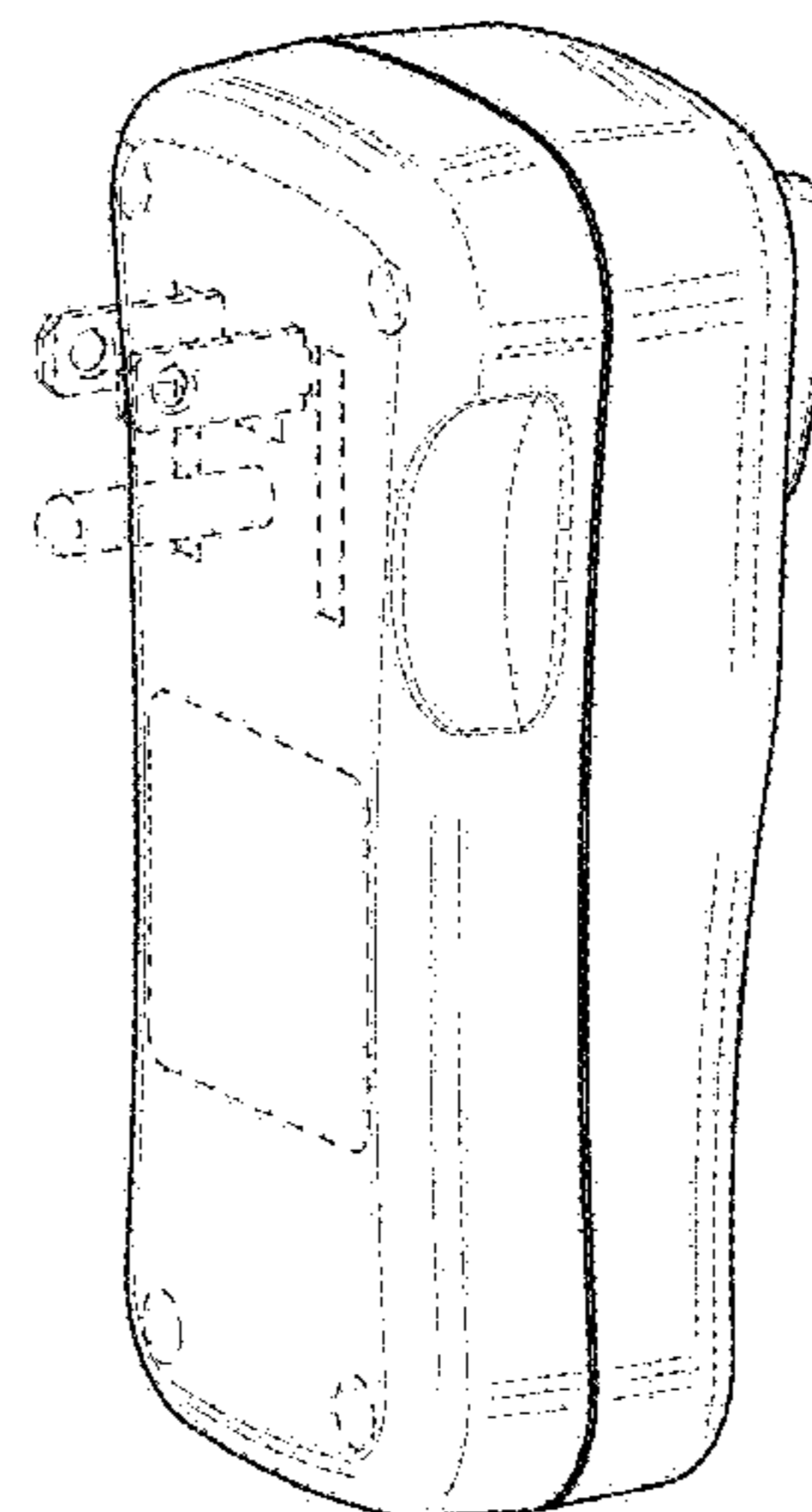
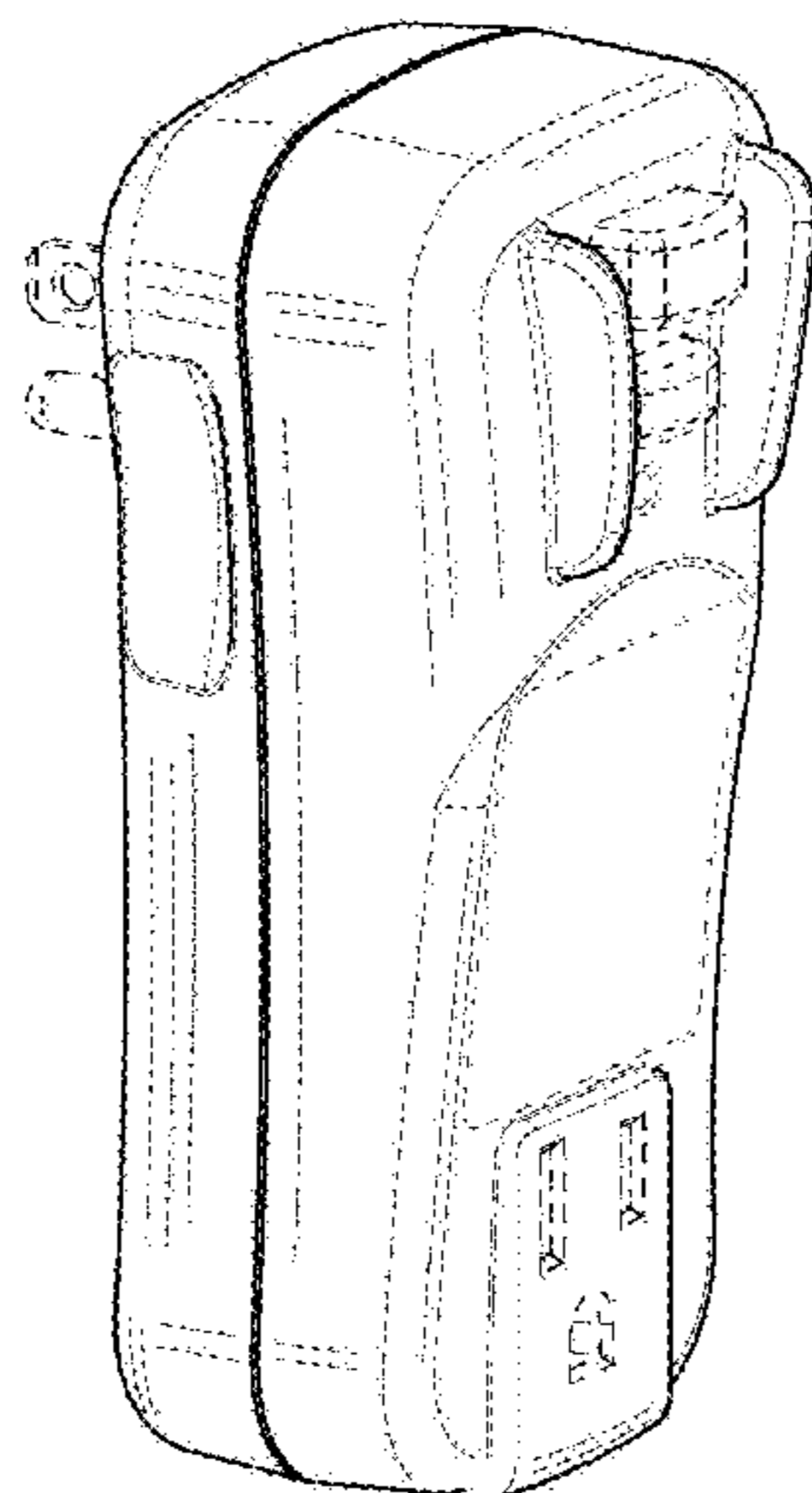
The ornamental design for an arc fault circuit interrupter
 outlet adapter, substantially as shown and described.

DESCRIPTION

FIG. 1 is a top perspective view of an arc fault circuit
 interrupter outlet adapter;
 FIG. 2 is a bottom perspective view thereof;
 FIG. 3 is a top view thereof;
 FIG. 4 is a right view, with the left side view a mirror image
 thereof;
 FIG. 5 is a bottom view thereof;
 FIG. 6 is a rear view thereof; and,
 FIG. 7 is a front view thereof.

The broken lines in the drawings are for the purpose of
 illustrating portions of the arc fault circuit interrupter outlet
 adapter that form no part of the claimed design.

1 Claim, 7 Drawing Sheets



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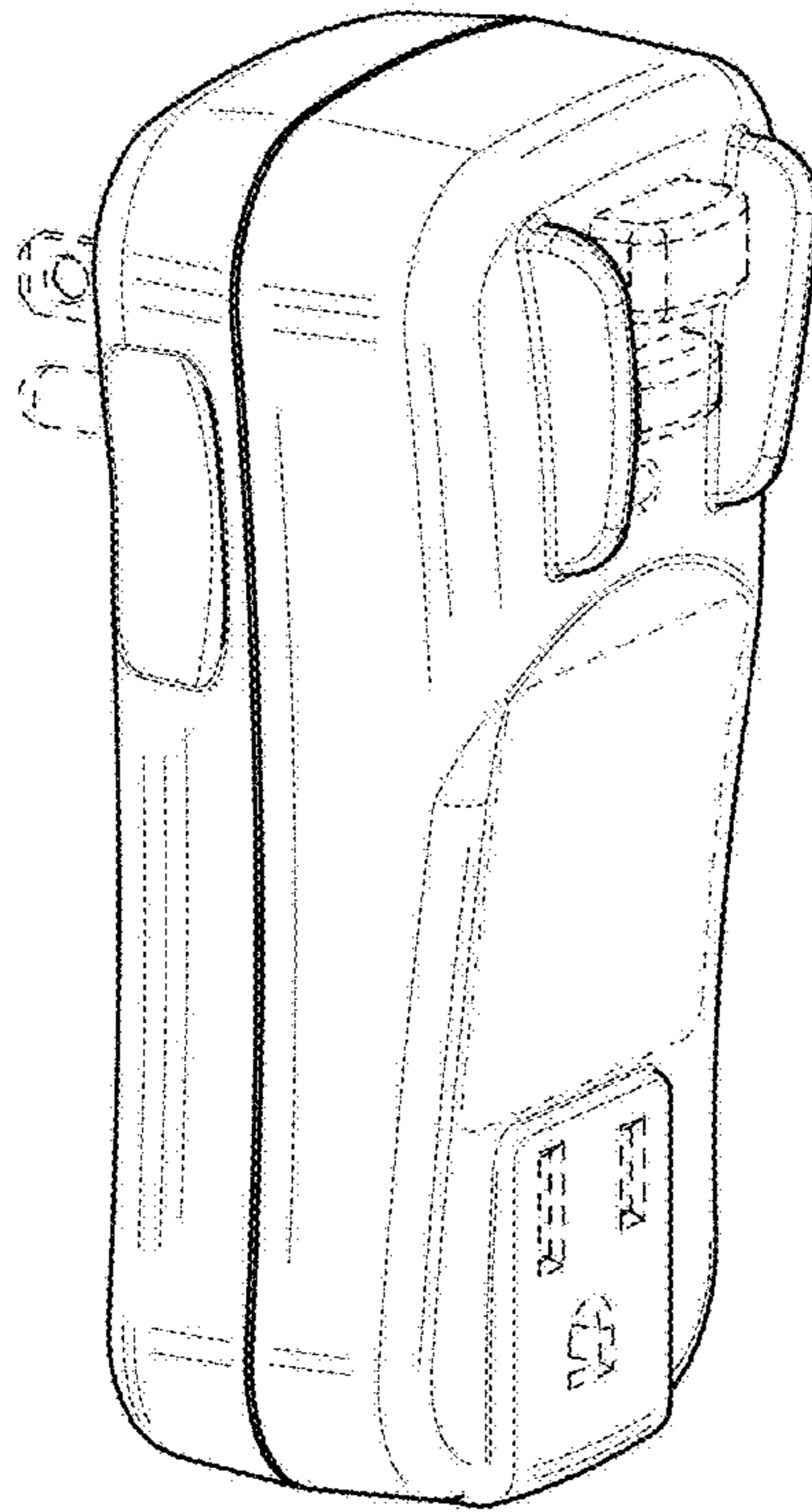


FIG. 1

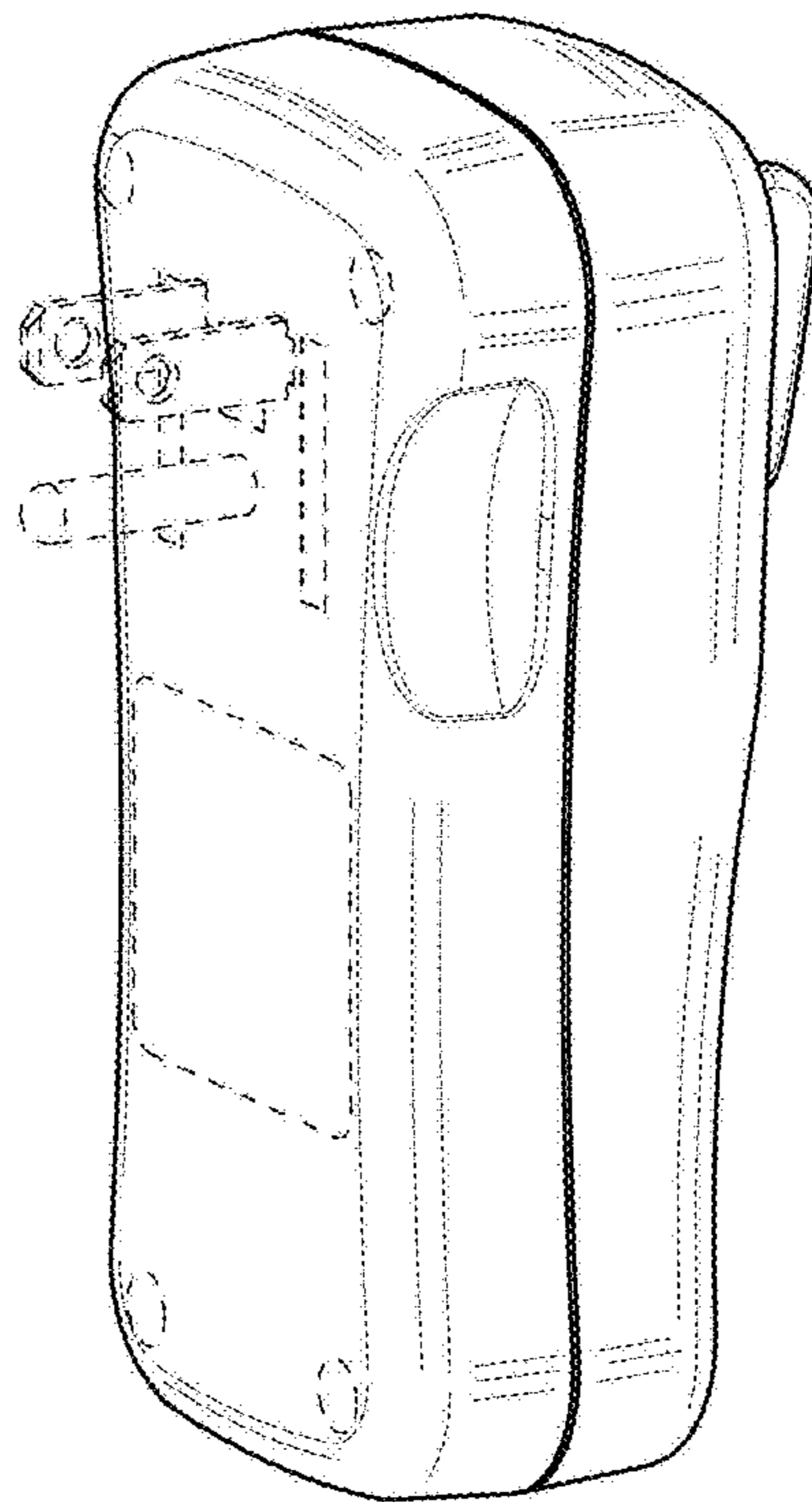


FIG. 2

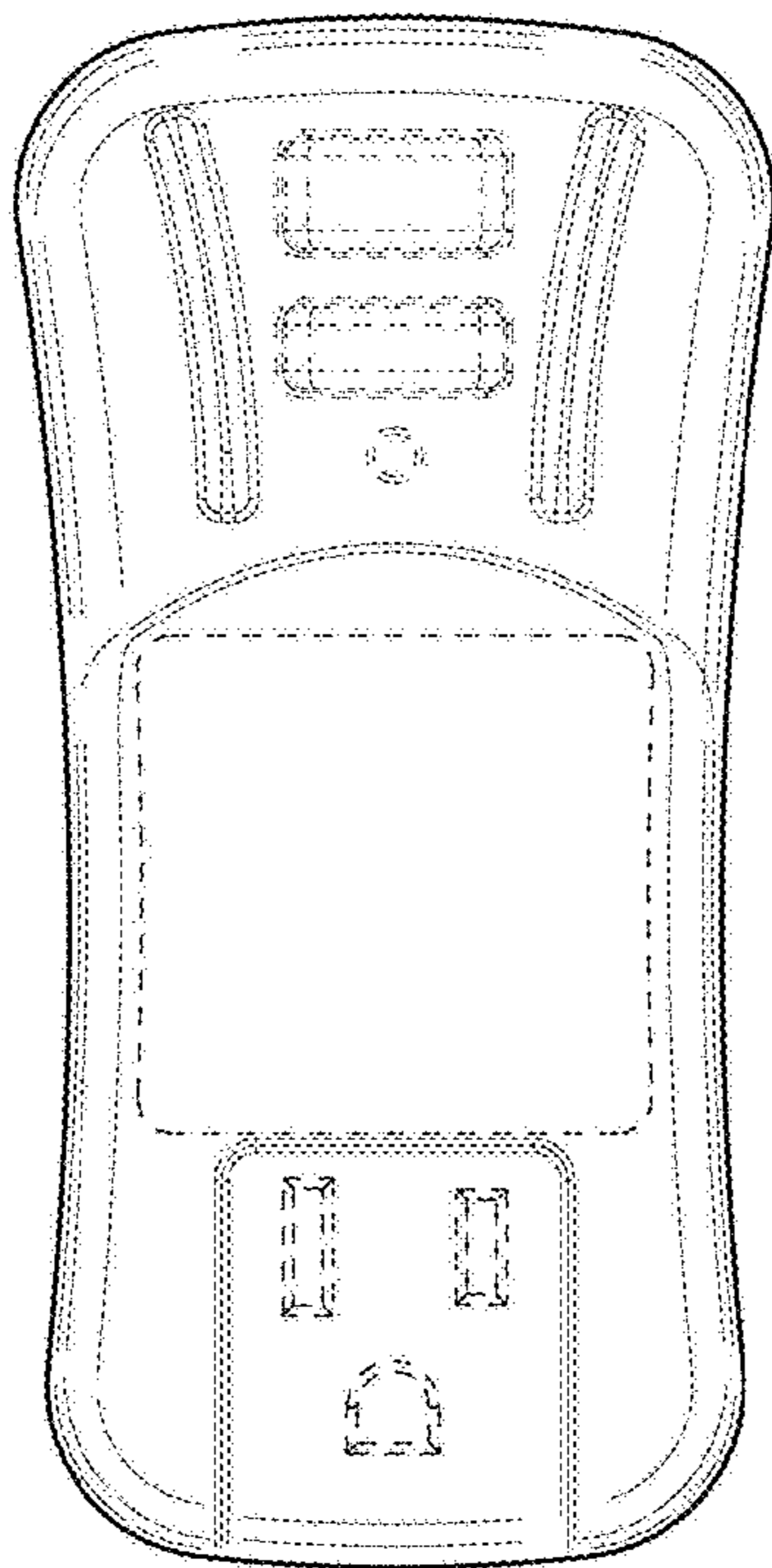


FIG. 3

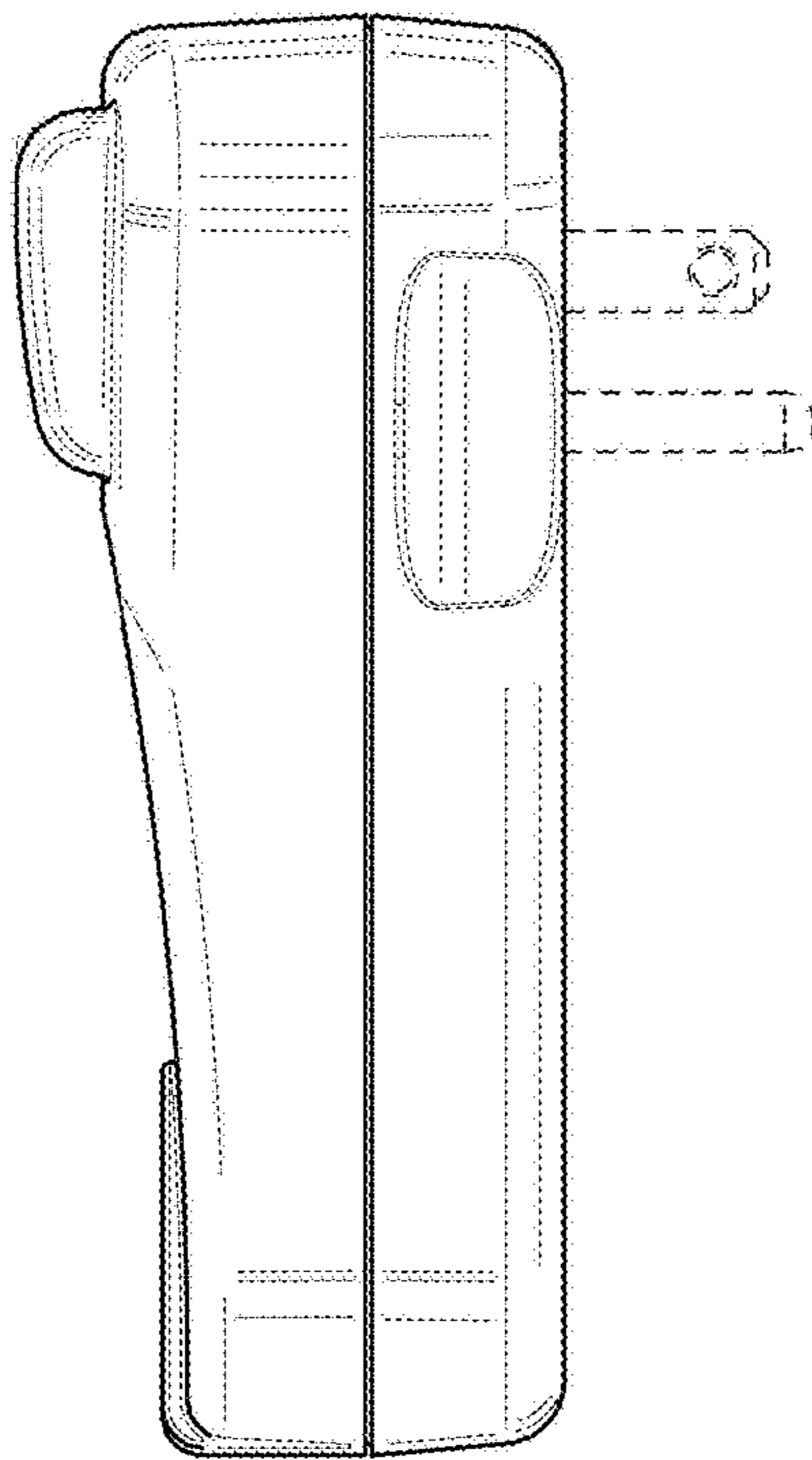


FIG. 4

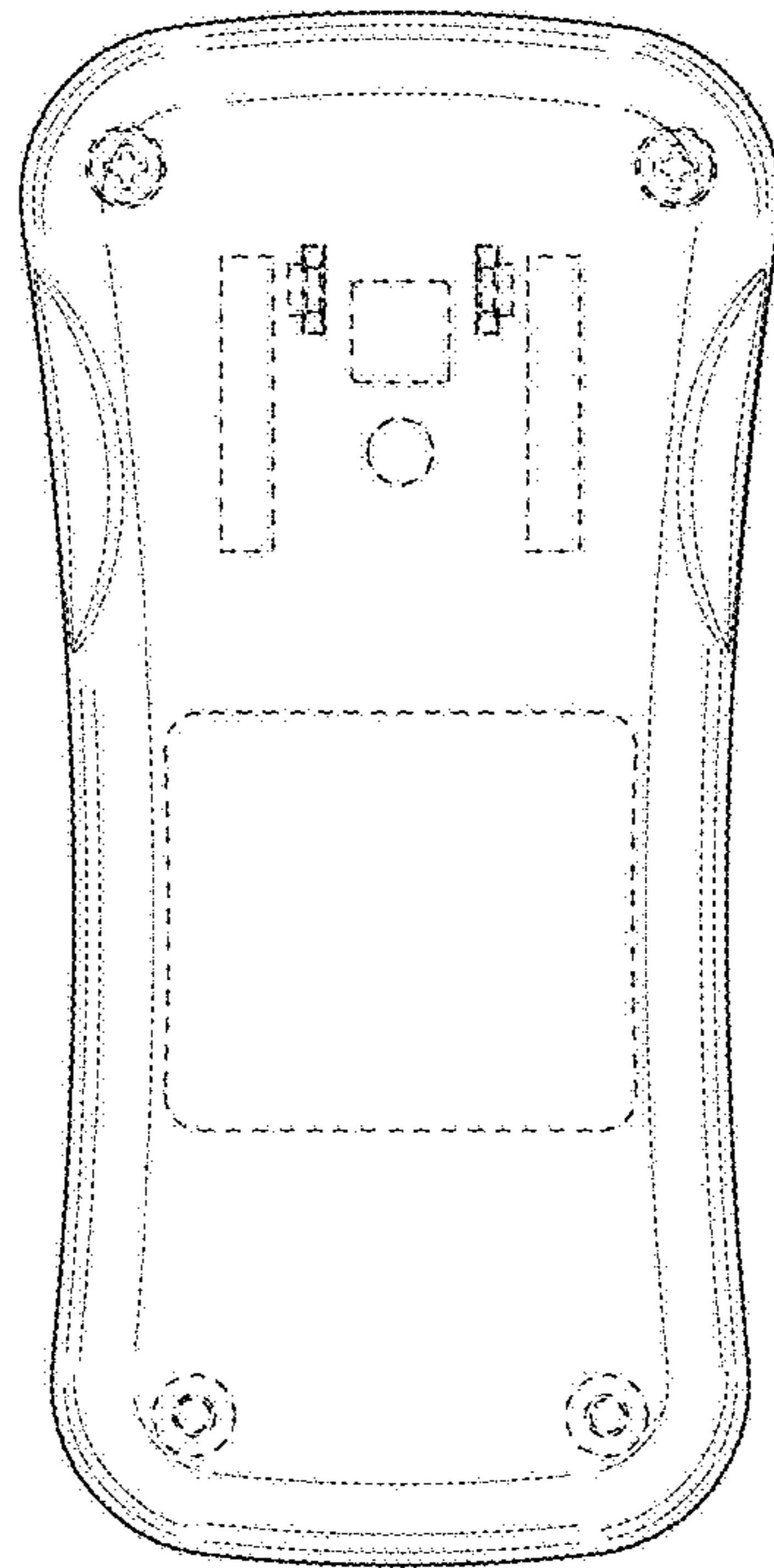


FIG. 5

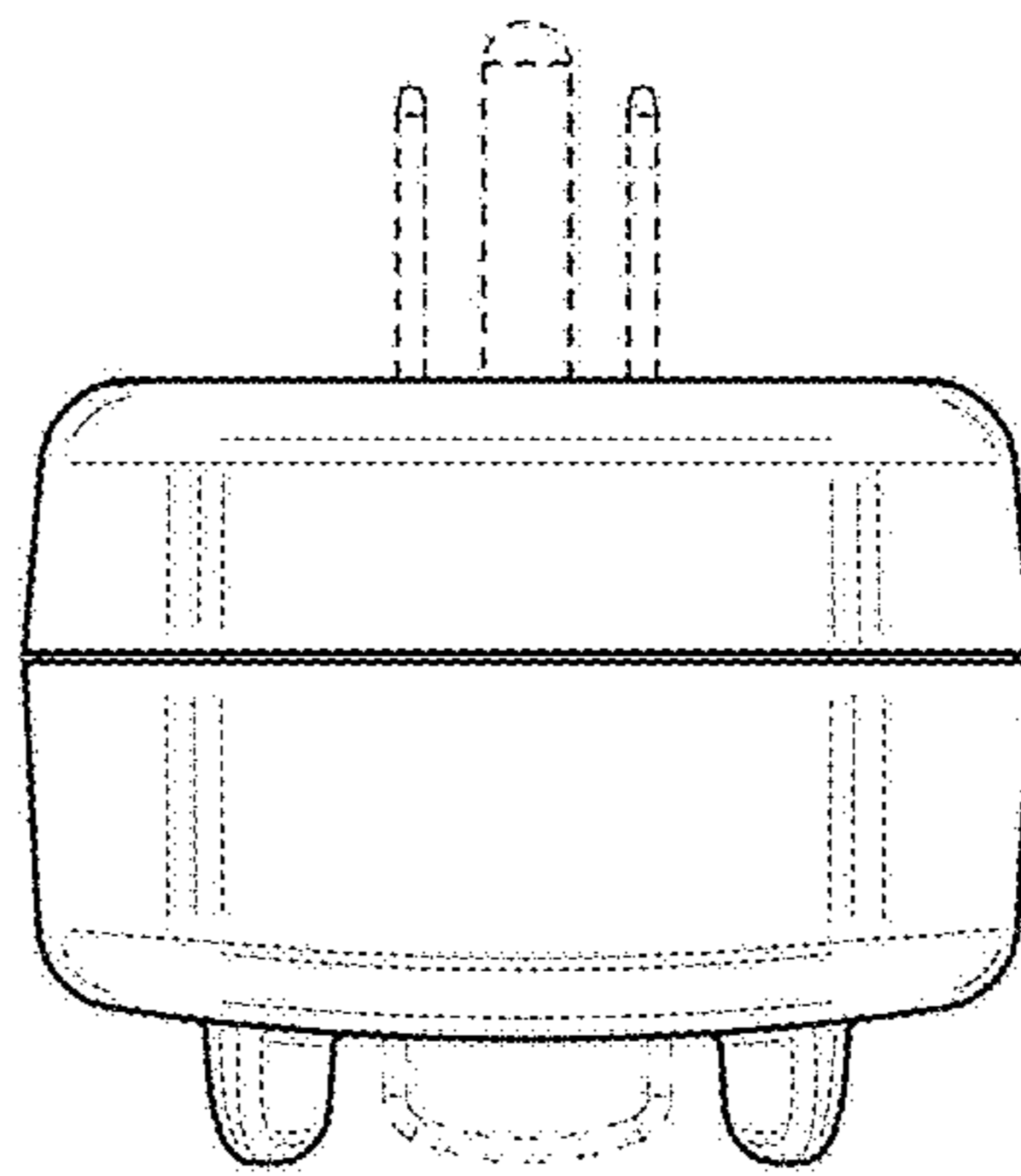


FIG. 6

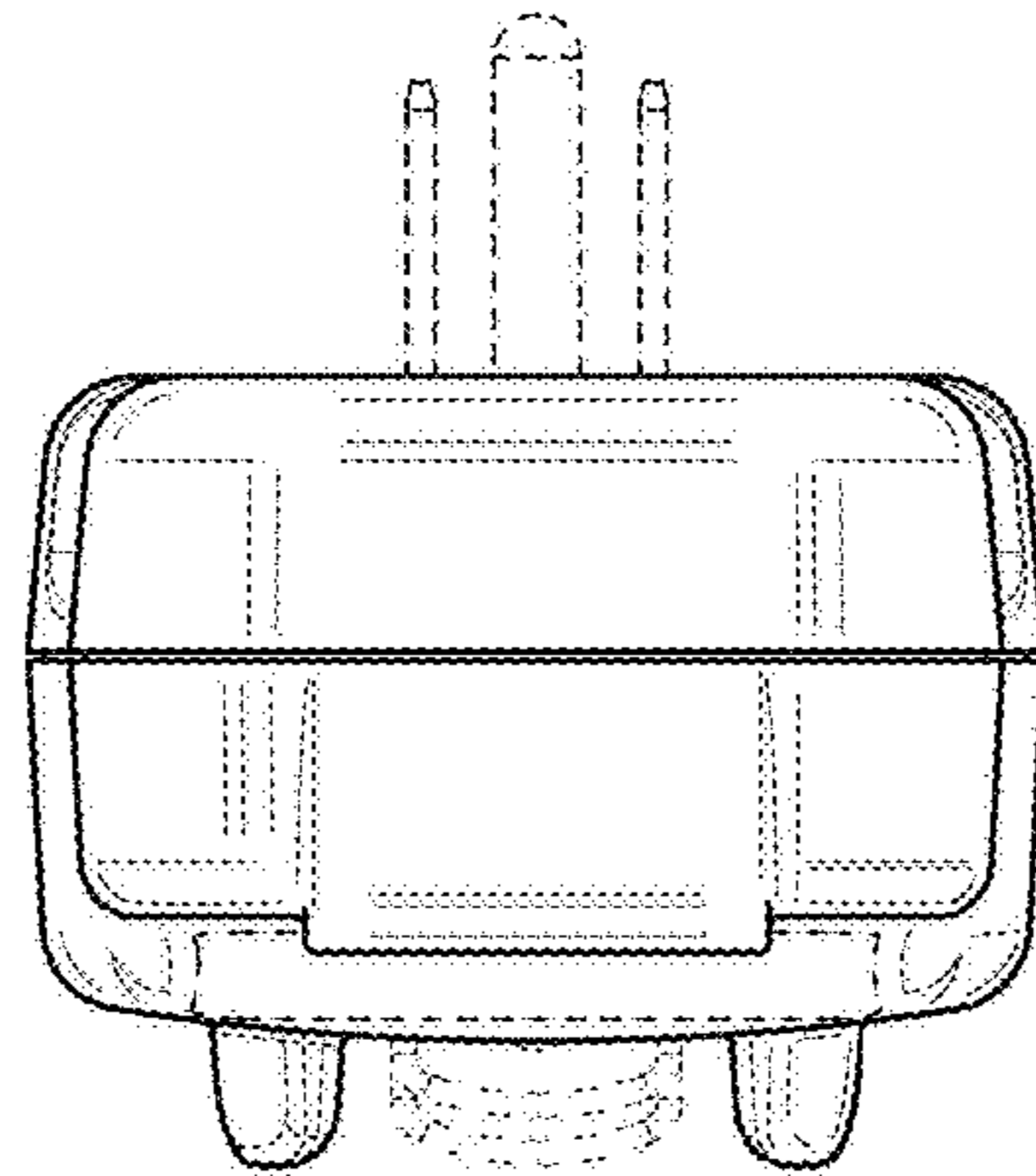


FIG. 7