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(12) **United States Design Patent** (10) **Patent No.:** **US D866,564 S**
Lee et al. (45) **Date of Patent:** **** Nov. 12, 2019**

(54) **STAND FOR ELECTRONIC DEVICE**

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(**) Term: **15 Years**

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(51) **LOC (12) Cl.** **08-07**

(52) **U.S. Cl.**
USPC **D14/447**

(58) **Field of Classification Search**
USPC D14/447, 432, 434, 439, 440, 451, 452, D14/457, 239, 250, 253, 203.7; D8/363, D8/373, 380; D6/406.3, 406.4, 406.5, D6/406.6; D12/415; D21/330, 331; D3/218

CPC A47B 21/04; A47B 2097/006; A47B 2097/005; A47B 2023/049; A45C 2011/002; A45C 2011/003; F16M 2200/00; F16M 13/00

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a stand for electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a front, top and right perspective view of stand for electronic device showing claimed design;

FIG. 2 is a front view thereof;

FIG. 3 is a back view thereof;

FIG. 4 is a left side view thereof;

FIG. 5 is a right side view thereof;

FIG. 6 is a top view thereof;

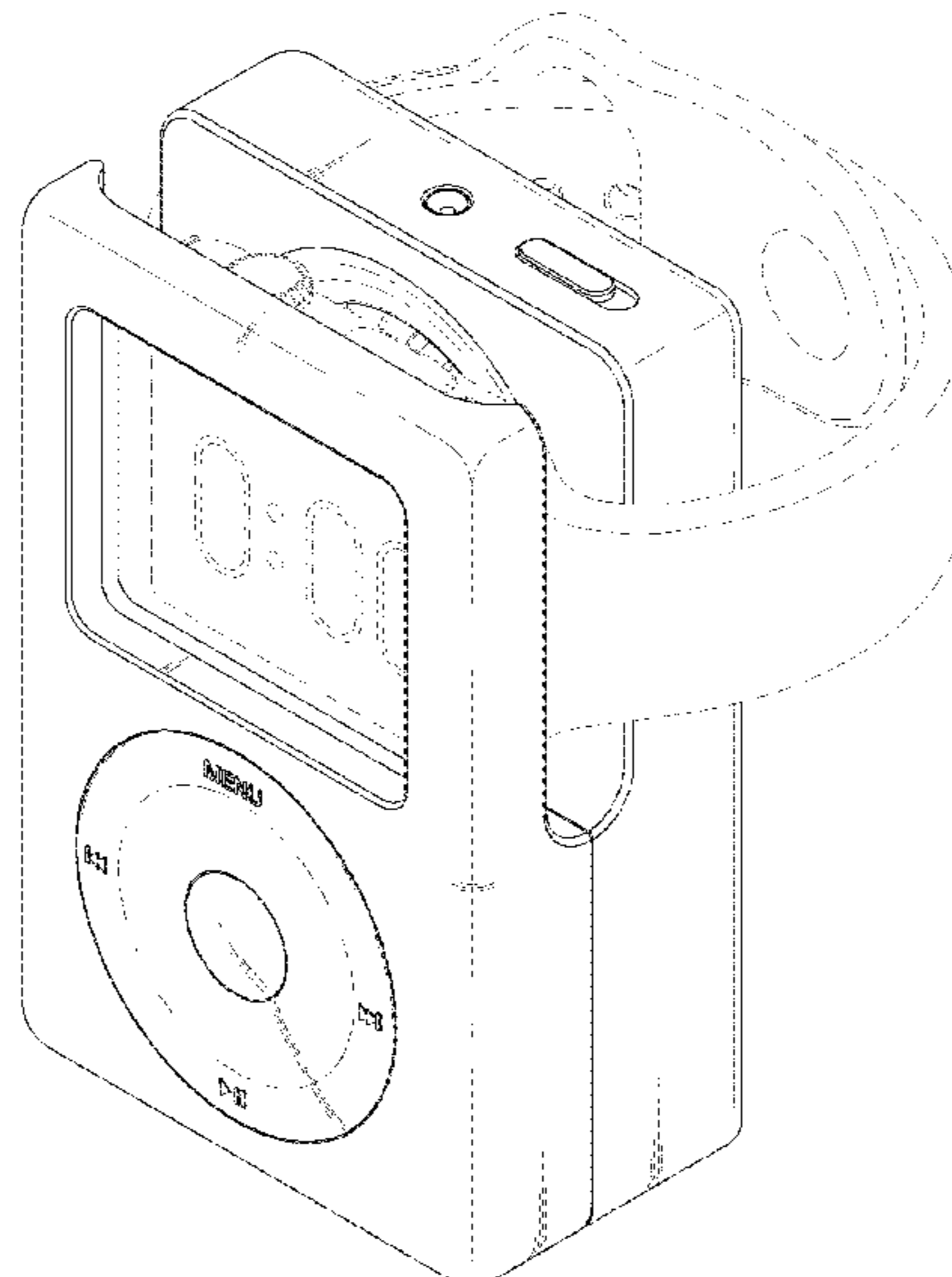
FIG. 7 is a bottom view thereof;

FIG. 8 is a back, bottom and right perspective view thereof; and,

FIG. 9 is a front, top and right perspective view thereof in use.

The broken lines shown in FIGS. 1, 2, 6, and 7 depict parts of the stand for electric device and form no part of the claimed design. The broken lines shown in FIG. 9 depict environment and form no part of the claimed design.

1 Claim, 8 Drawing Sheets



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FIG. 1

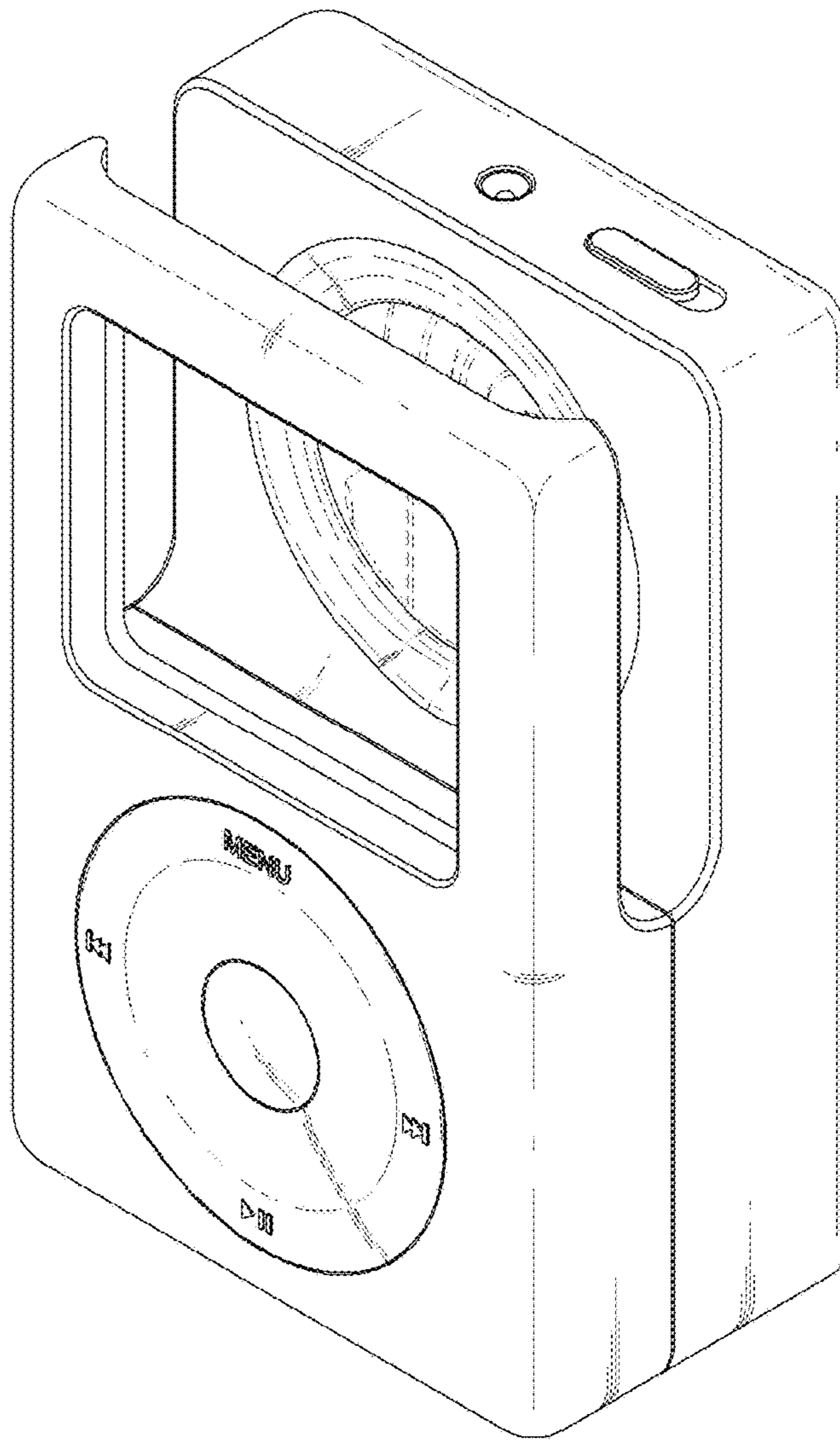


FIG. 2

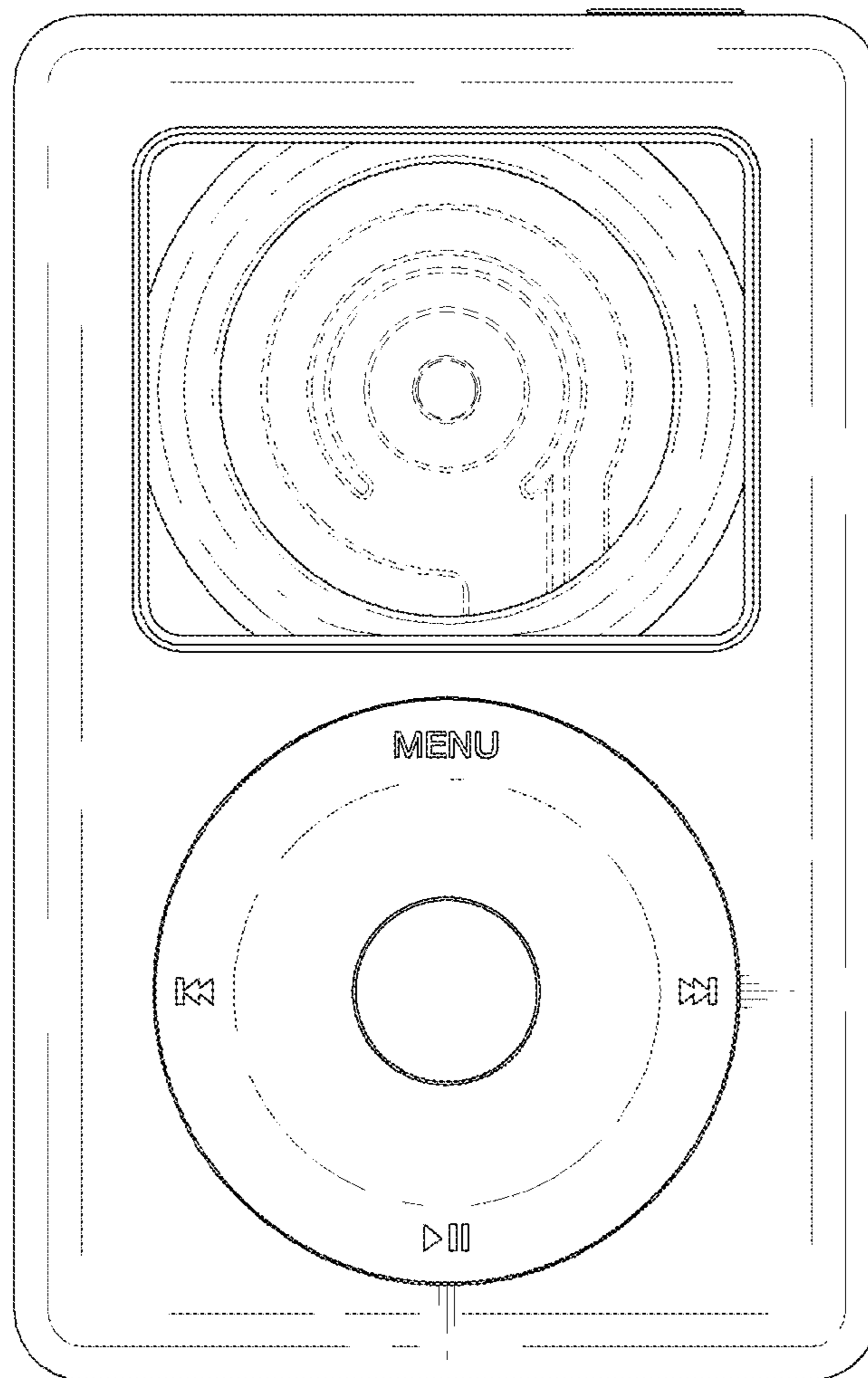


FIG. 3

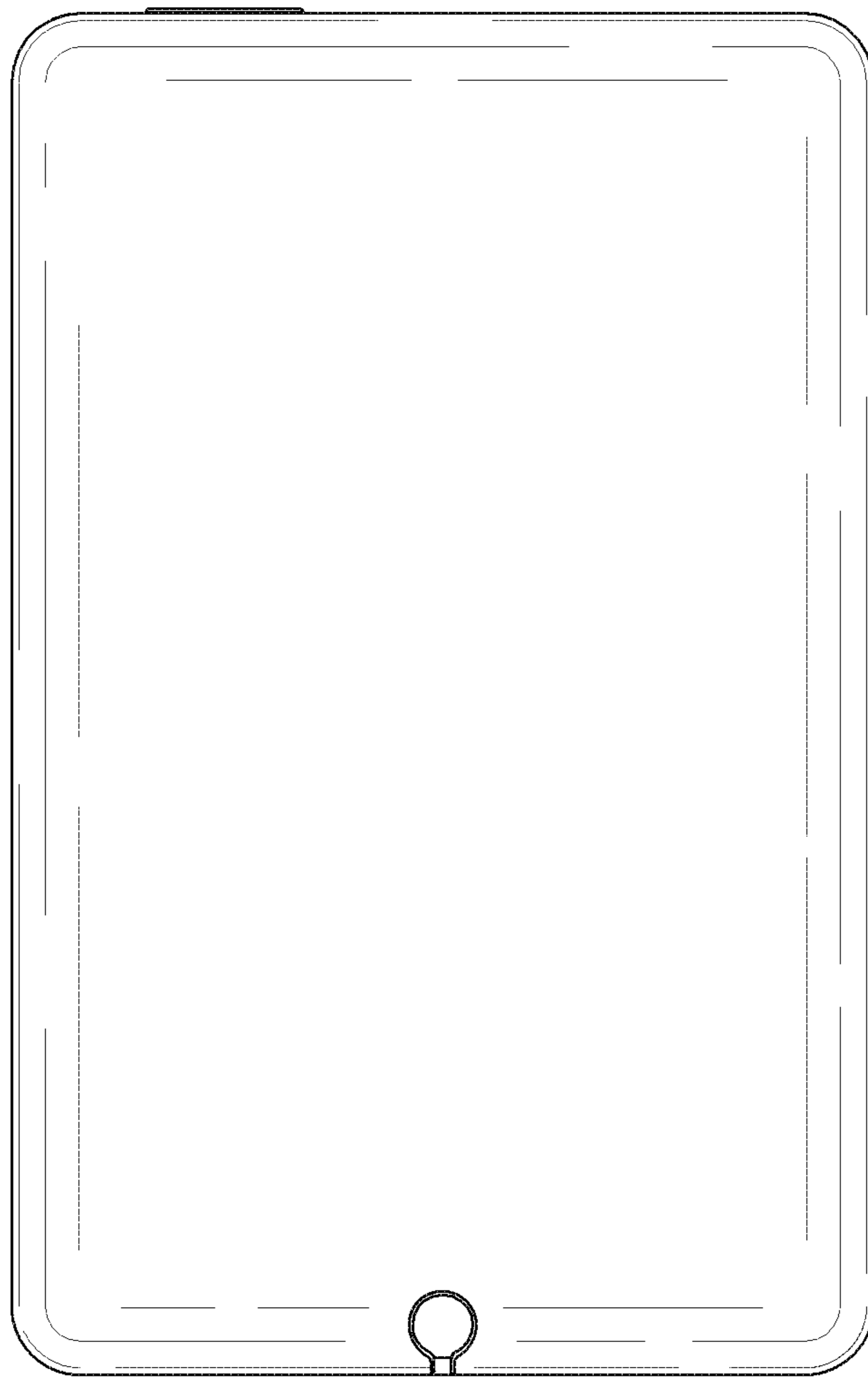


FIG. 4

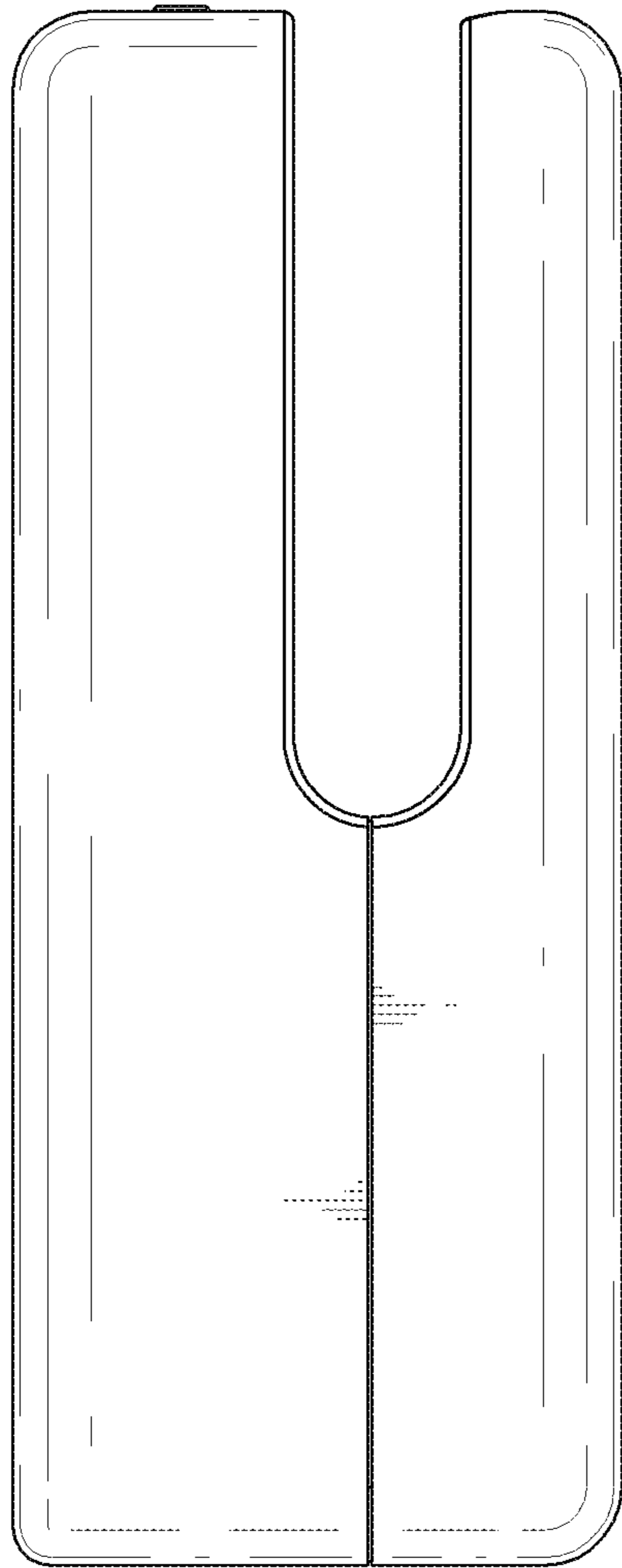


FIG. 5

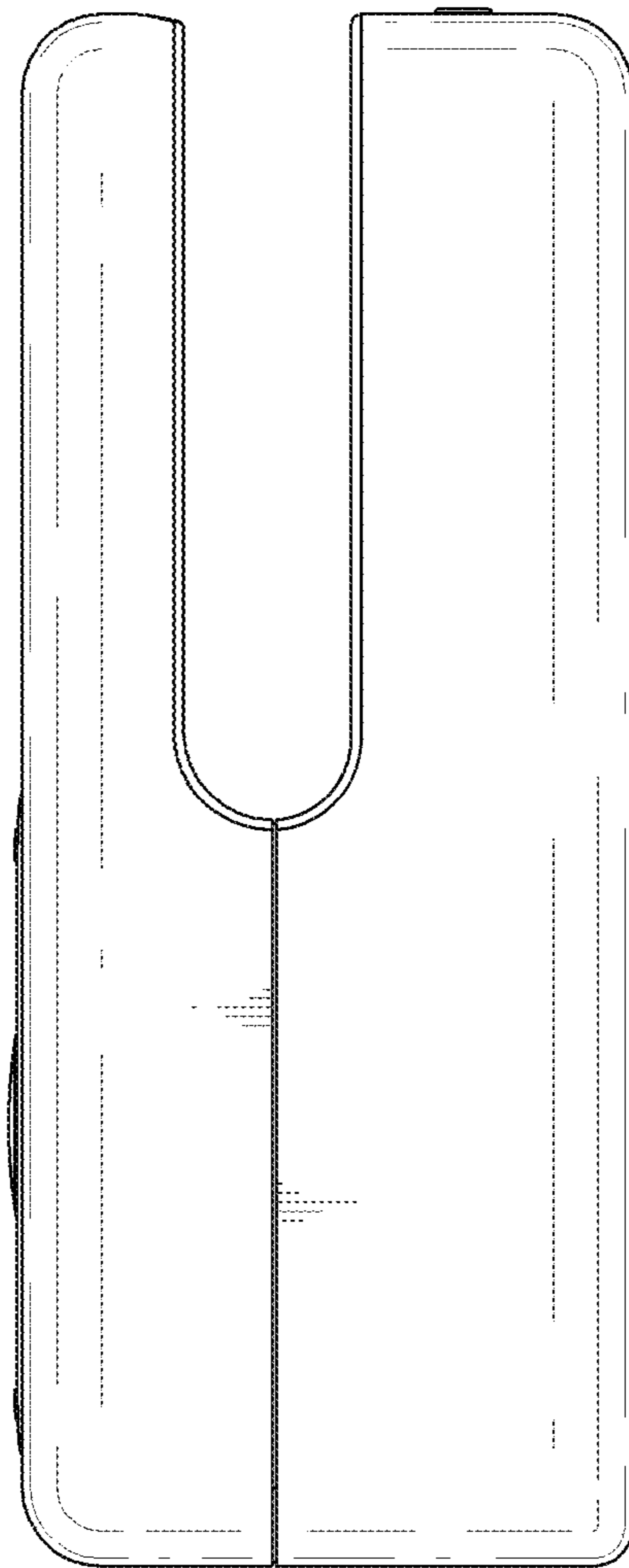


FIG. 6

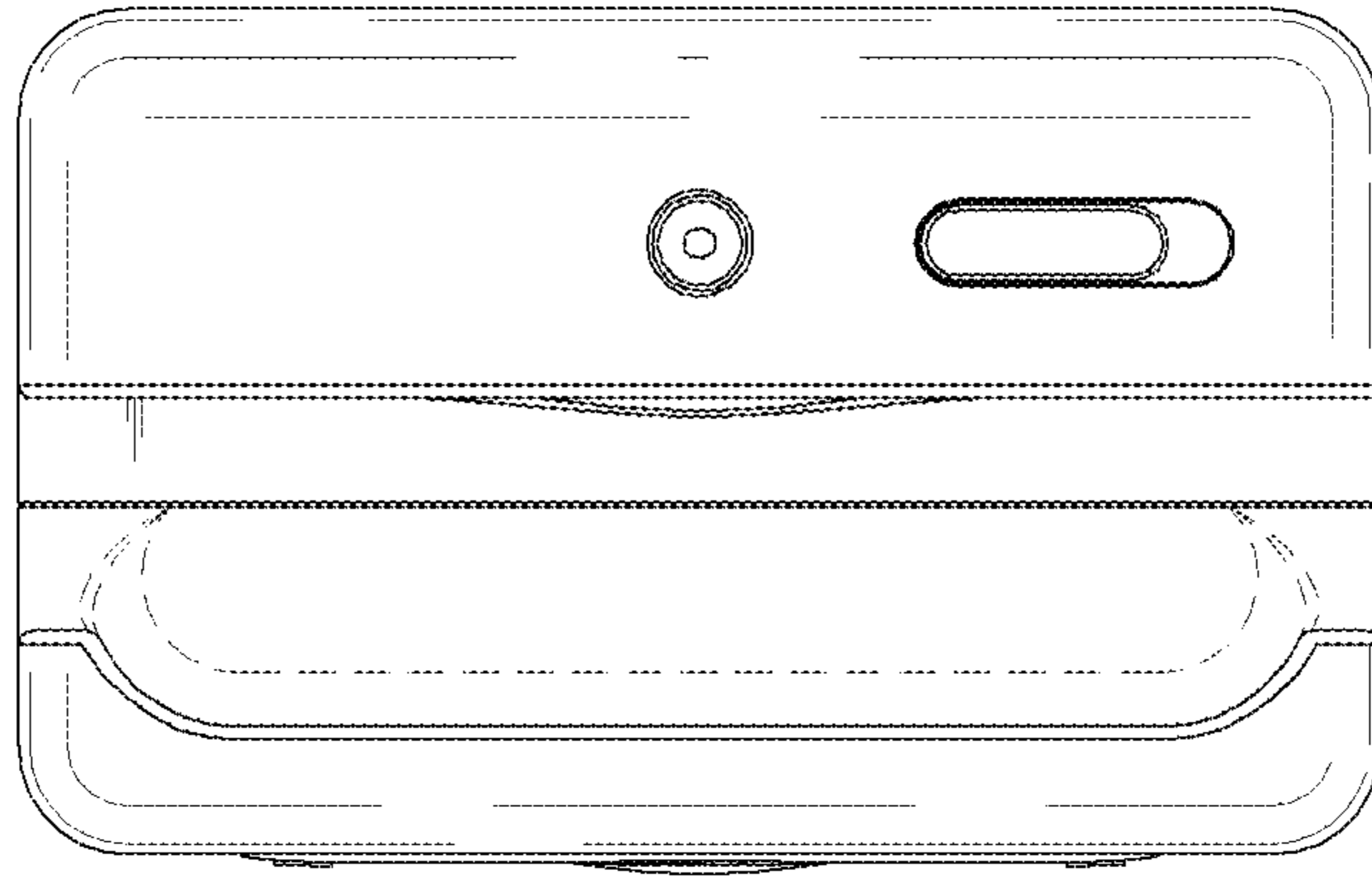


FIG. 7

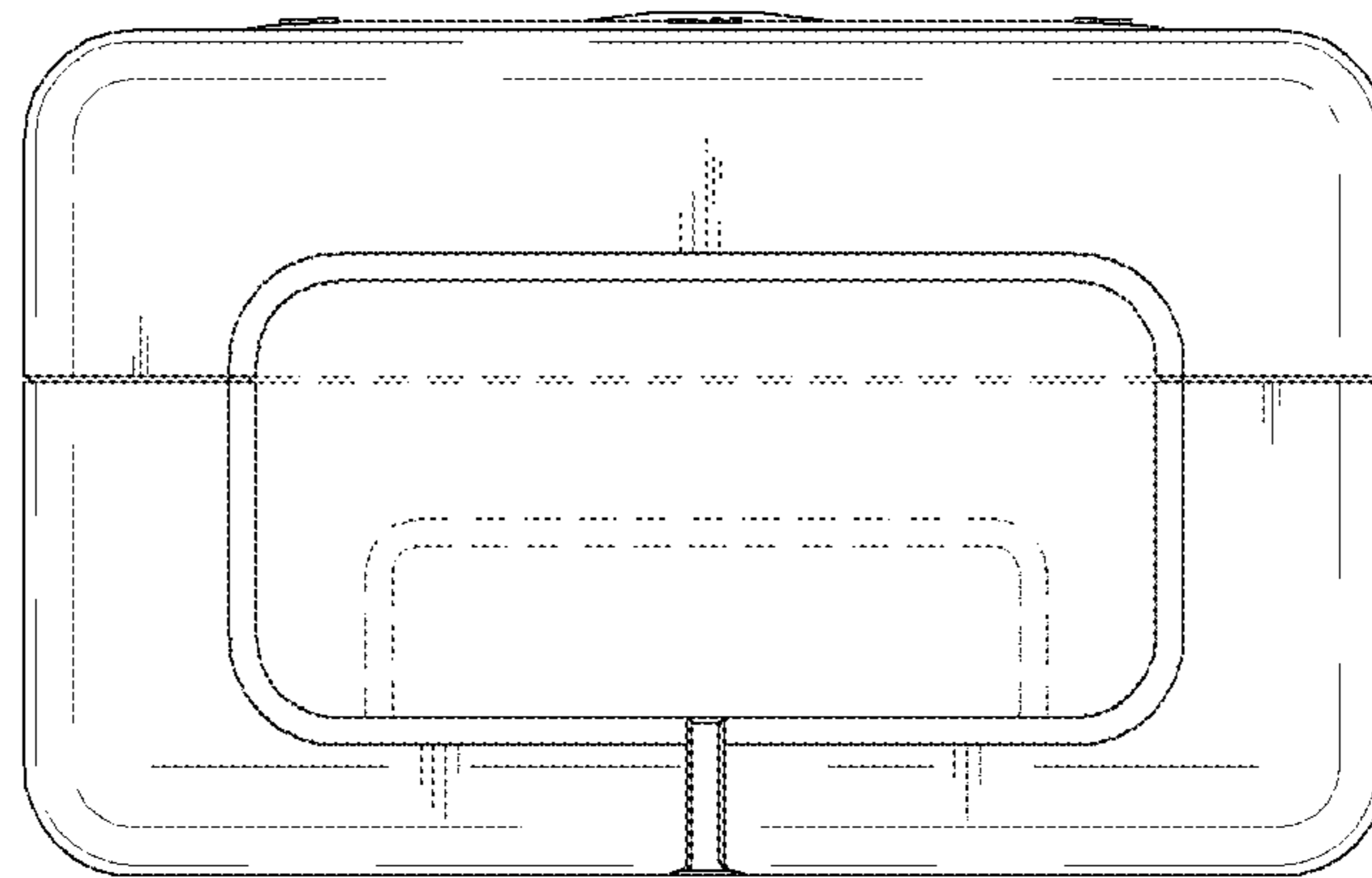


FIG. 8

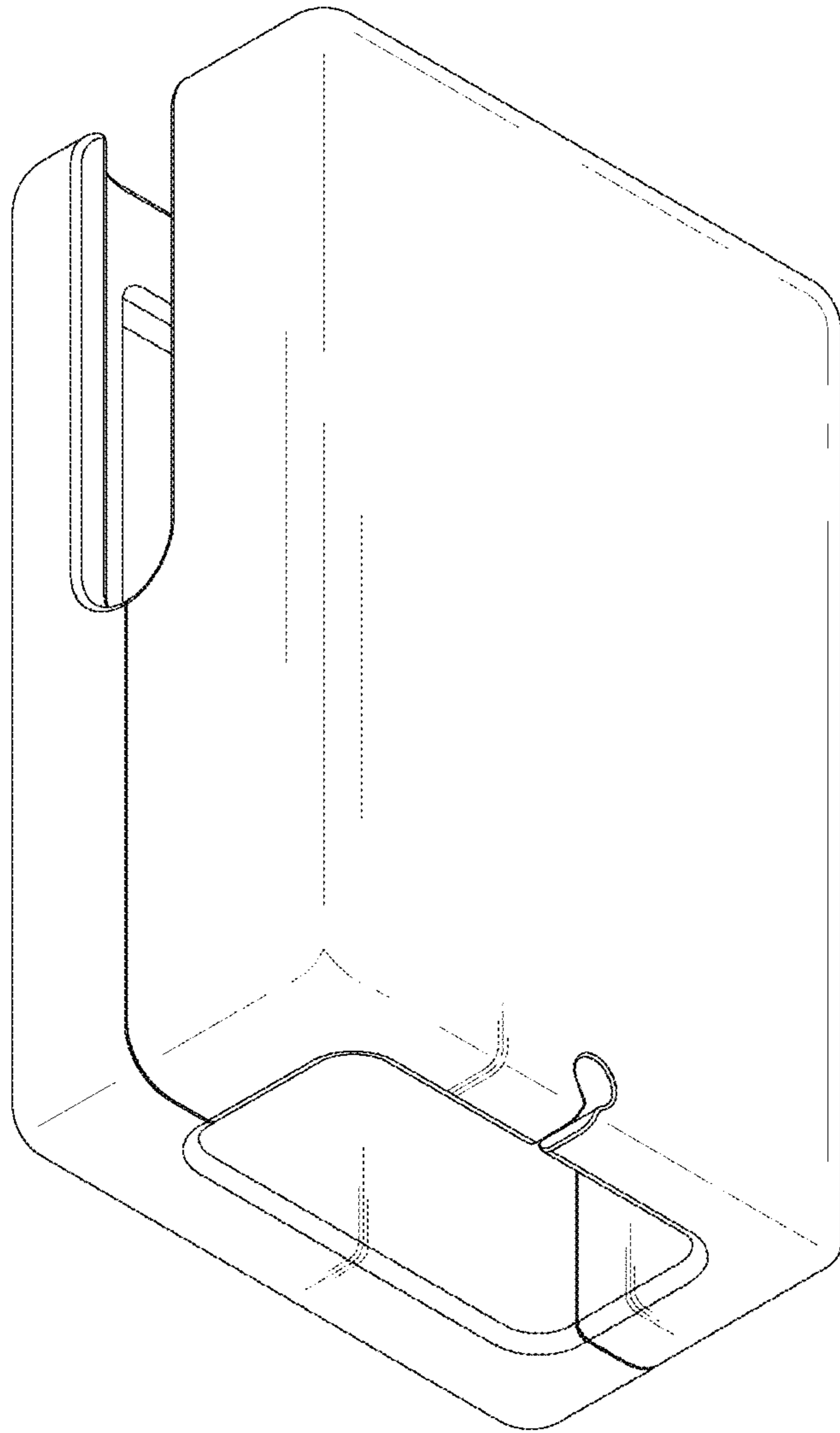


FIG. 9

