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(12) **United States Design Patent** (10) **Patent No.:** **US D866,485 S**
Aromin et al. (45) **Date of Patent:** **** Nov. 12, 2019**

(54) **IN-LINE GROUND FAULT CIRCUIT INTERRUPTER (GFCI)**

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(**) Term: **15 Years**

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(51) **LOC (12) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/160**

(58) **Field of Classification Search**
USPC D13/110, 123, 133, 134, 137.1, 137.3, D13/137.4, 138.1, 138.2, 146, 147, 149, D13/153, 154, 160, 173, 184, 199
CPC H01H 9/54; H01H 13/86; H01H 83/00; H01R 12/53; H01R 13/68; H01R 39/00; H02H 1/00; H02H 3/00; H02H 3/16; H02H 3/162; H02H 9/08; H05K 5/00
See application file for complete search history.

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Primary Examiner — Angela J Lee
Assistant Examiner — Shawn T Gingrich

(57) **CLAIM**

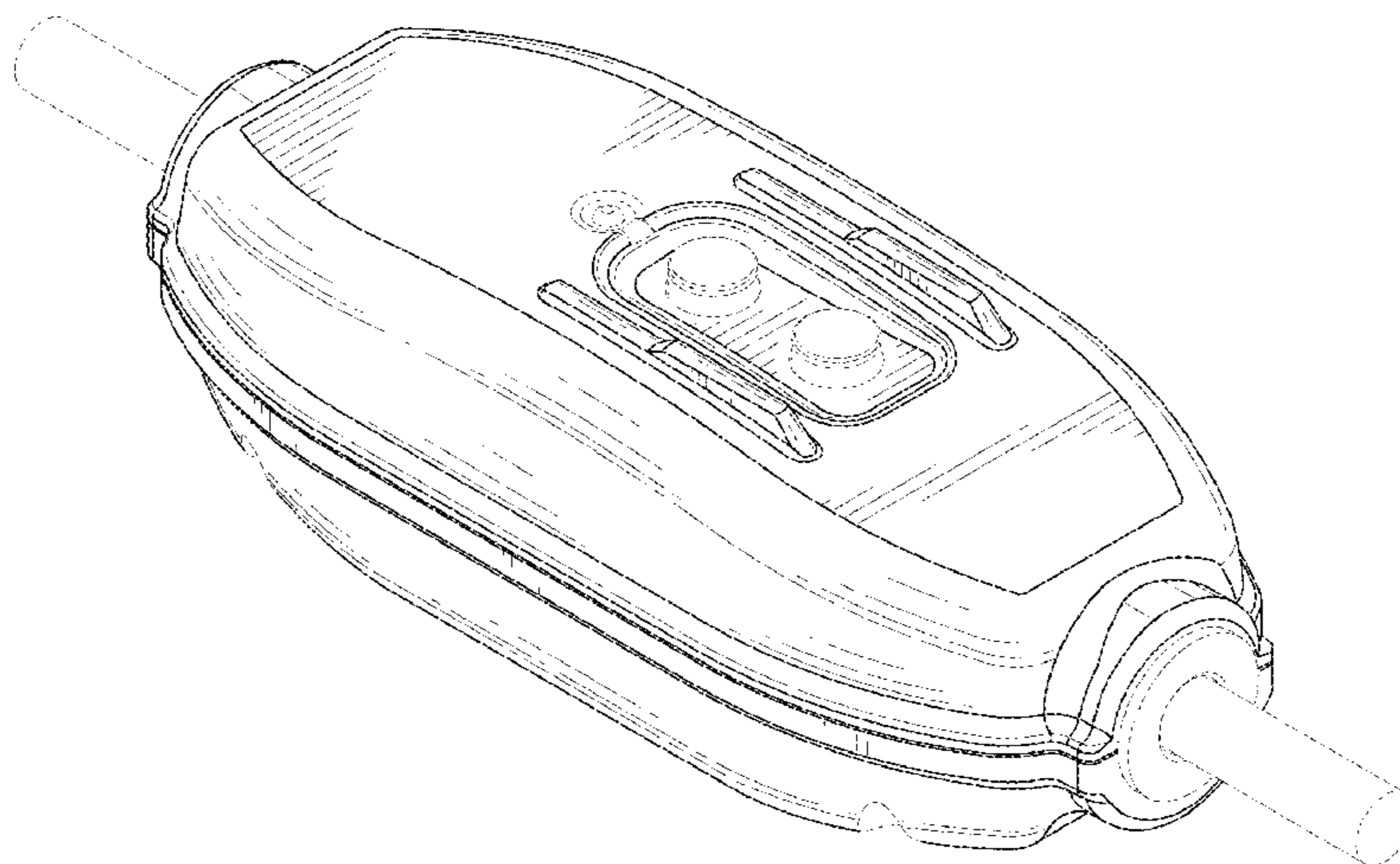
The ornamental design for an in-line ground fault circuit interrupter (GFCI), as shown and described.

DESCRIPTION

FIG. 1 is a top perspective view of the in-line ground fault circuit interrupter (GFCI) employing our new design; FIG. 2 is a top view of FIG. 1; FIG. 3 is a bottom view of FIG. 1; FIG. 4 is a right side view of FIG. 1; FIG. 5 is a left side view of FIG. 1; FIG. 6 is a front end view of FIG. 1; and, FIG. 7 is a rear end view of FIG. 1.

The broken lines in the drawings represent portions of the in-line ground fault circuit interrupter (GFCI) that form no part of the claimed design.

1 Claim, 7 Drawing Sheets



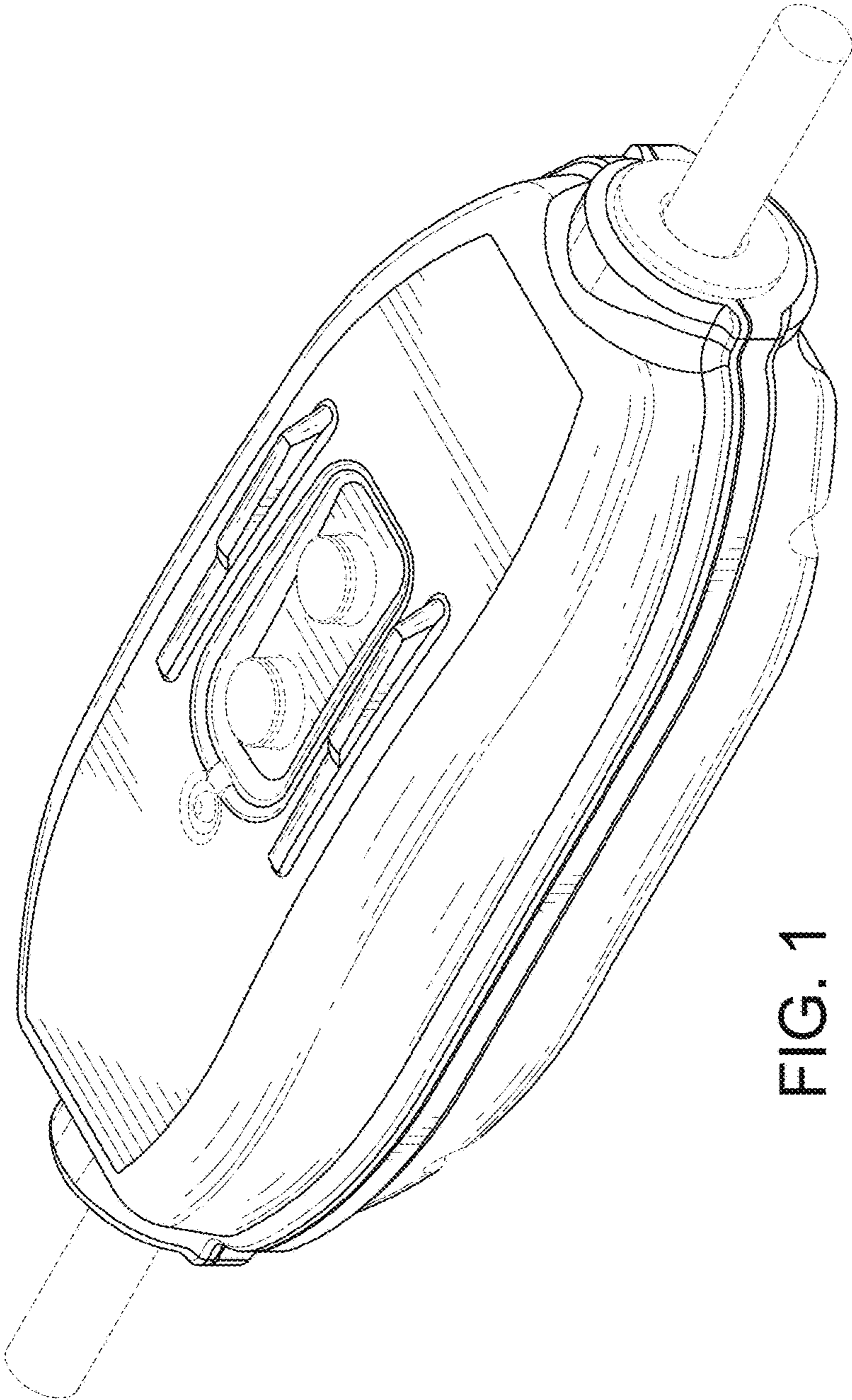


FIG. 1

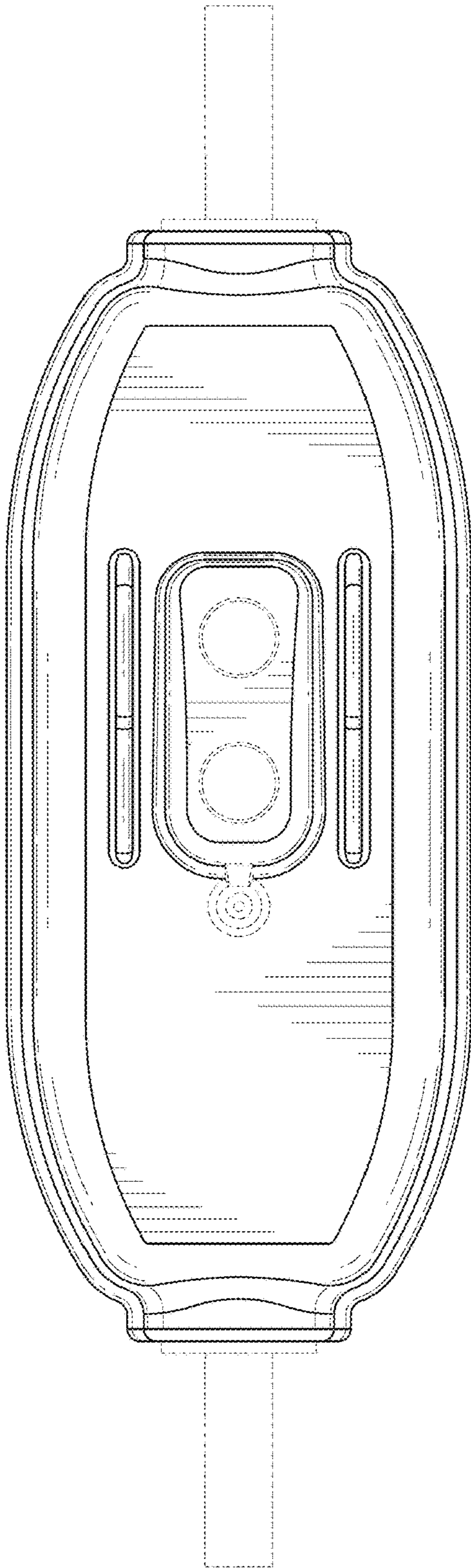


FIG. 2

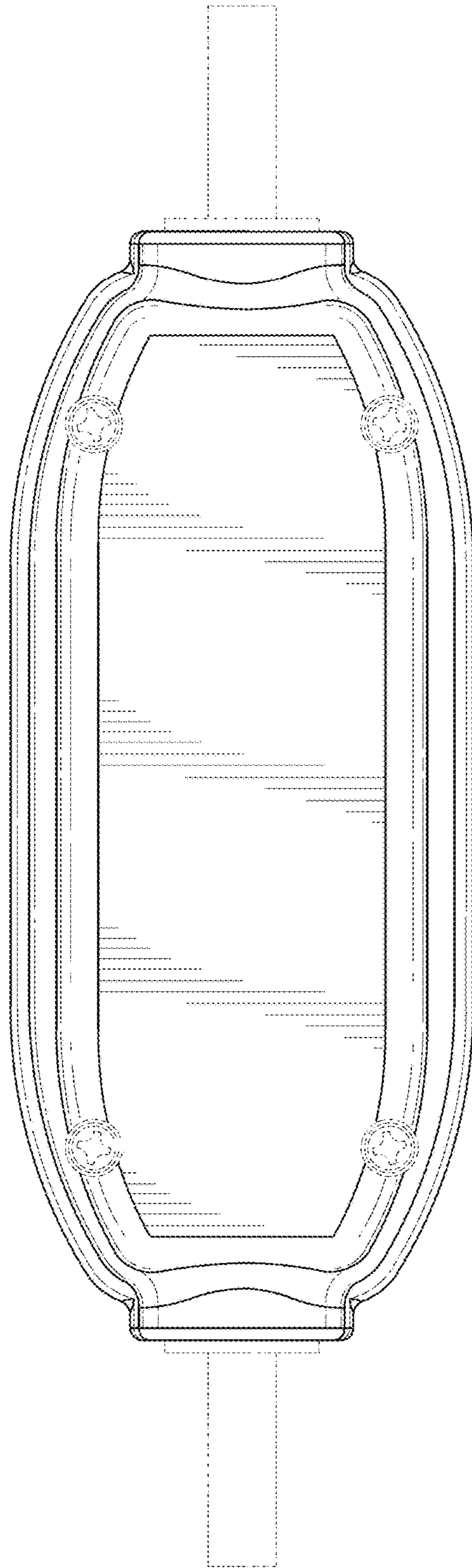


FIG. 3

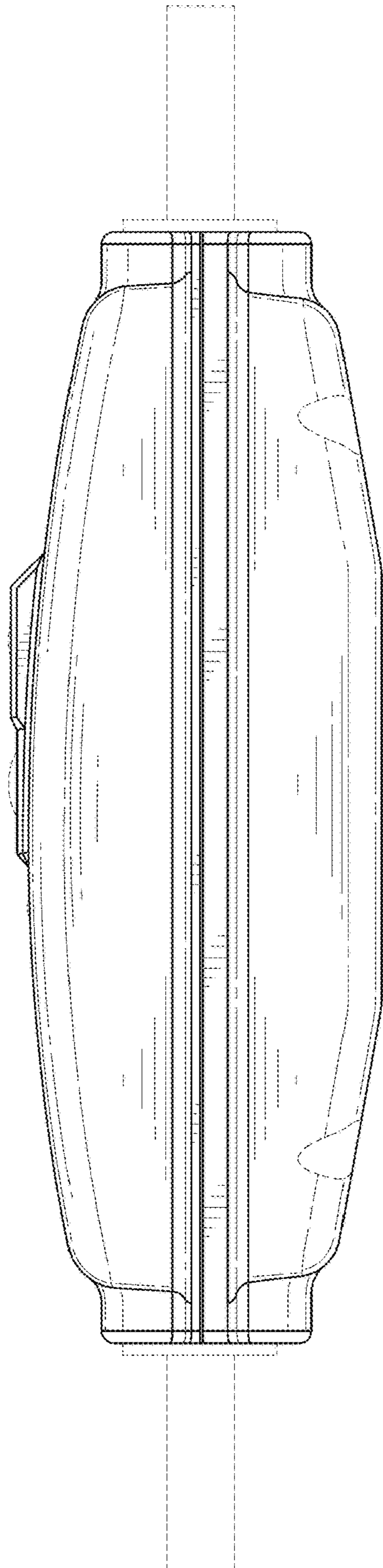


FIG. 4

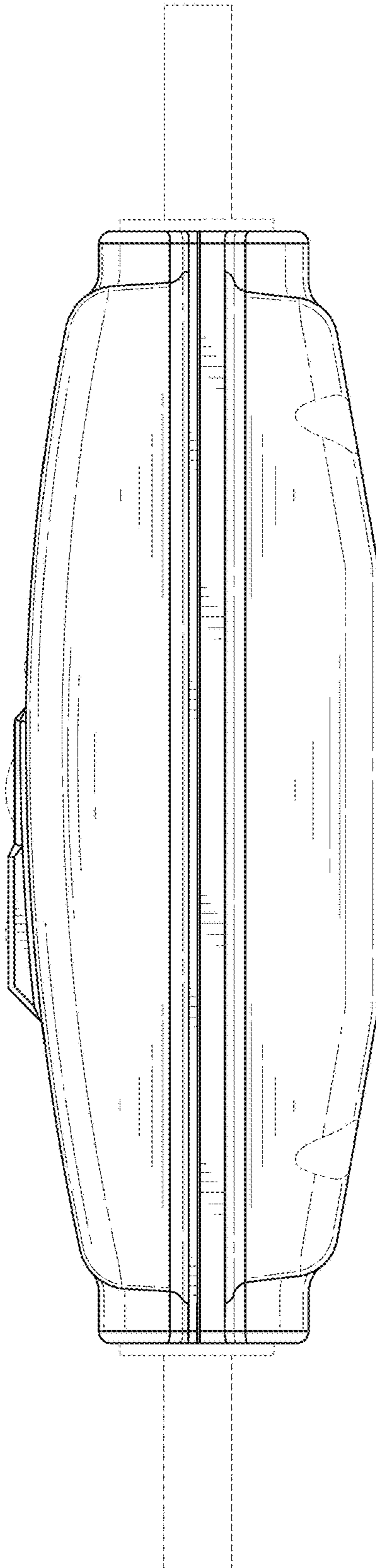


FIG. 5

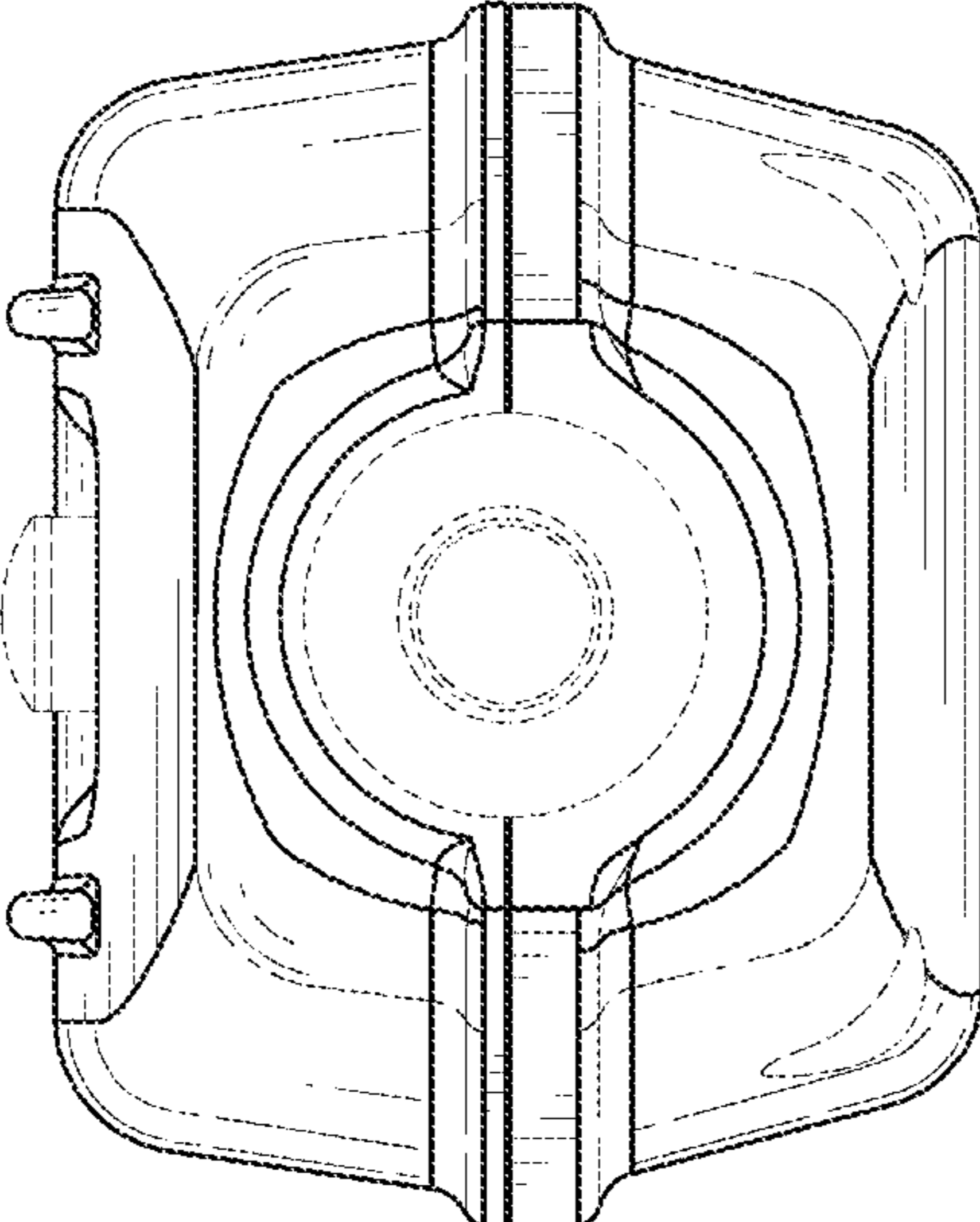


FIG. 6

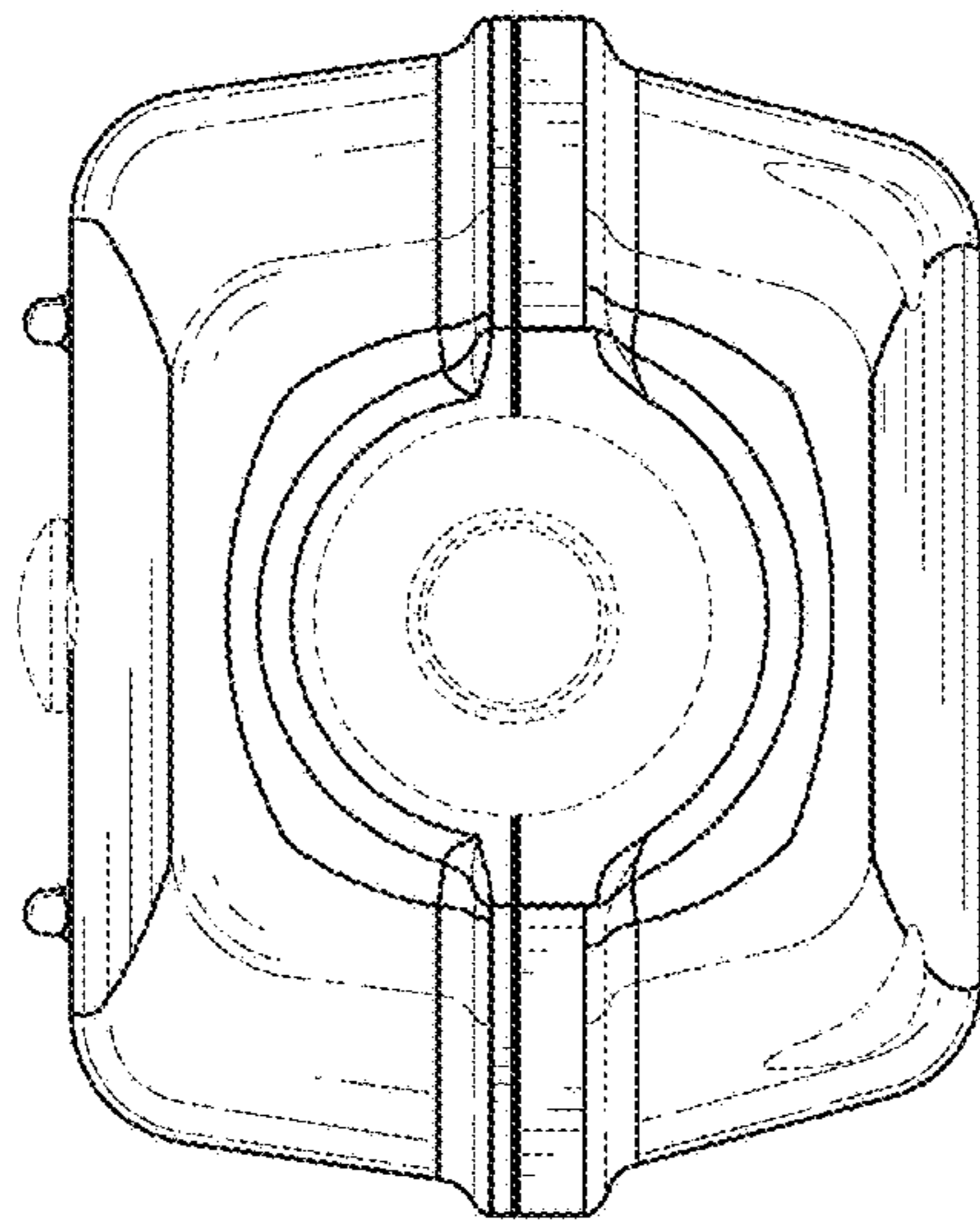


FIG. 7