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(12) **United States Design Patent** (10) **Patent No.:** **US D866,356 S**
Elrod et al. (45) **Date of Patent:** **** Nov. 12, 2019**

(54) **ENCLOSURE FOR MAIN UNIT OF VOLTAGE, CURRENT, OR TEMPERATURE SENSOR**

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(**) Term: **15 Years**

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Related U.S. Application Data

(63) Continuation of application No. 29/597,622, filed on Mar. 17, 2017, now Pat. No. Des. 816,520.

(51) **LOC (12) Cl.** **10-04**

(52) **U.S. Cl.**
USPC **D10/52; D10/53**

(58) **Field of Classification Search**

USPC 10/49, 50, 52, 53, 55, 56, 60, 104.1, 10/106.6; D13/168, 184; D14/218
CPC .. F24F 11/001; F24F 11/0012; F24F 11/0032; F24F 11/0086; F24F 2011/0057; F23N 2041/02; F23N 2025/12; G08B 3/10; G08B 25/10; G08B 21/10; G08B 21/14; G08B 21/16; G08B 21/18; G08B 29/145; G08B 29/183; G05D 23/1902; G01N 33/0004; G01N 33/0037; G01N 33/0039; G01N 33/004; G01N 33/0054; G01N 33/006; G01N 33/0075

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for an enclosure for a main unit of a voltage, current, or temperature sensor, as shown and described.

DESCRIPTION

FIG. 1 is a top front right perspective view of an enclosure for a main unit of a voltage, current, or temperature sensor showing our new design.

FIG. 2 is a bottom rear left perspective view thereof.

FIG. 3 is a front elevation view thereof.

FIG. 4 is a right side elevation view thereof.

FIG. 5 is a left side elevation view thereof.

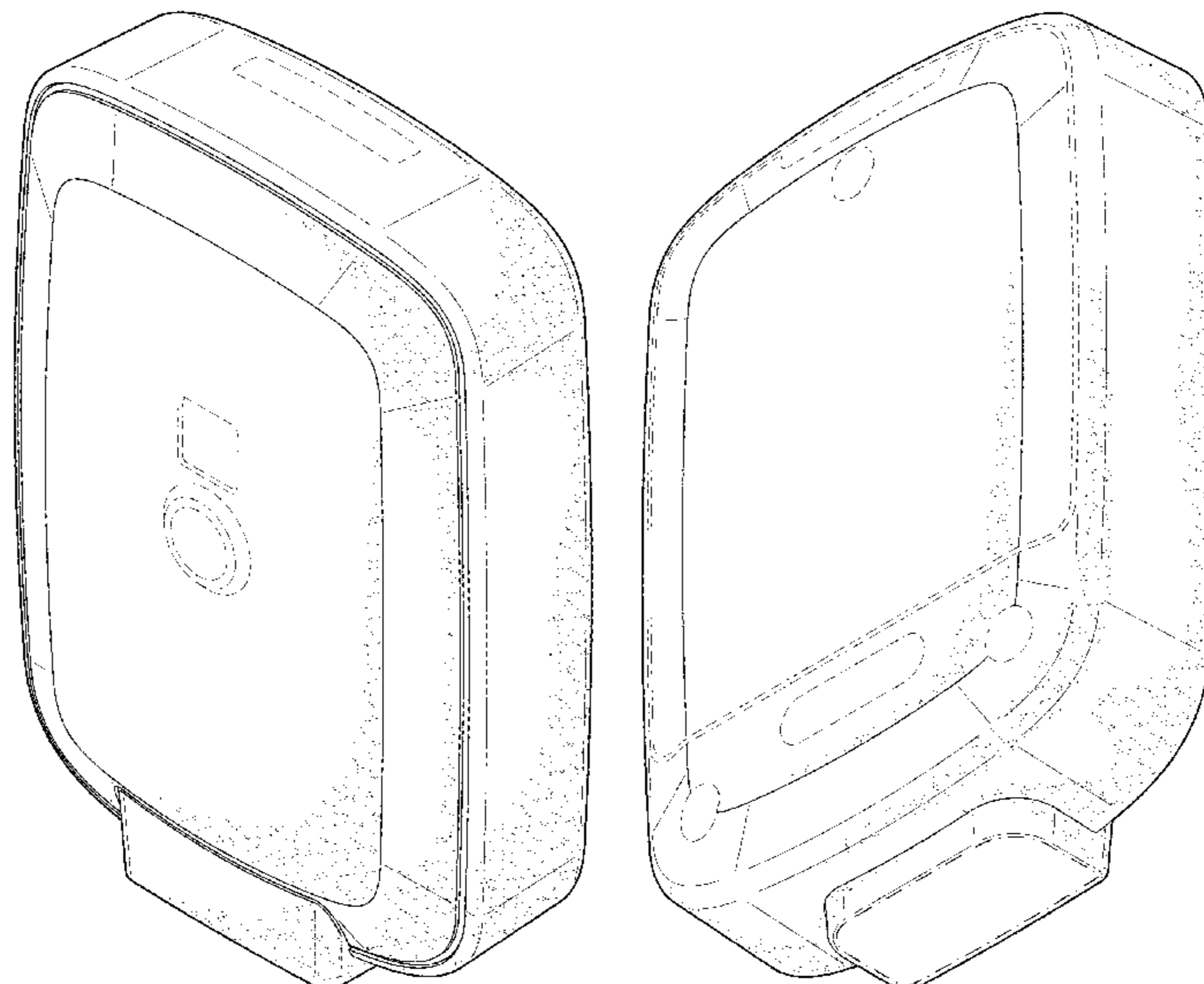
FIG. 6 is a top plan view thereof.

FIG. 7 is a bottom plan view thereof; and,

FIG. 8 is a rear elevation view thereof.

In the drawings, the broken lines depict portions of the enclosure which form no part of the claim. The dash-dot broken lines define the bounds of the claimed design and form no part thereof.

1 Claim, 6 Drawing Sheets



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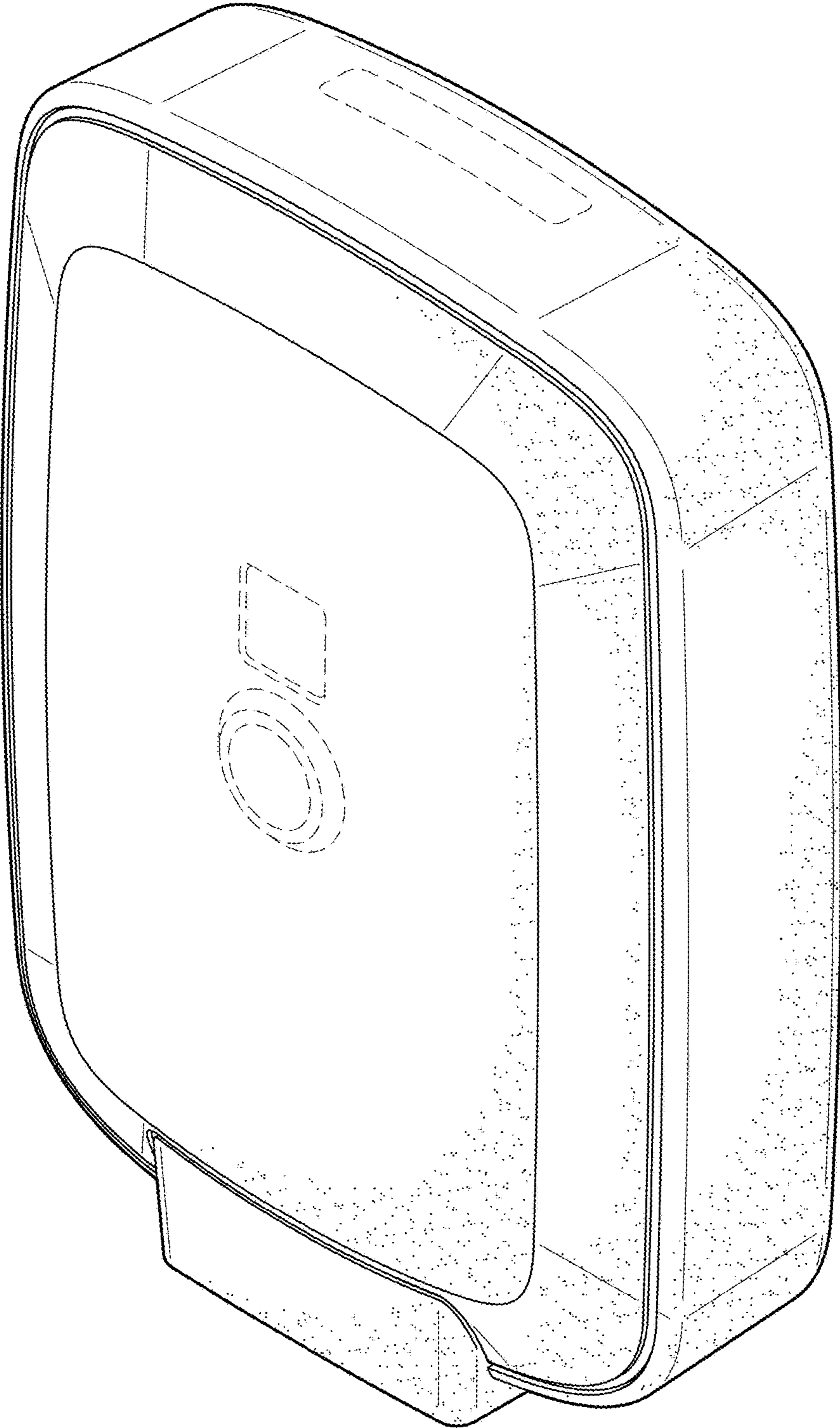


FIG. 1

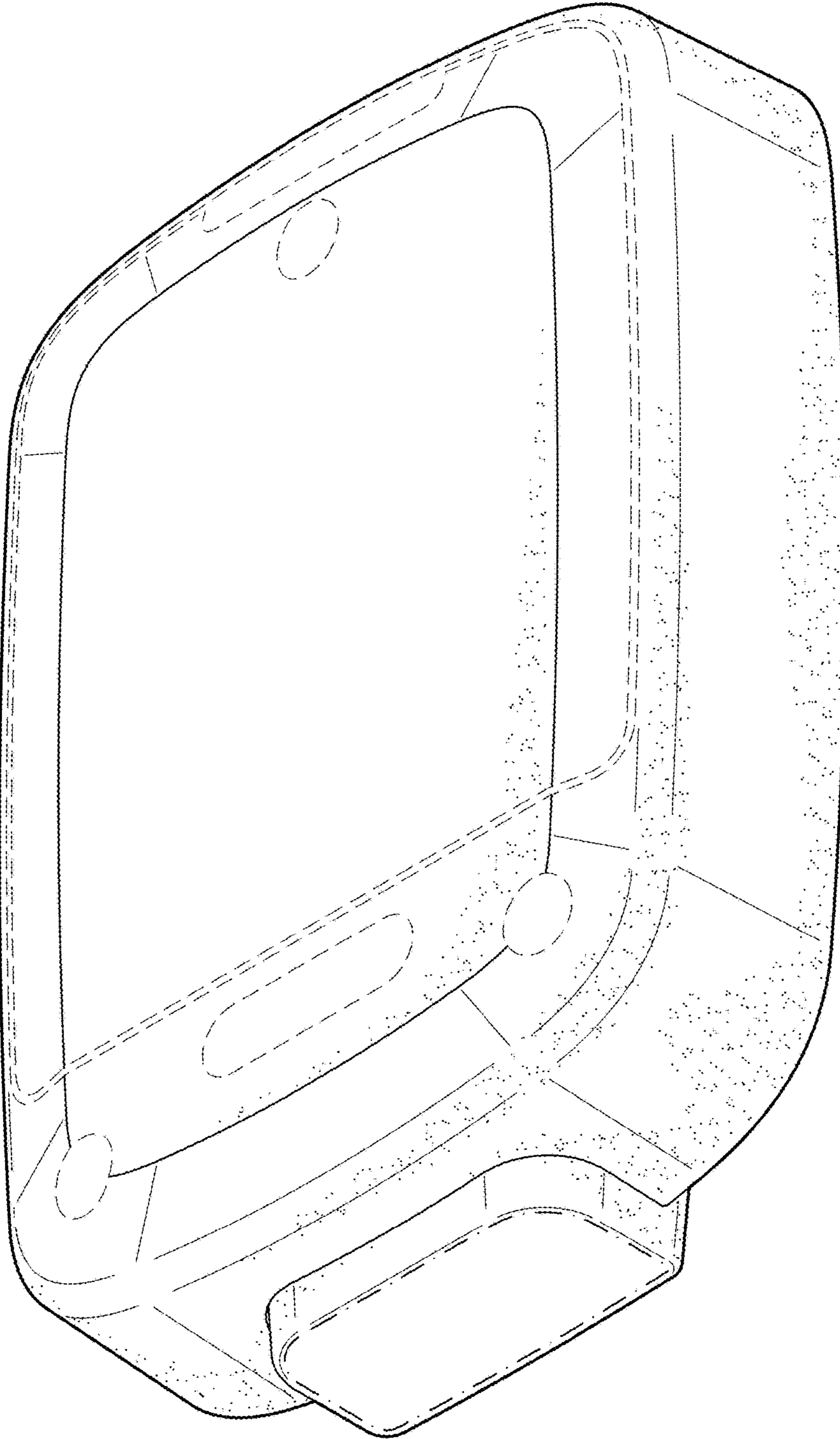


FIG. 2

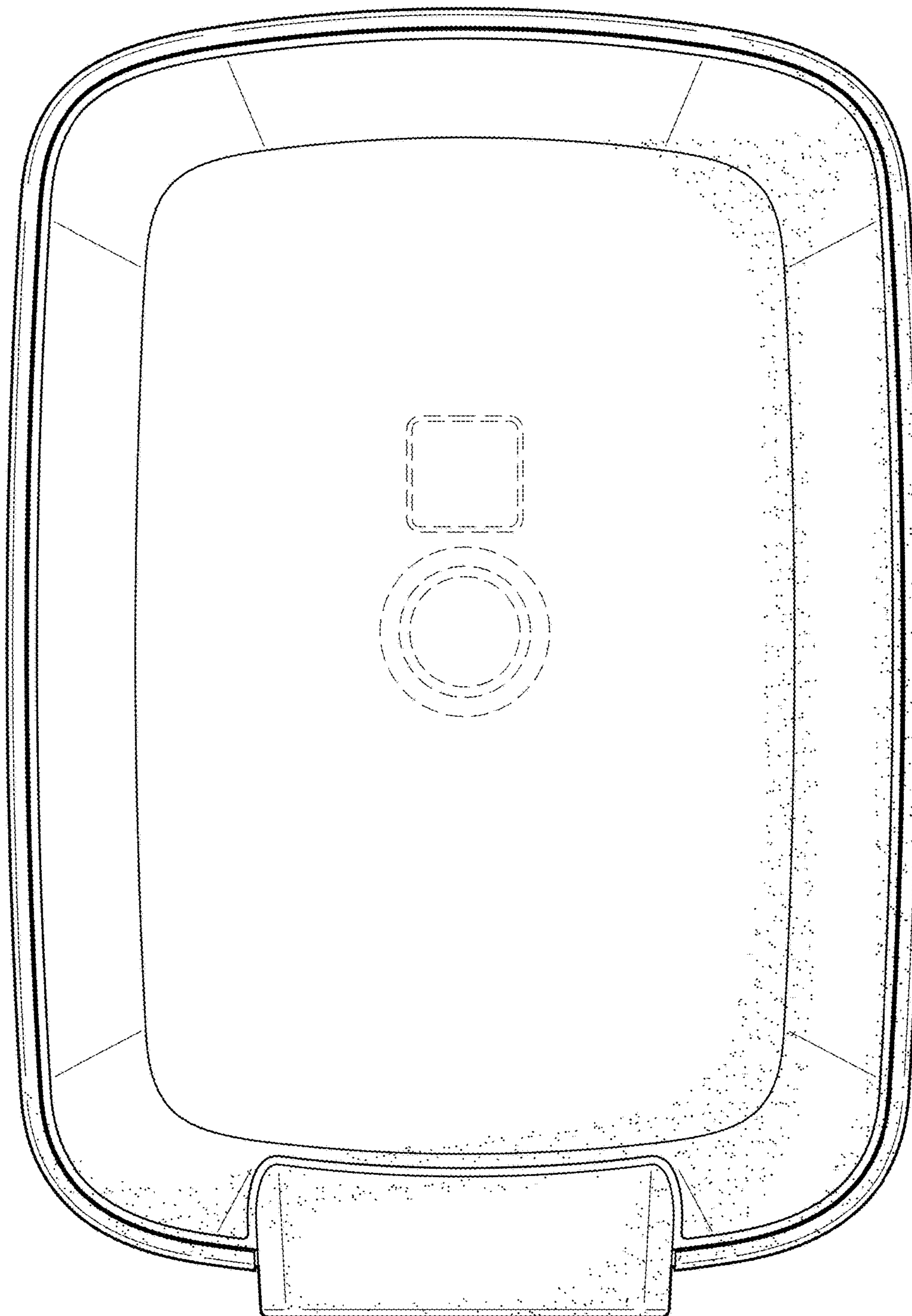


FIG. 3

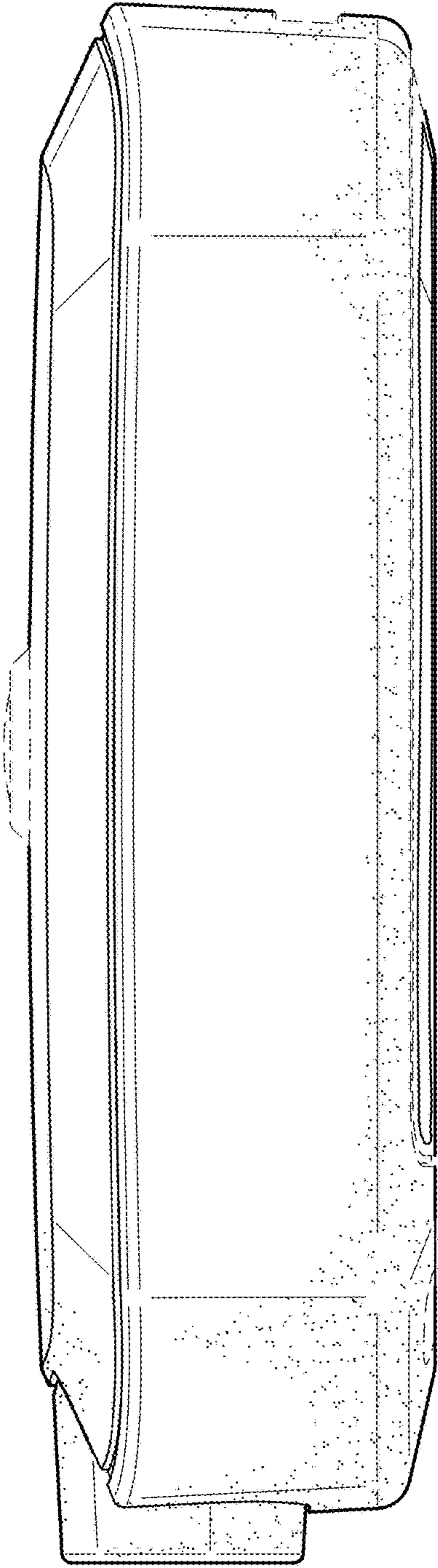


FIG. 4

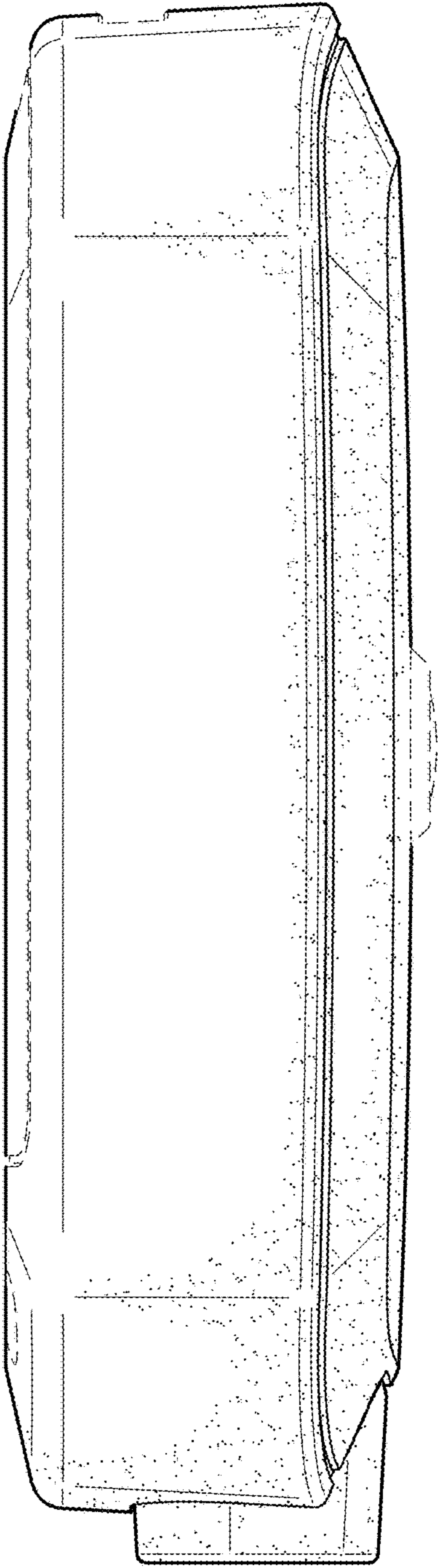


FIG. 5

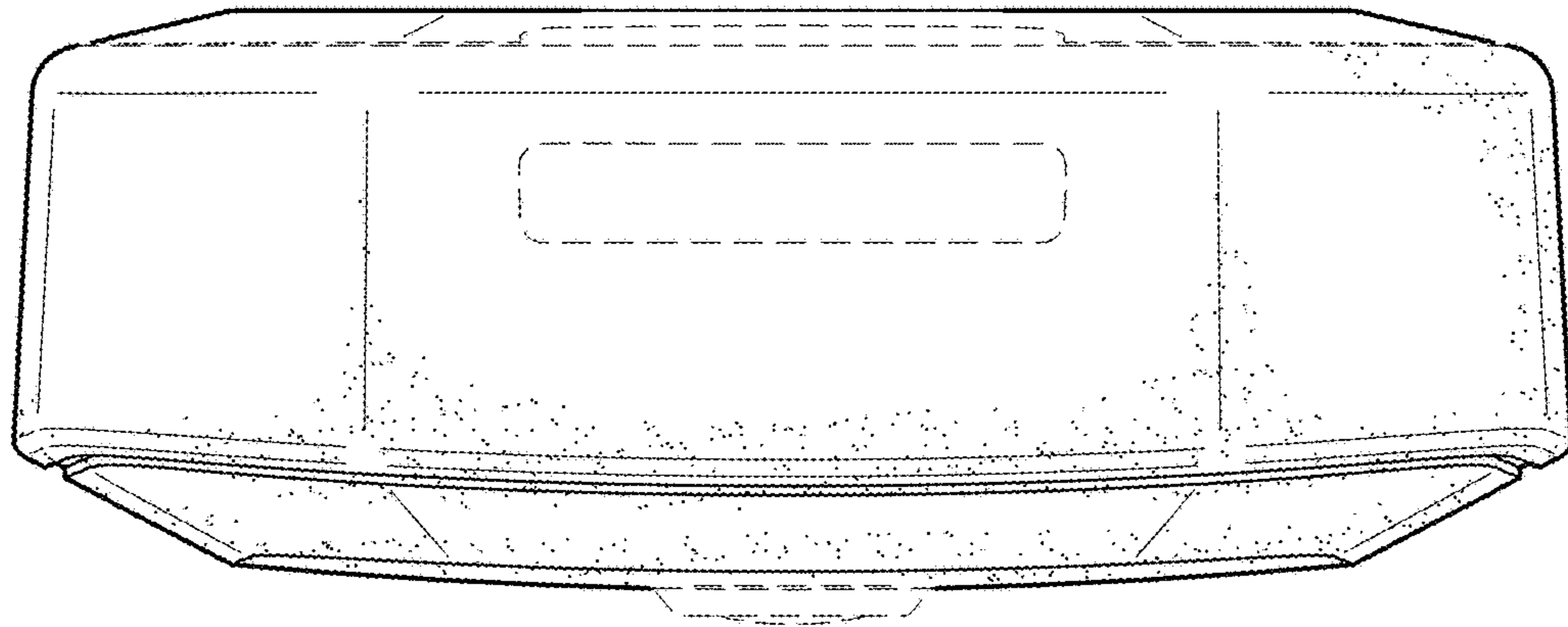


FIG. 6

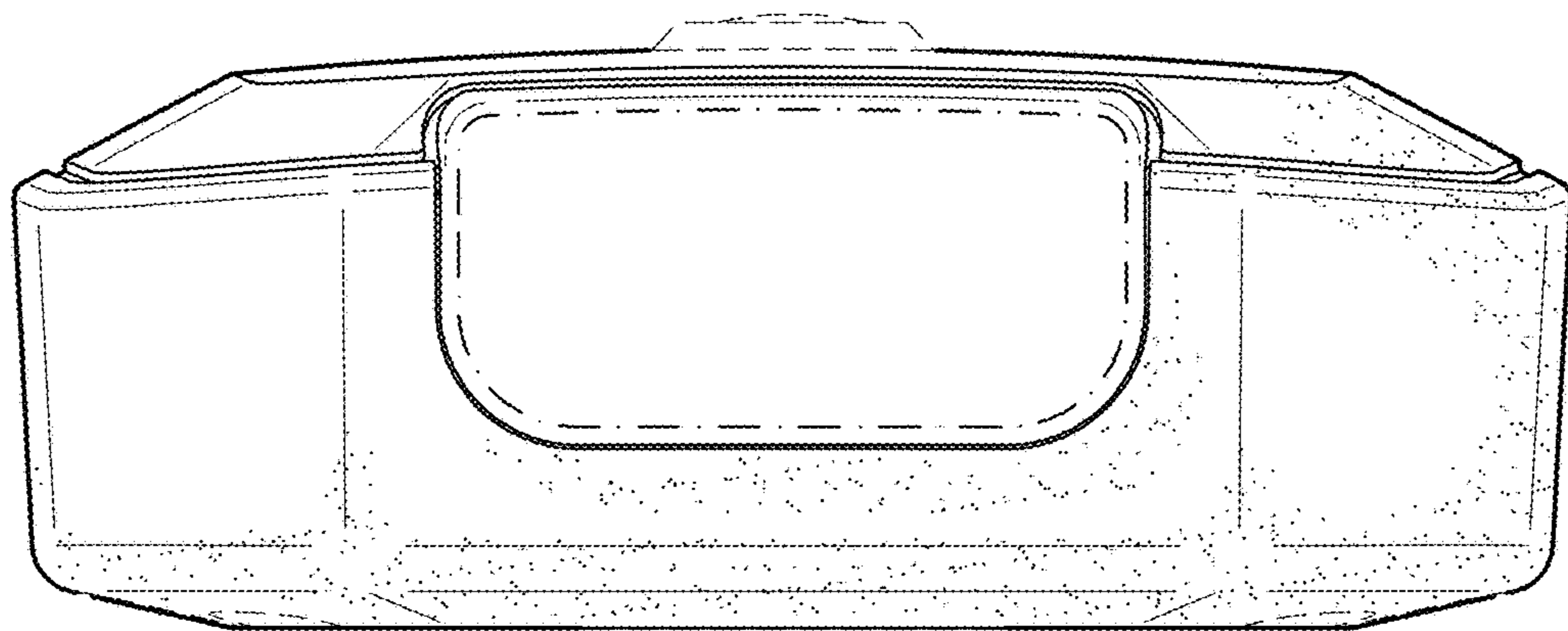


FIG. 7

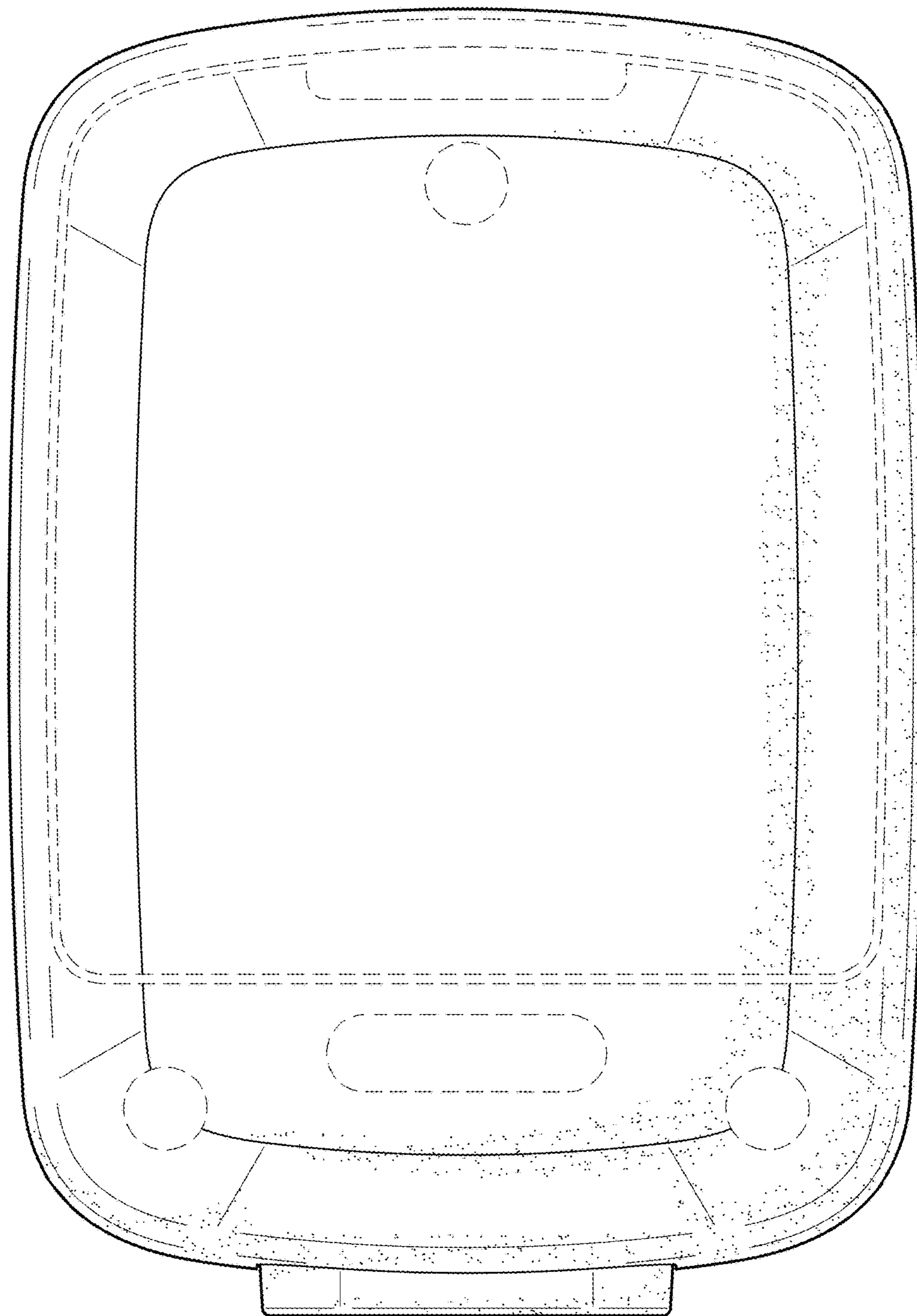


FIG. 8