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(12) **United States Design Patent** (10) **Patent No.:** **US D865,538 S**
Ni et al. (45) **Date of Patent:** **** Nov. 5, 2019**

(54) **DIGITAL-IN-WALL TIMER**
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(Continued)

(**) Term: **15 Years**

(21) Appl. No.: **29/598,357**

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(51) **LOC (12) Cl.** **10-03**

(52) **U.S. Cl.**
USPC **D10/40; D13/162**

(58) **Field of Classification Search**
USPC D10/1, 2-3, 5-7, 14-15, 22-28, 40-41,
D10/46.1, 46.2, 49-50, 52, 55, 59,
D10/122-124, 126-128, 132;
D14/170-171, 197, 218, 489-490, 492;
D15/28; D20/20-21, 29; D7/589;
D13/162, 162.1, 171
CPC G04B 19/00; G04B 19/04; G04B 19/042;
G04B 19/044; G04B 19/06; G04B
19/065; G04B 19/08; G04B 19/085;
G04B 19/26; G04B 19/264; G04B
45/0069; G04B 47/02; G04B 47/04;
G04B 47/048; G04C 3/001; G04C 19/00;
G04C 19/02; G04C 19/06; G04F 1/00;
G04F 3/00; G04F 3/02; A47J 31/407

See application file for complete search history.

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(Continued)

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(57) **CLAIM**

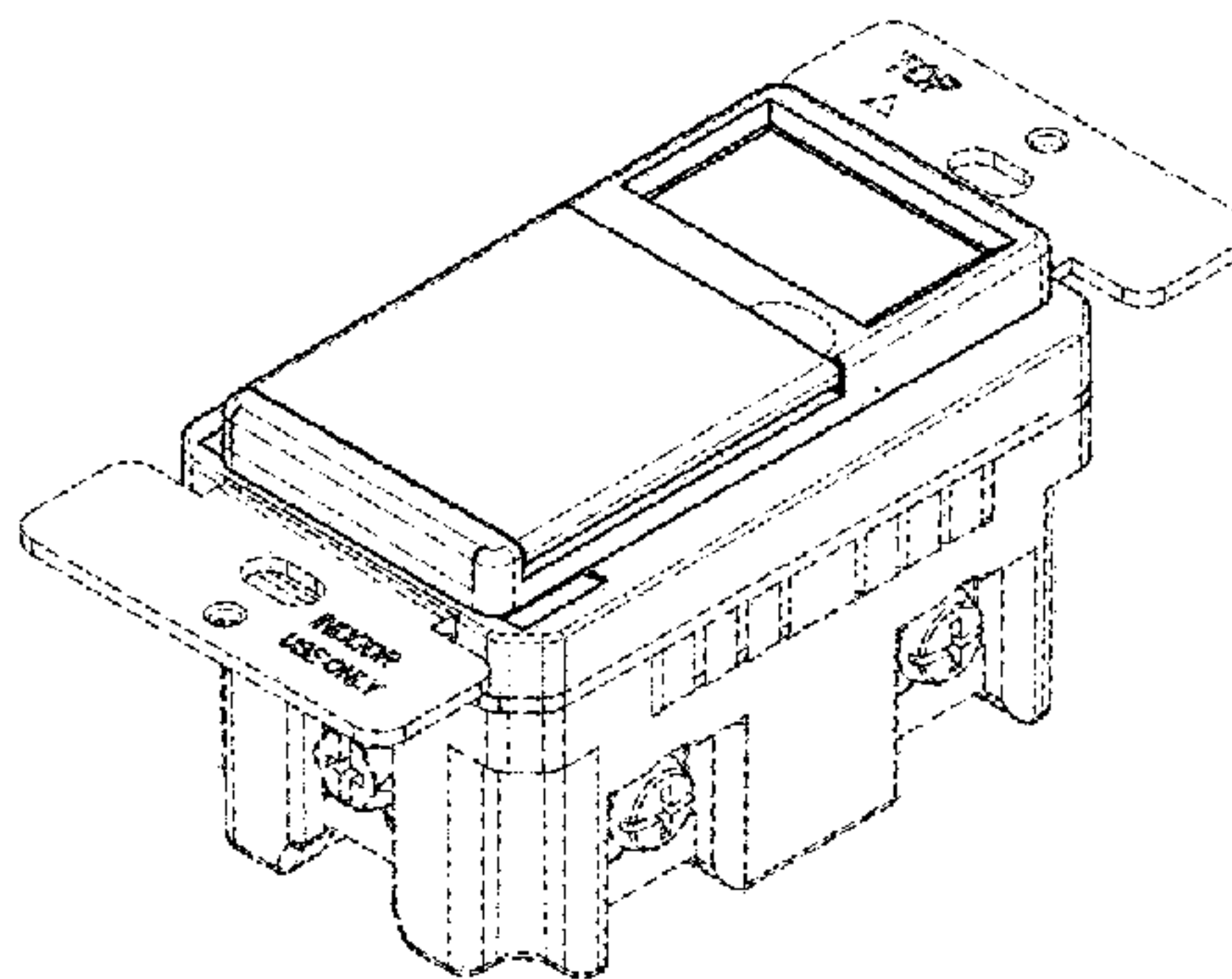
The ornamental design for a digital-in-wall timer, as shown
and described.

DESCRIPTION

FIG. 1 is a perspective view of a digital-in-wall timer of the
subject disclosure;
FIG. 2 is a closed front view thereof;
FIG. 3 is an open front view thereof;
FIG. 4 is a left side view thereof;
FIG. 5 is a right side view thereof;
FIG. 6 is a top view thereof;
FIG. 7 is a bottom view thereof; and,
FIG. 8 is a back view thereof.

The broken lines show portions of the digital in-wall timer
that form no part of the claimed design.

1 Claim, 8 Drawing Sheets



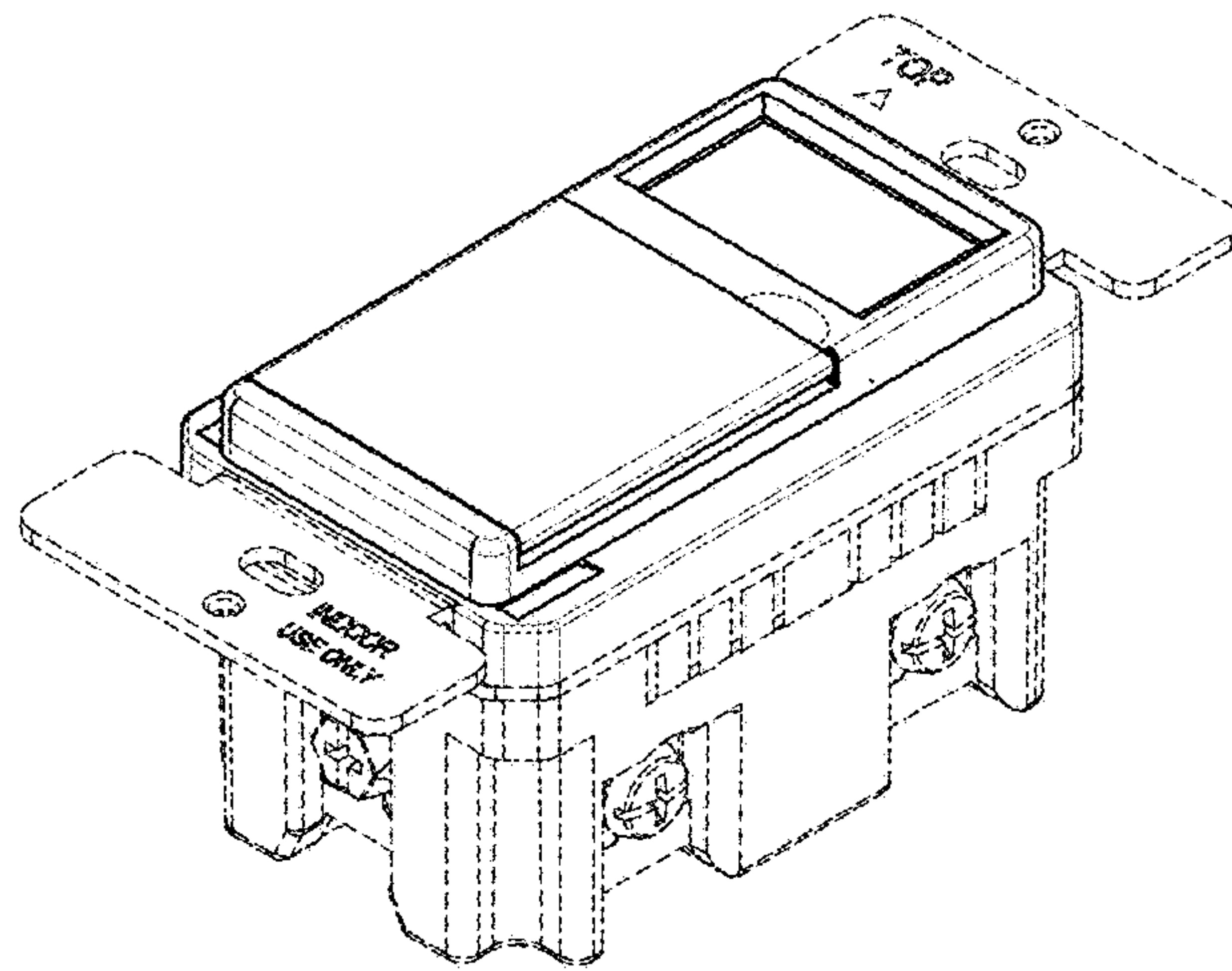


FIG 1

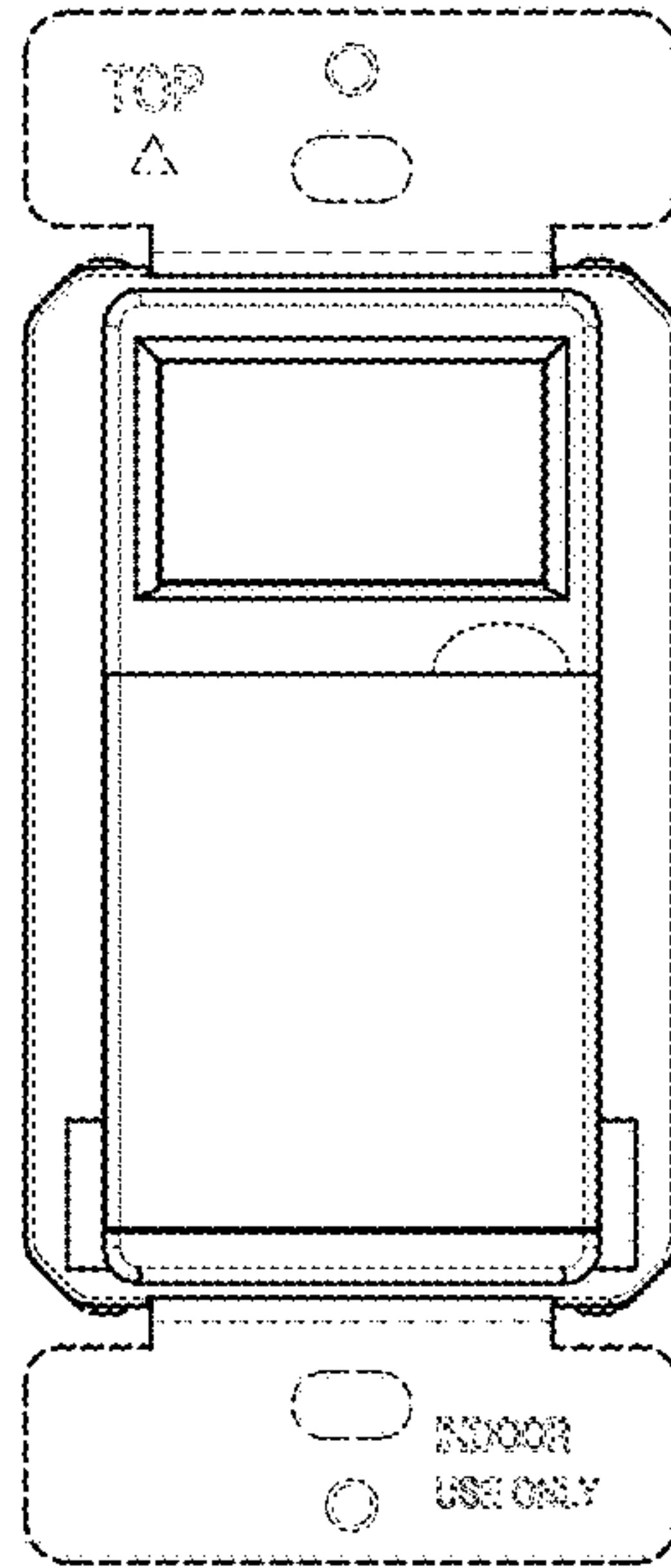


FIG 2

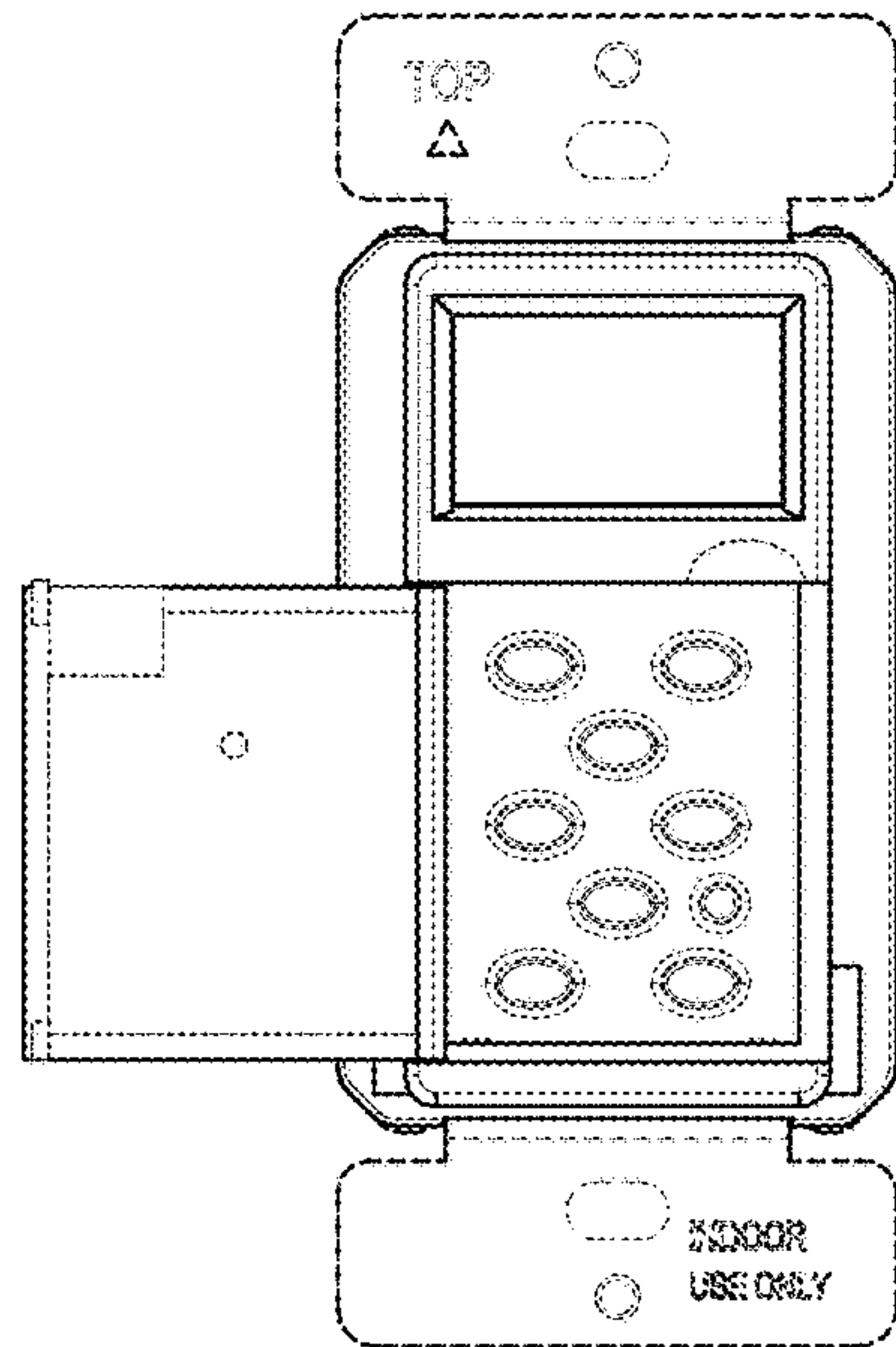


FIG 3

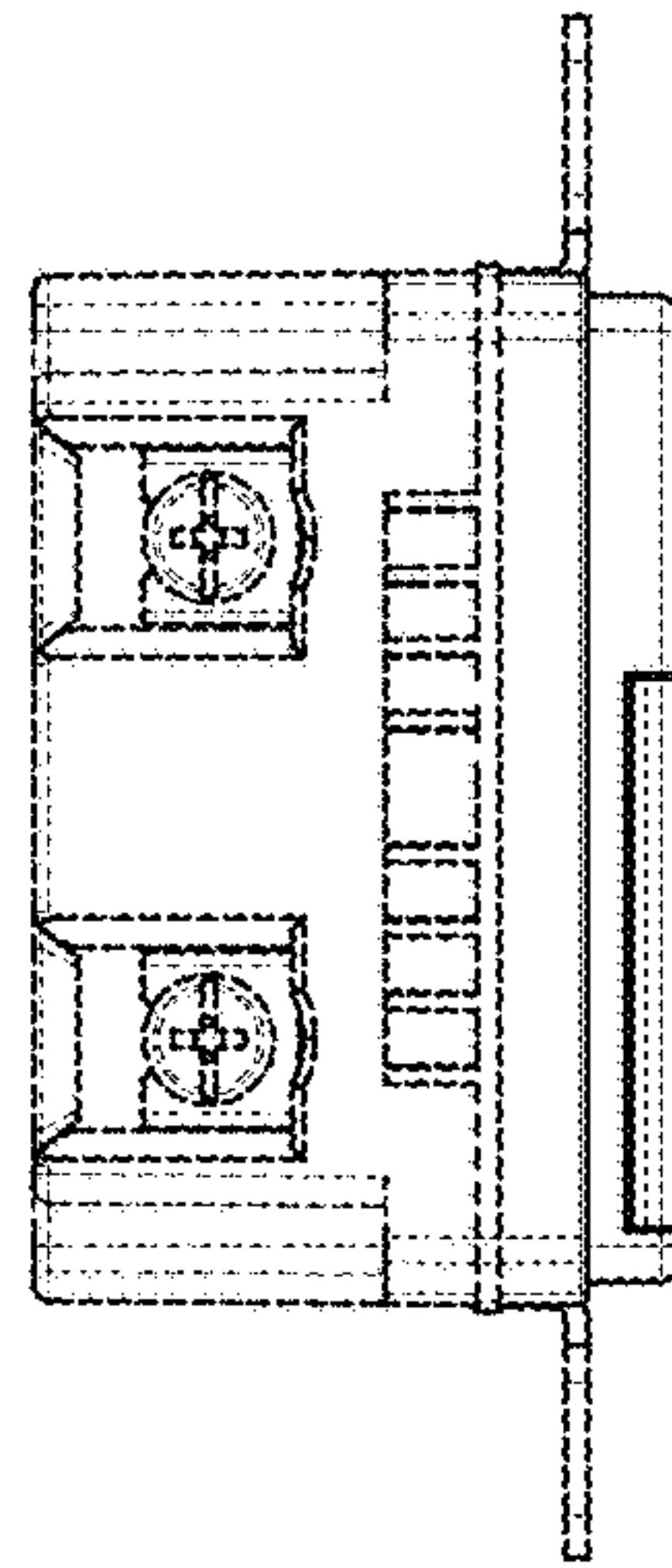


FIG 4

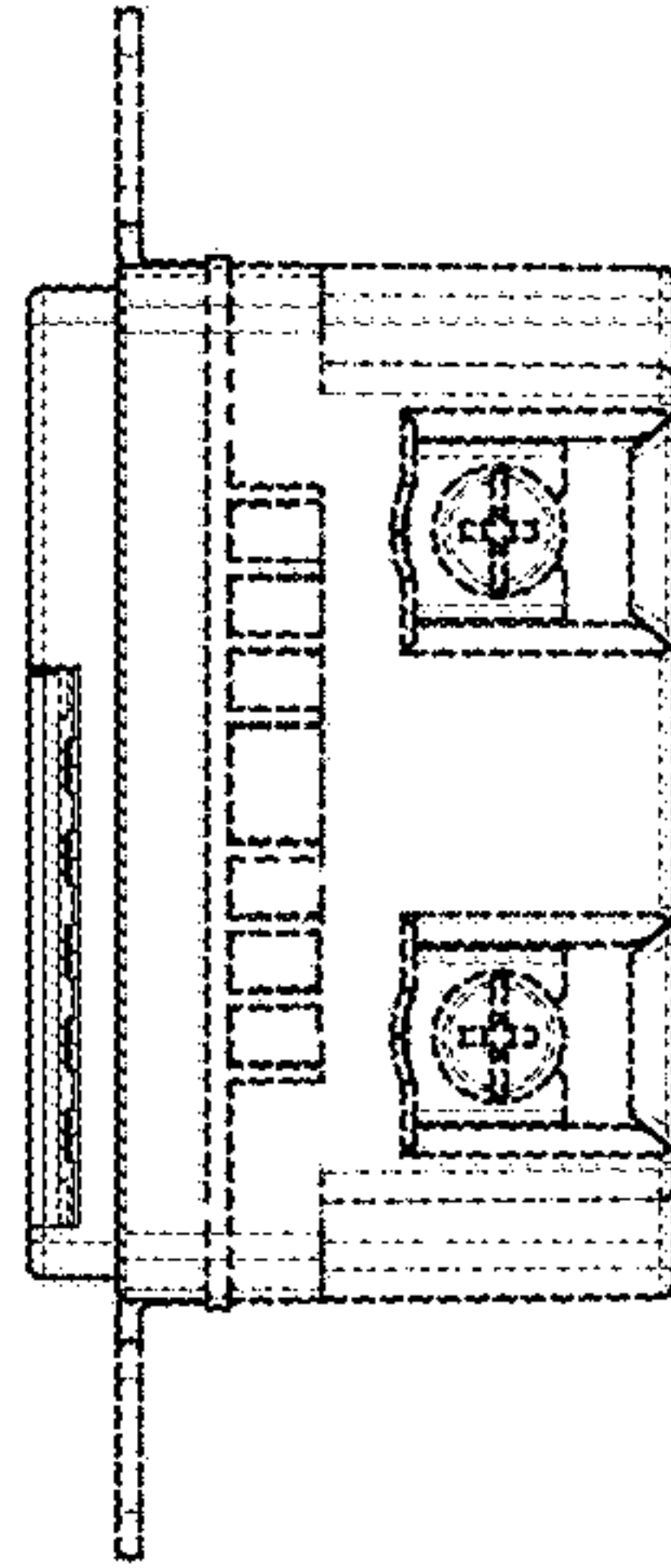


FIG 5

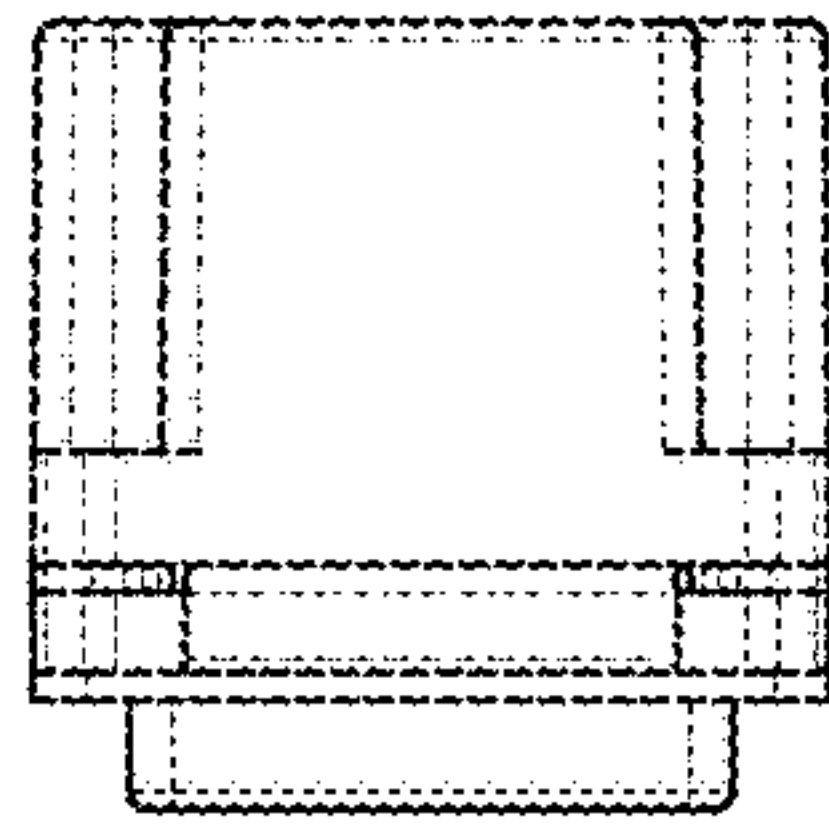


FIG 6

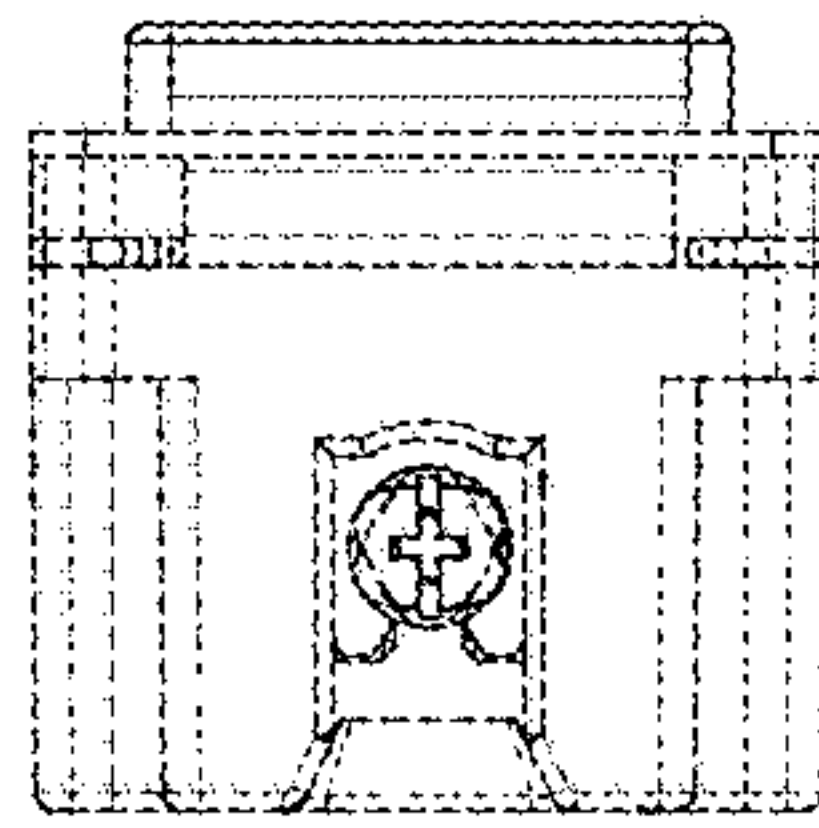


FIG 7

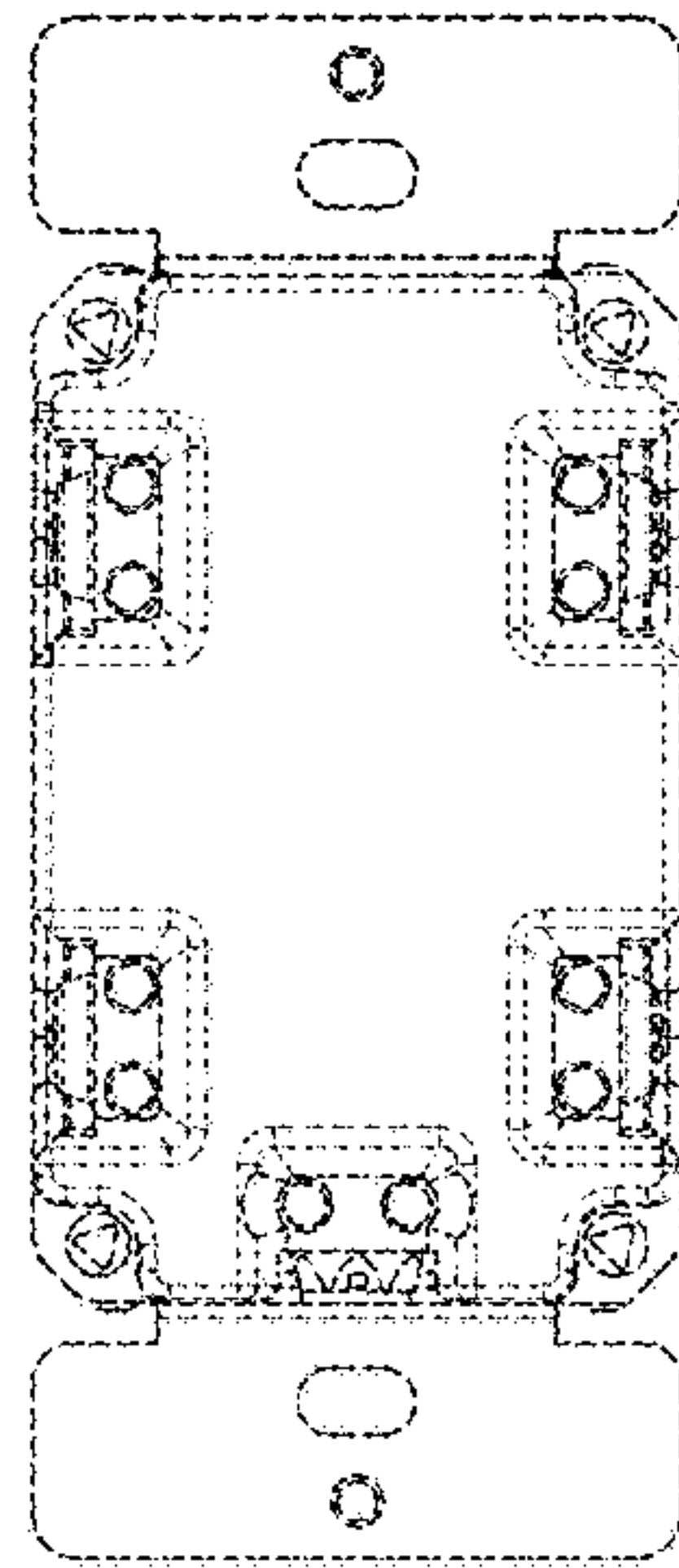


FIG 8