

US00D864995S

(12) **United States Design Patent**
Laurino

(10) **Patent No.:** **US D864,995 S**
(45) **Date of Patent:** **** Oct. 29, 2019**

(54) **ELECTRICAL TEST AND MEASUREMENT APPARATUS**

D775,199 S * 12/2016 Vulk D14/489
D778,948 S * 2/2017 Maccubbin D14/489
D802,015 S * 11/2017 Dragoi D14/489

(71) Applicant: **Fluke Corporation**, Everett, WA (US)

* cited by examiner

(72) Inventor: **Ferdinand Y. Laurino**, Seattle, WA (US)

Primary Examiner — Antoine Duval Davis
(74) *Attorney, Agent, or Firm* — Seed Intellectual Property Law Group LLP

(73) Assignee: **Fluke Corporation**, Everett, WA (US)

(57) **CLAIM**

(**) Term: **15 Years**

The ornamental design for an electrical test and measurement apparatus, as shown and described.

(21) Appl. No.: **29/616,850**

DESCRIPTION

(22) Filed: **Sep. 8, 2017**

(51) **LOC (12) Cl.** **14-04**

(52) **U.S. Cl.**
USPC **D14/489**

(58) **Field of Classification Search**
USPC D10/80, 485, 489
CPC G06F 3/048; G06F 3/0481; G06F 3/04812;
G06F 3/04815; G06F 3/04817; G06F 3/0482;
G06F 3/0483; G06F 3/0484; G06F 3/04842;
G06F 3/04845; G06F 3/04847; G06F 3/0485; G06F 3/04855;
G06F 3/0486; G06F 3/0487; G06F 3/0488;
G06F 3/04883; G06F 3/04886; H04N 1/00424

FIG. 1 is a front elevation view of an electrical test and measurement apparatus showing my new design.

FIG. 2 is another front elevation view of an electrical test and measurement apparatus with my new design as shown in FIG. 1.

FIG. 3 is a detailed front elevation view of an electrical test and measurement apparatus or a portion thereof with my new design as shown in FIGS. 1 and 2.

FIG. 4 is another front elevation view of an electrical test and measurement apparatus showing my new design.

FIG. 5 is another front elevation view of an electrical test and measurement apparatus with my new design as shown in FIG. 4.

FIG. 6 is a detailed front elevation view of an electrical test and measurement apparatus or a portion thereof with my new design as shown in FIGS. 4 and 5.

The broken line perimeter in FIGS. 3 and 6 represents an electrical test and measurement apparatus or a portion thereof and forms no part of the claimed design. The broken lines in FIGS. 1, 2, 4, and 5 illustrate environment only and form no part of the claimed design.

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D679,726 S * 4/2013 Kobayashi D14/489
D691,630 S * 10/2013 Wu D14/489
D759,037 S * 6/2016 Kadosh D14/485

1 Claim, 6 Drawing Sheets



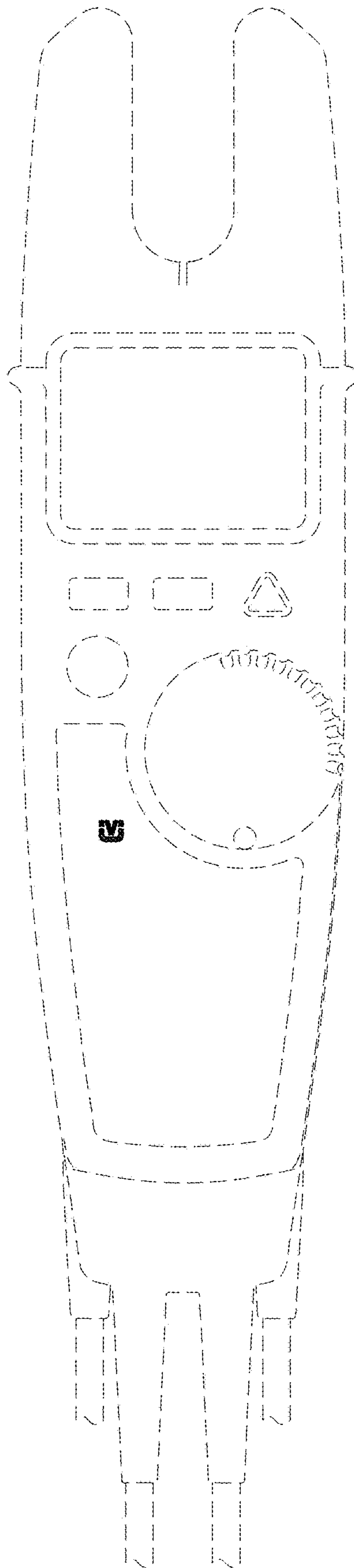


FIG. 1

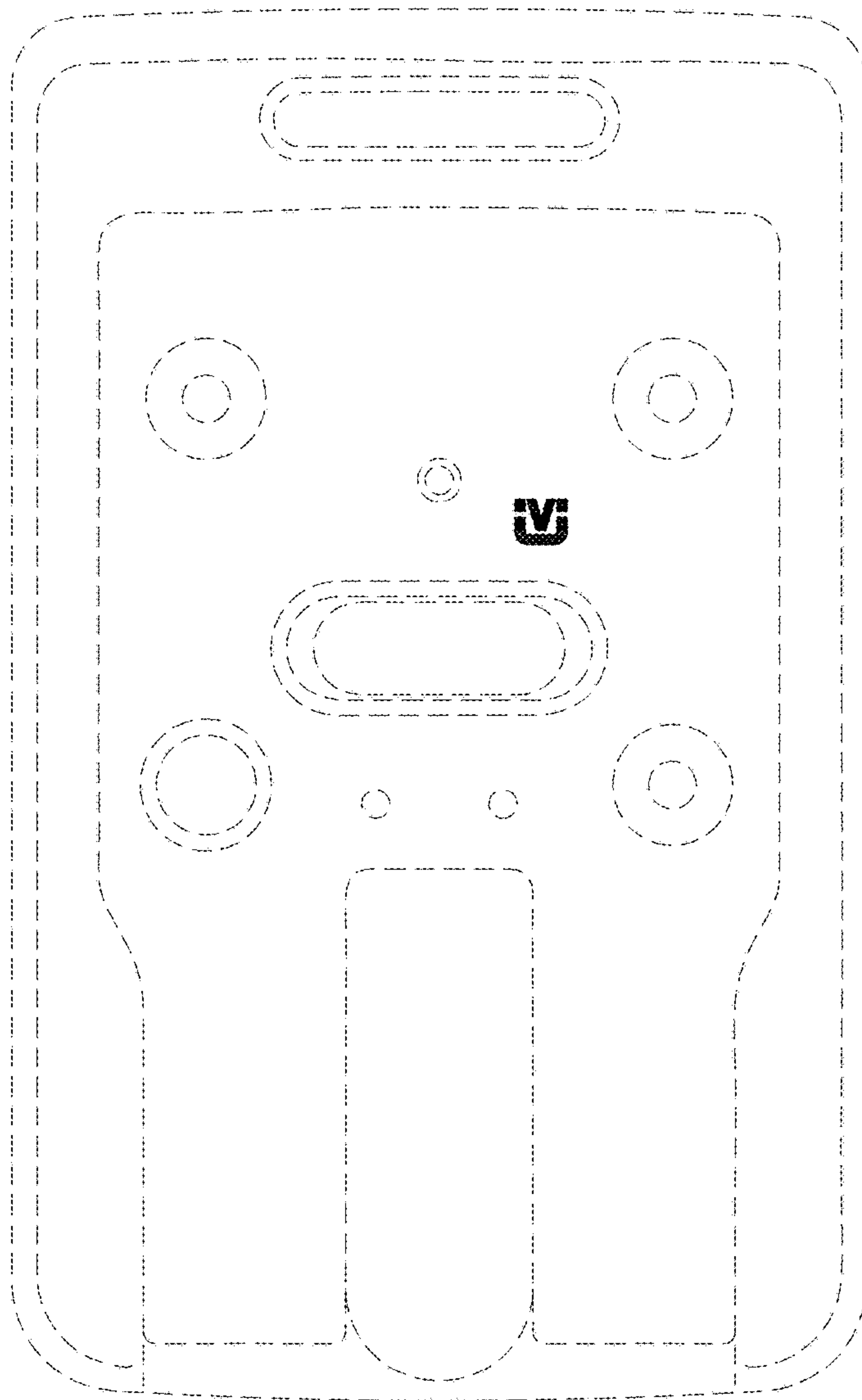


FIG. 2



FIG. 3

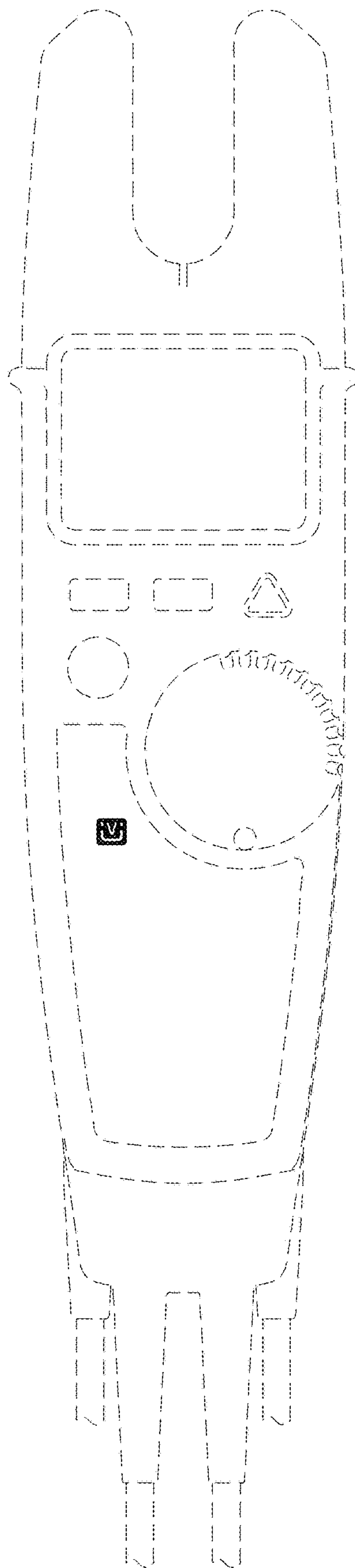


FIG. 4

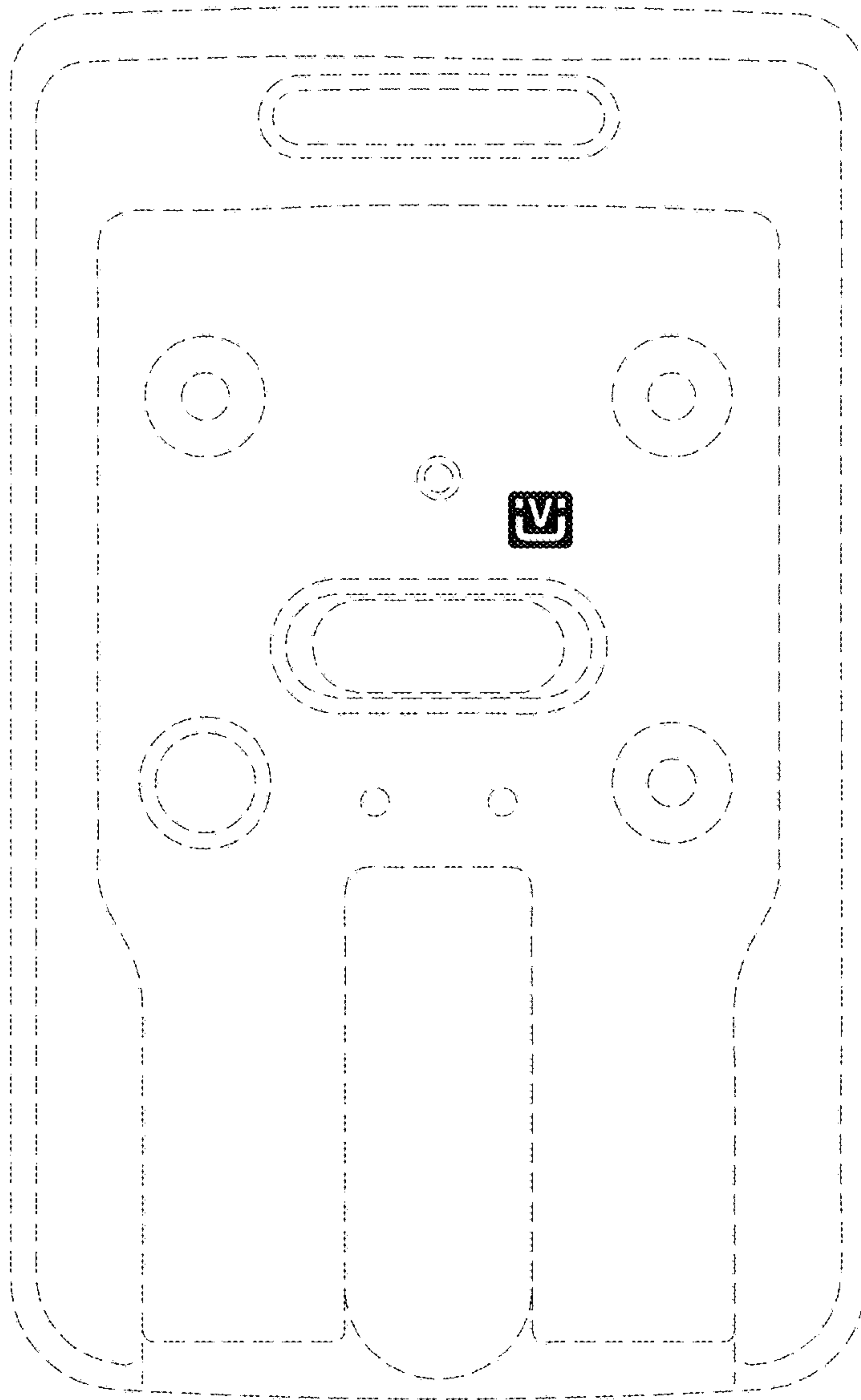


FIG. 5



FIG. 6