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(12) **United States Design Patent** (10) **Patent No.:** **US D864,933 S**
Park et al. (45) **Date of Patent:** **** Oct. 29, 2019**

(54) **ELECTRONIC DEVICE**

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(Continued)

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(73) Assignee: **SAMSUNG ELECTRONICS CO., LTD.**, Gyeonggi-Do (KR)

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(Continued)

(**) Term: **15 Years**

Primary Examiner — Bridget L Eland

(21) Appl. No.: **29/656,196**

(74) *Attorney, Agent, or Firm* — Cantor Colburn LLP

(22) Filed: **Jul. 11, 2018**

(57) **CLAIM**

(30) **Foreign Application Priority Data**

The ornamental design for an electronic device, as shown and described.

Jan. 12, 2018 (KR) 30-2018-0002044

DESCRIPTION

(51) **LOC (12) Cl.** **14-03**

FIG. 1 is a front perspective view of an electronic device showing our new design;

(52) **U.S. Cl.**

FIG. 2 is a front view thereof;

USPC **D14/248**

FIG. 3 is a rear view thereof;

(58) **Field of Classification Search**

USPC D14/138 G, 248, 341, 371, 374, 138 R,
D14/138 AA, 138 AB, 138 AC, 138 AD,
D14/138 C, 247

FIG. 4 is a left-side view thereof;

CPC H04M 1/0202; H04M 1/0235; H04M
1/0237; H04M 1/0239; H04M 1/0266;
H04M 1/0268; H04M 1/027; H04M
1/0295

FIG. 5 is a right-side view thereof;

See application file for complete search history.

FIG. 6 is a top view thereof;

FIG. 7 is a bottom view thereof; and,

FIG. 8 is a rear perspective view thereof.

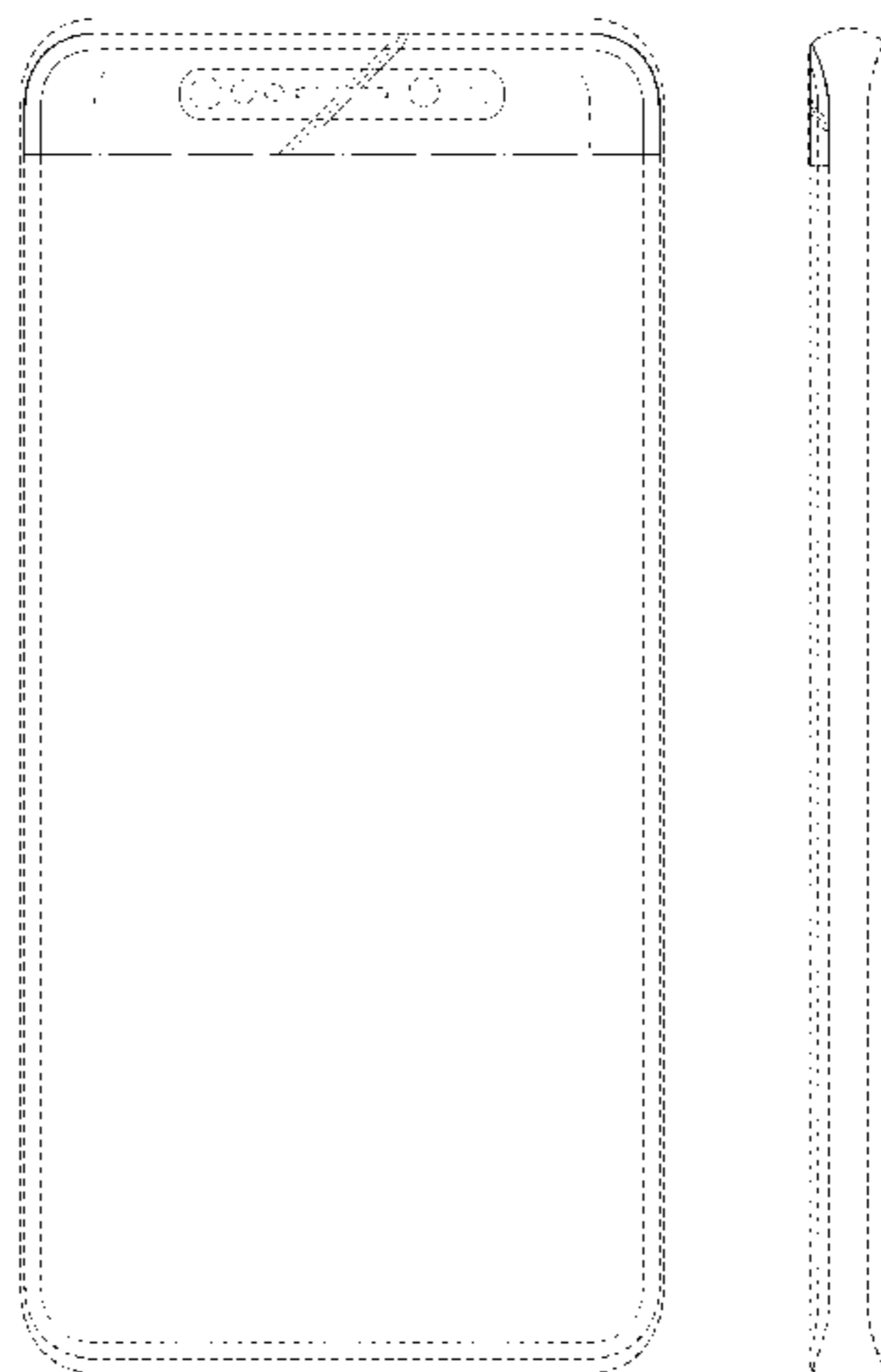
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The dot-dash and evenly-spaced broken lines within the solid line showing represent the bounds of the claimed design while all other evenly-spaced broken lines are directed to environment and are for illustrative purposes only; the broken lines form no part of the claimed design. In addition, the unshaded surface in the views that directly adjoin the claimed solid line edges forms no part of the claimed design.

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1 Claim, 6 Drawing Sheets



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FIG. 1

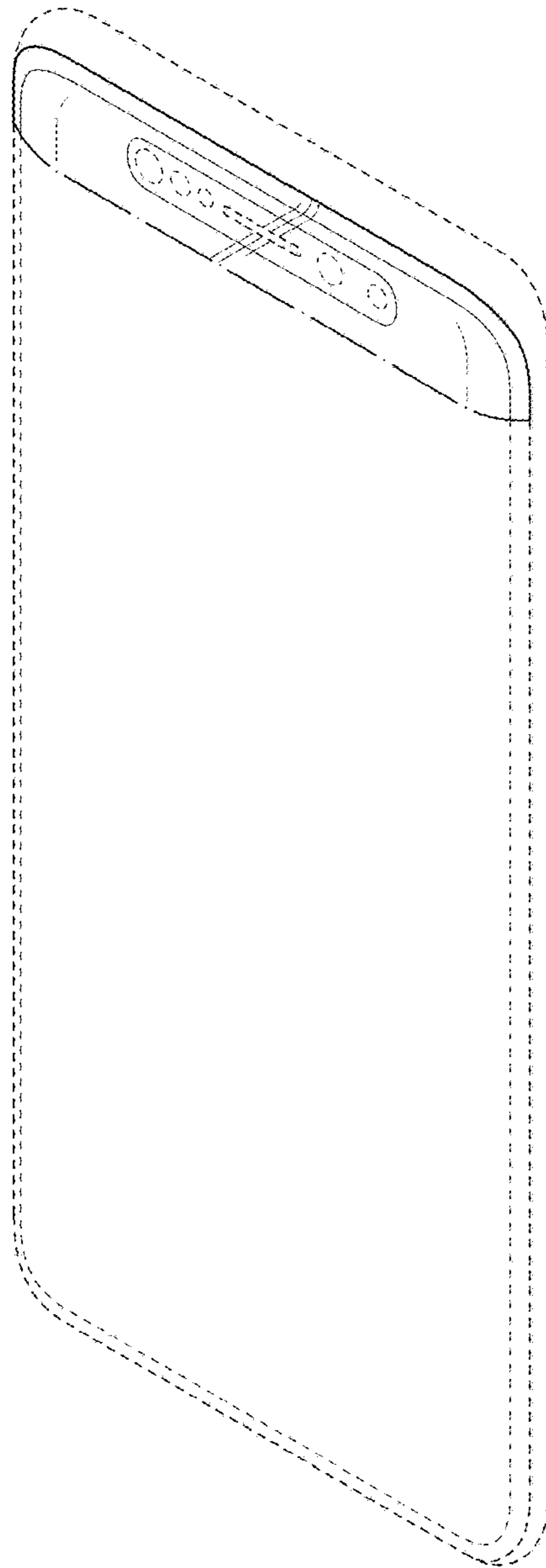


FIG.2

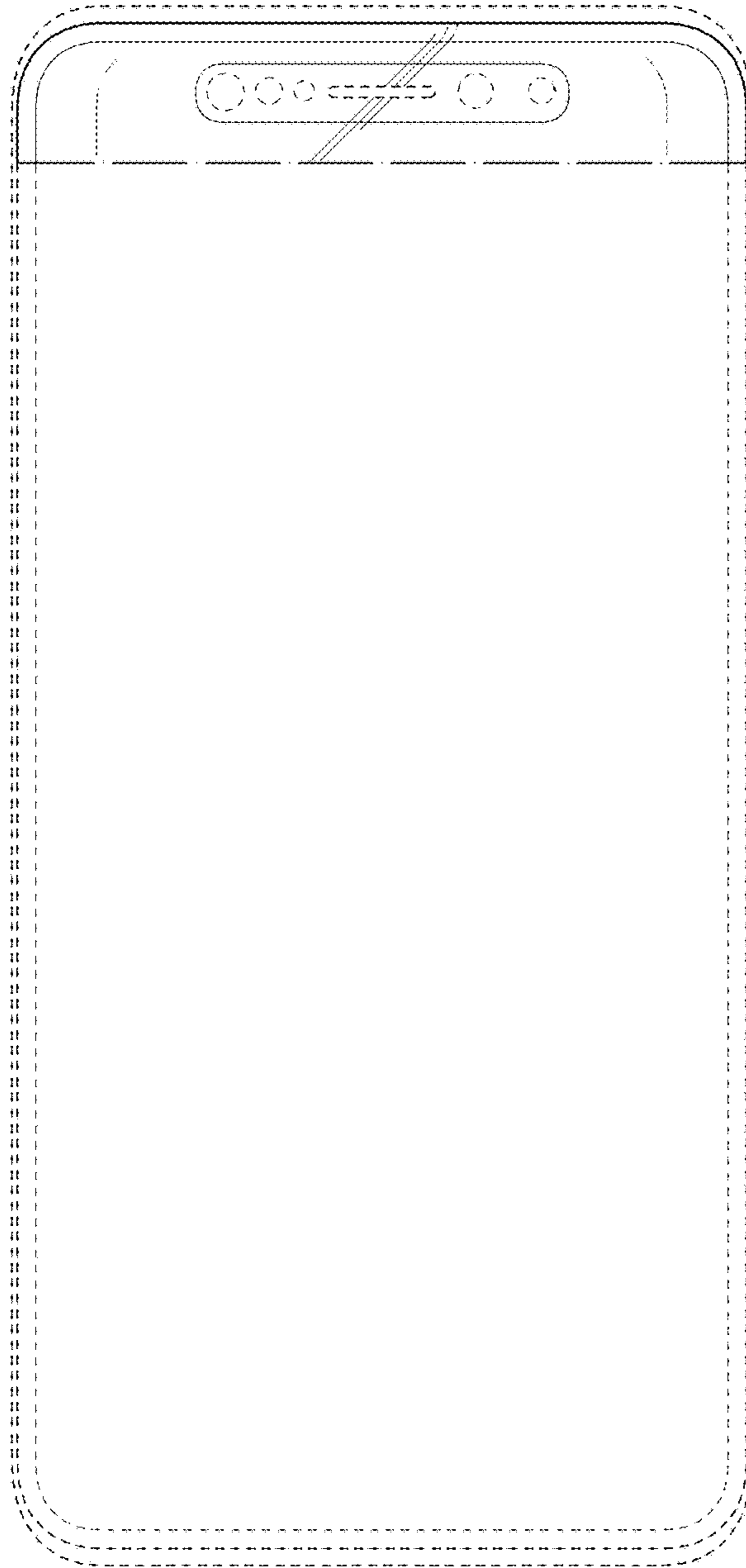


FIG.3

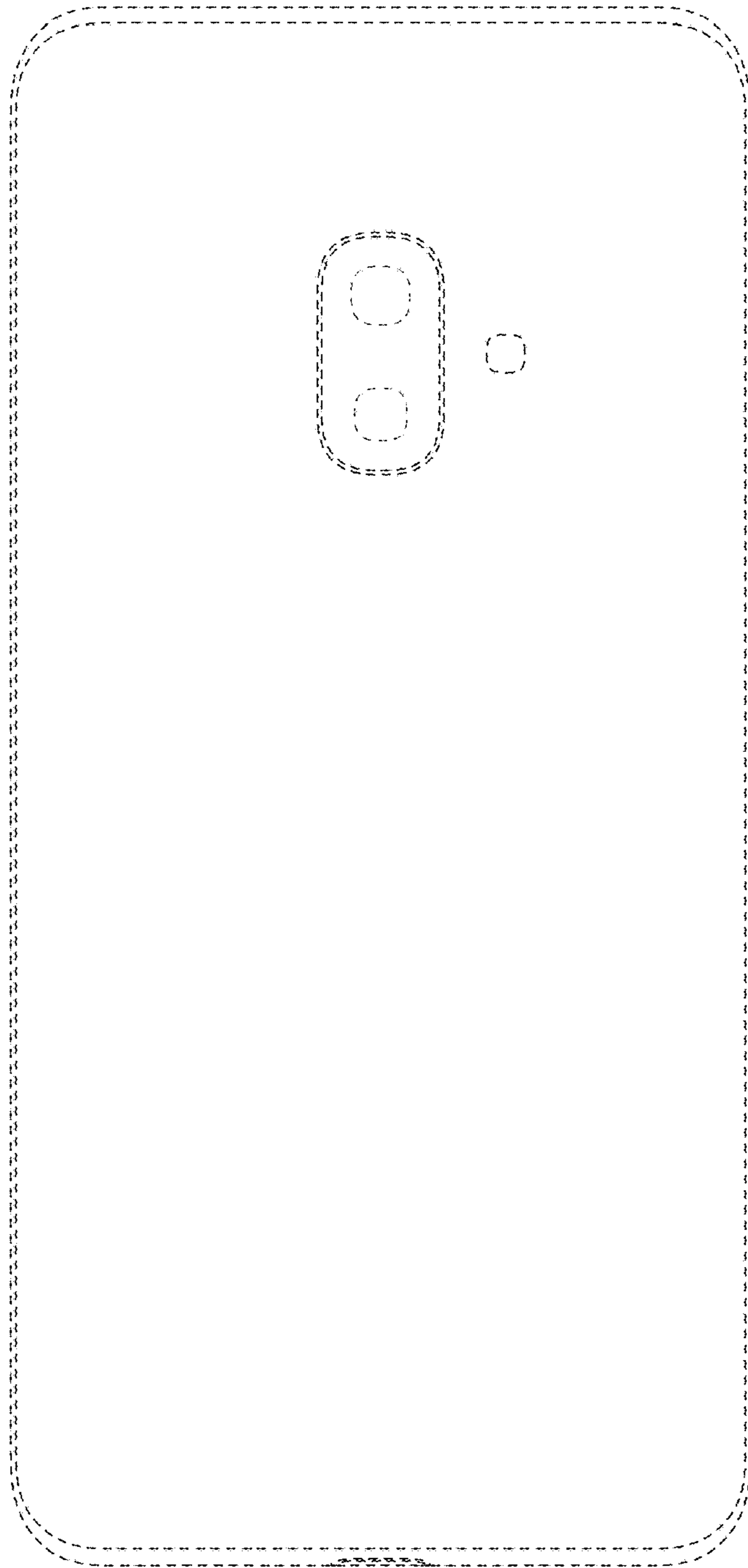


FIG.4

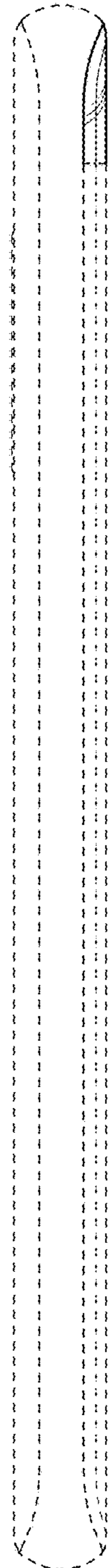


FIG.5

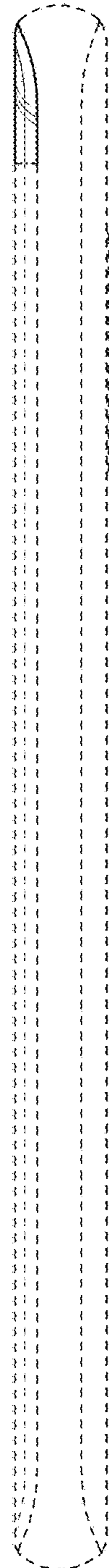


FIG.6

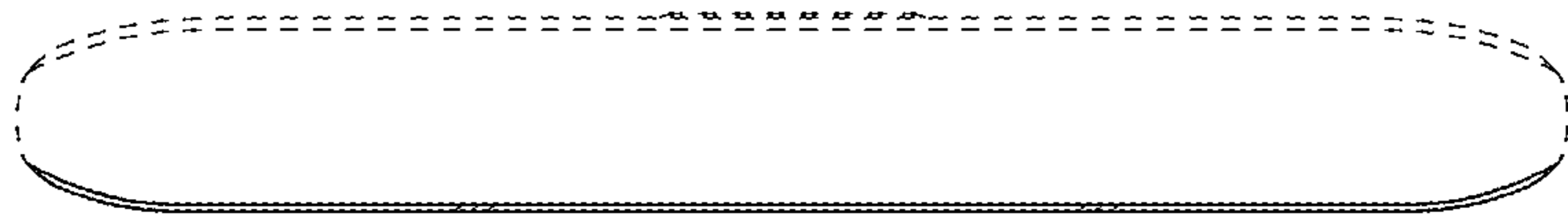


FIG.7

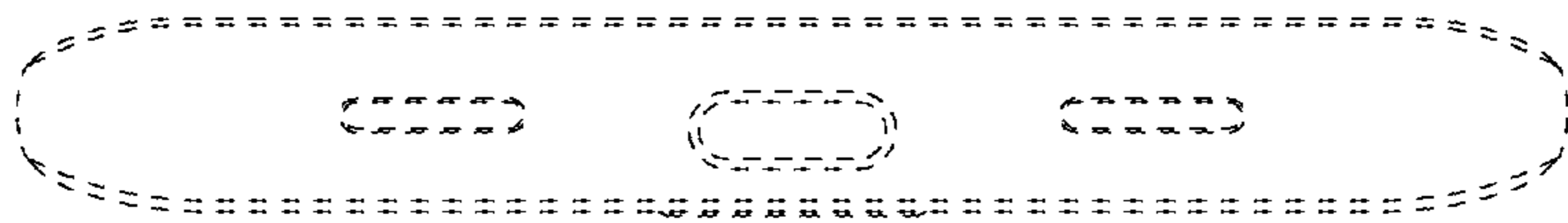


FIG.8

