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(12) **United States Design Patent** (10) **Patent No.:** **US D864,133 S**
Kellenberger et al. (45) **Date of Patent:** **** Oct. 22, 2019**

(54) **CIRCUIT BOARD**
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NJ (US)
(**) Term: **15 Years**
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(52) **U.S. Cl.**
USPC **D13/182**
(58) **Field of Classification Search**
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361/808, 809; 174/250, 253, 255;
363/144, 147
CPC H05K 3/30; H05K 3/301; H05K 3/284;
H05K 1/18; H05K 1/181; H05K 1/182;
H05K 1/183; H05K 1/185; H05K 1/00;
H05K 7/1417; H05K 7/02; H05K 7/12;
H05K 7/06; H01L 2924/14
See application file for complete search history.

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(Continued)

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(57) **CLAIM**

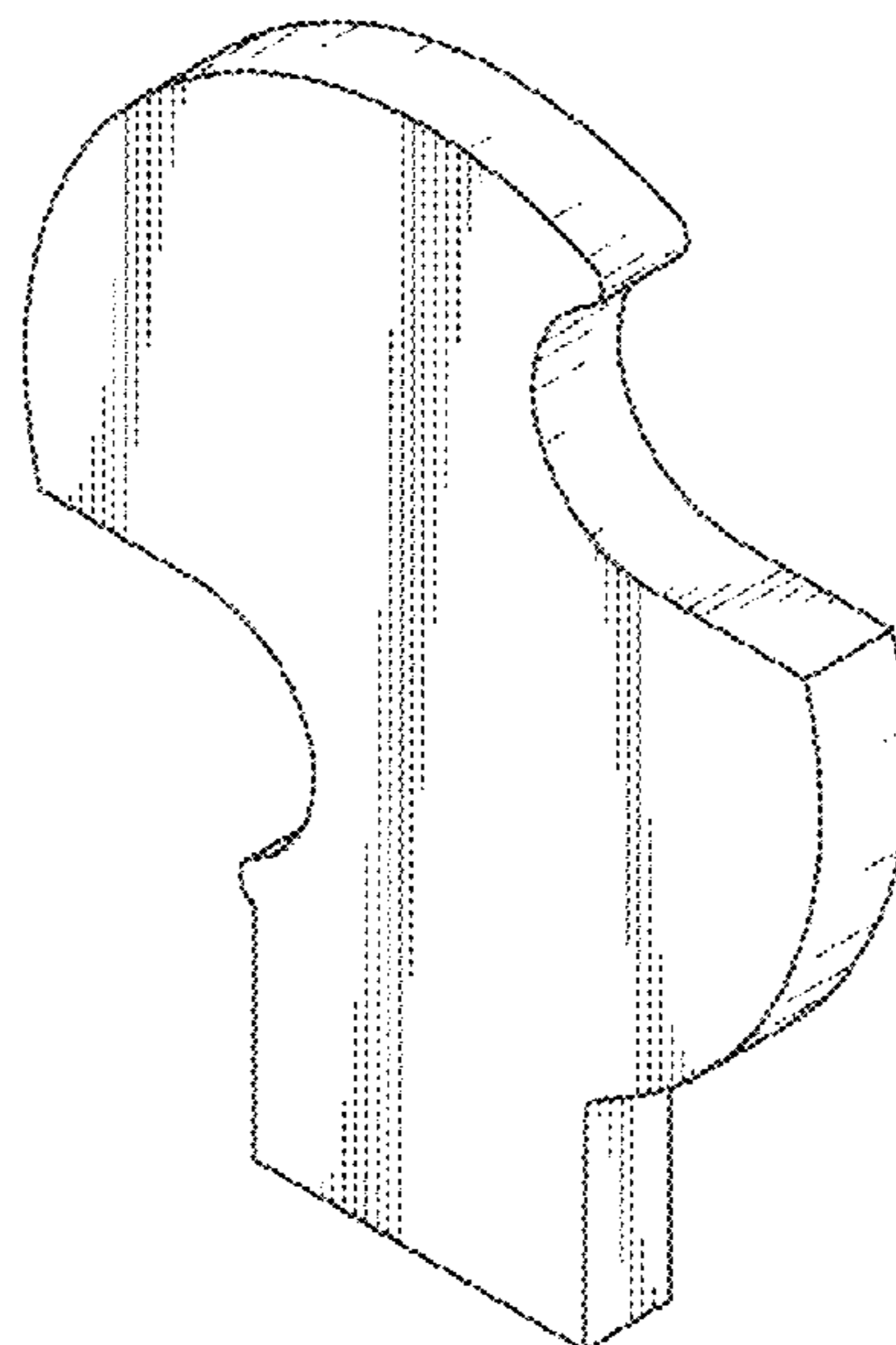
The ornamental design for a circuit board, as shown and described.

DESCRIPTION

FIG. 1 is a left side perspective view of a circuit board showing our new design;
FIG. 2 is a right side perspective view thereof;
FIG. 3 is a left side view thereof;
FIG. 4 is a right side view thereof;
FIG. 5 is a front view thereof;
FIG. 6 is a rear view thereof;
FIG. 7 is a top plan view thereof; and,
FIG. 8 is a bottom plan view thereof.
The broken lines shown in the drawings represent portions of the circuit board that form no part of the claimed design.

1 Claim, 3 Drawing Sheets

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FIG. 2

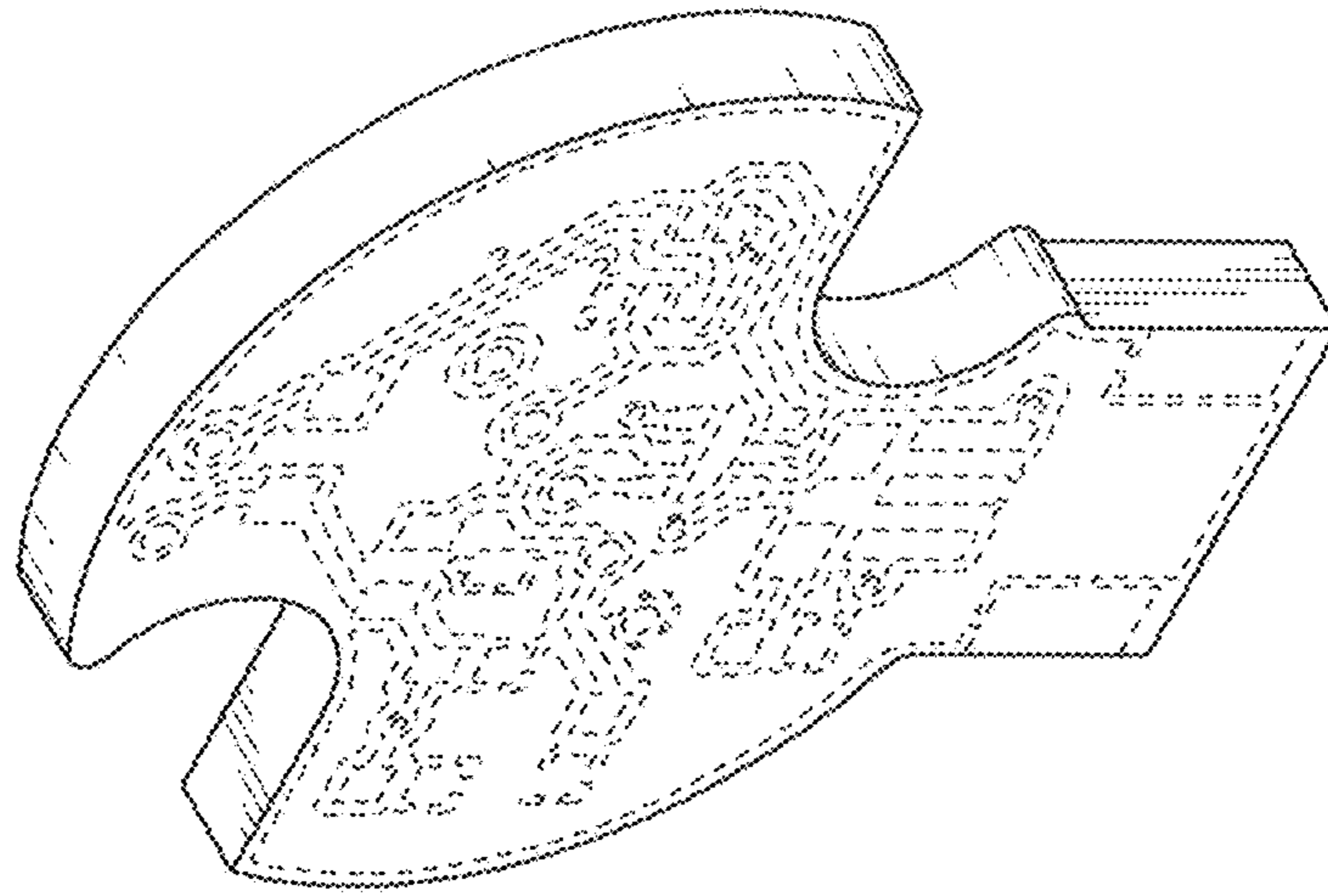


FIG. 1

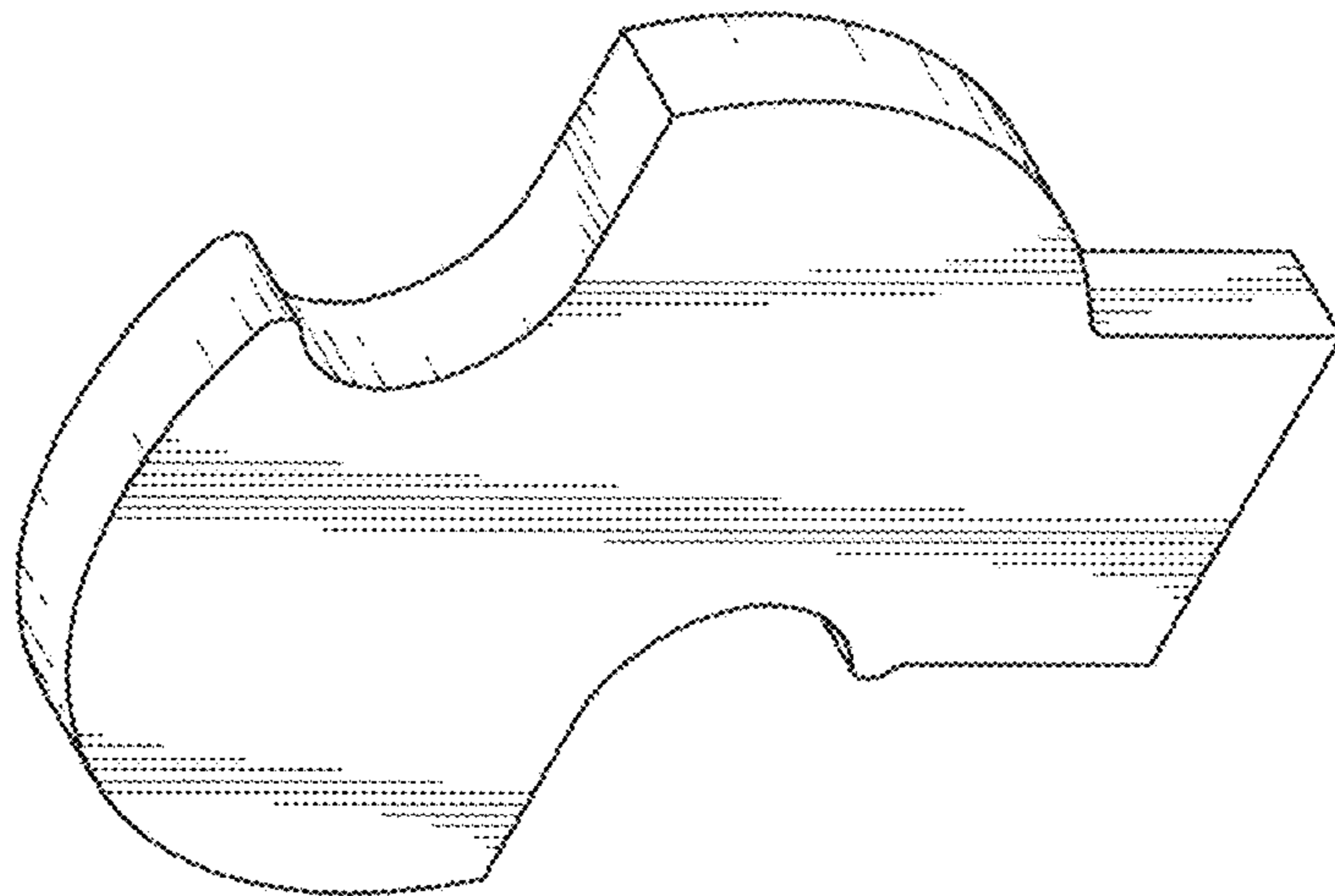


FIG. 4

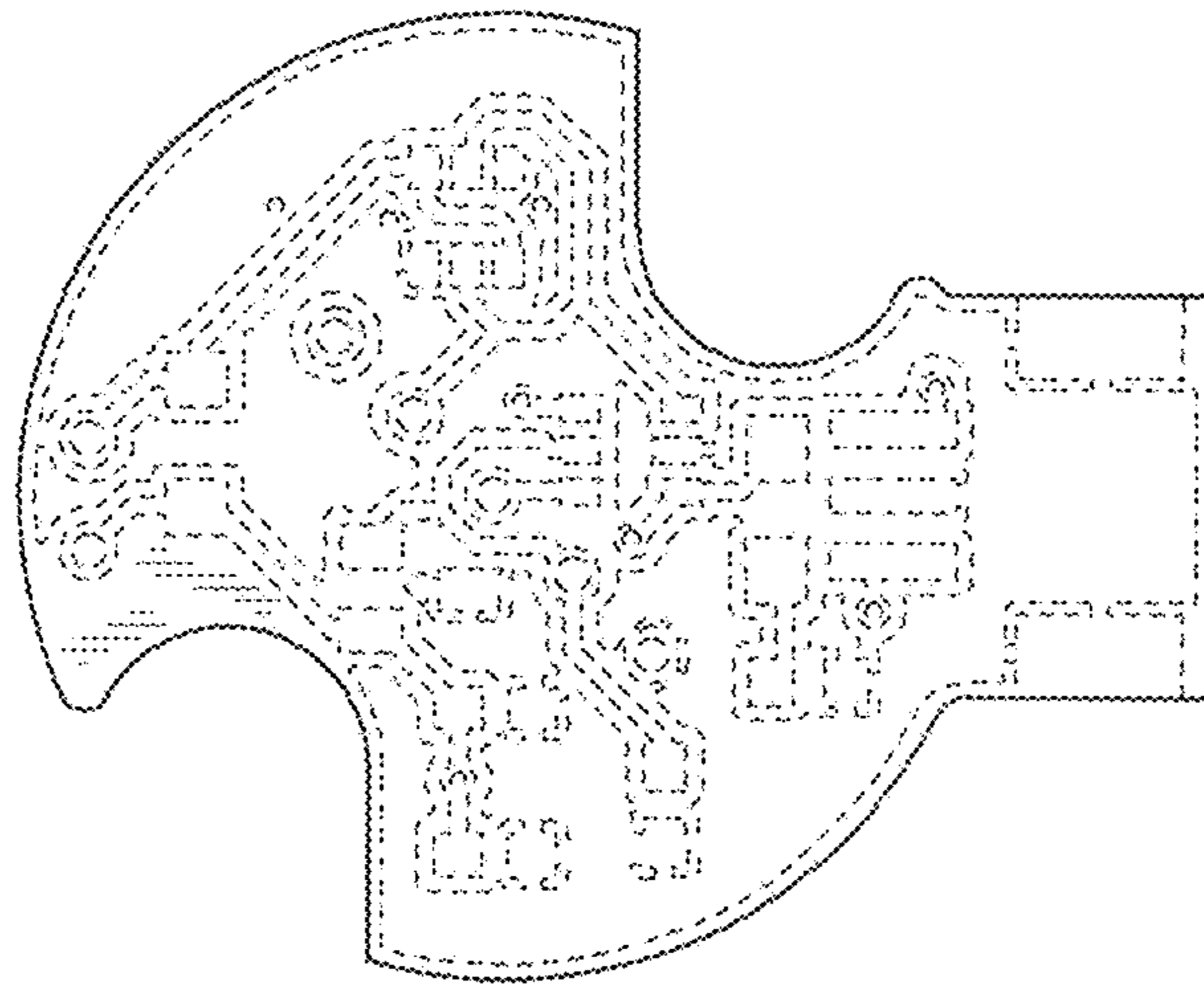


FIG. 3

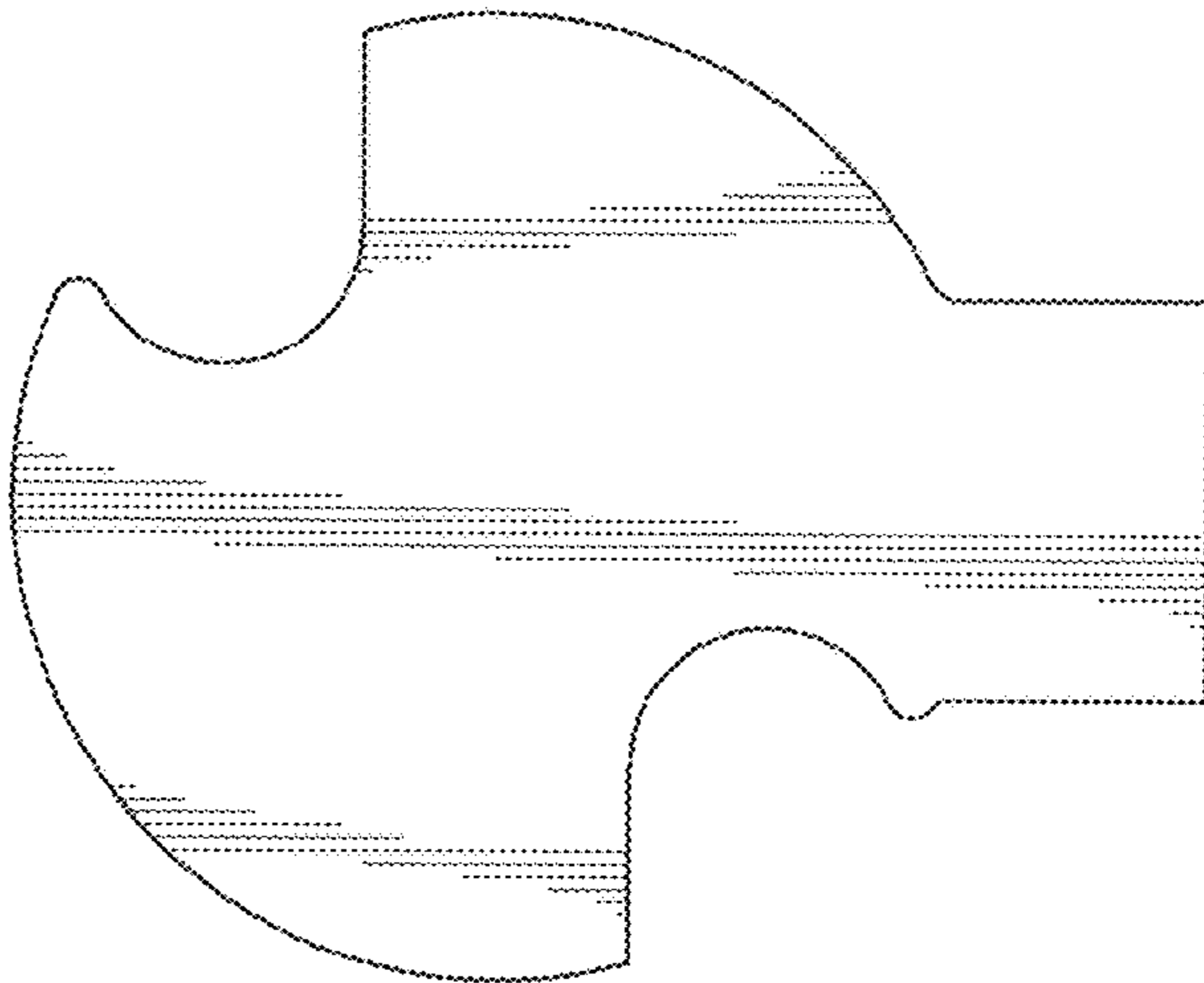


FIG. 5

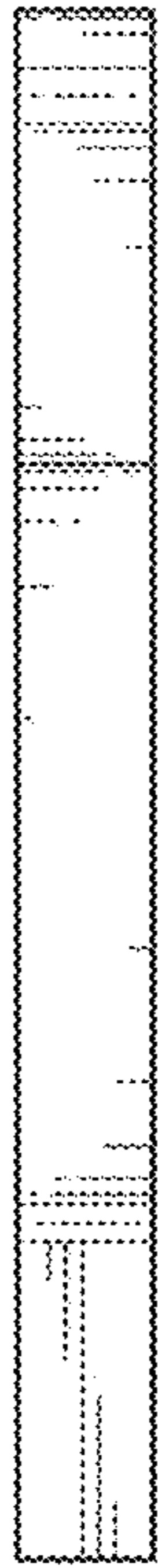


FIG. 6

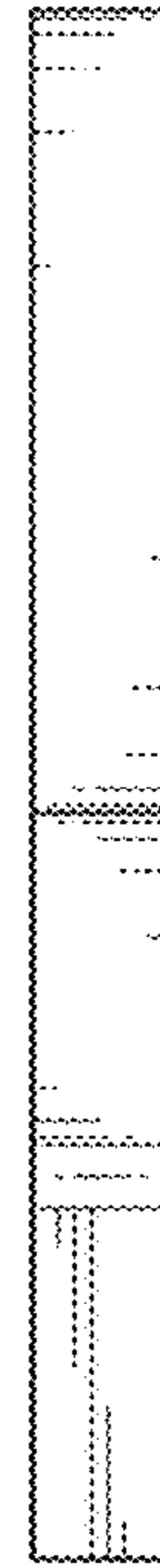


FIG. 7



FIG. 8

