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(12) **United States Design Patent** (10) **Patent No.:** **US D862,404 S**
Murata et al. (45) **Date of Patent:** **** Oct. 8, 2019**

(54) **SEALING MATERIAL RING FOR A SEMICONDUCTOR MANUFACTURING APPARATUS**
(71) Applicants: **KOKUSAI ELECTRIC CORPORATION**, Tokyo (JP); **TOHO KASEI Co., Ltd.**, Nara (JP)
(72) Inventors: **Satoru Murata**, Toyama (JP); **Shinya Morita**, Toyama (JP); **Masashi Suzuki**, Yamatokoriyama (JP)

(73) Assignees: **KOKUSAI ELECTRIC CORPORATION**, Tokyo (JP); **TOHO KASEI CO., LTD.**, Nara (JP)

(**) Term: **15 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (12) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/182, 184, 199; D15/144, 144.1; D30/152; D3/207; D8/399; D11/26
CPC C25D 17/10; C25D 17/12; C25D 17/14; C25D 17/06; C25D 17/08; C25D 7/12; C25D 7/10; H01L 21/283; H01H 9/02; H01H 9/0214; H01H 9/04; H01H 9/161; H01H 13/04; H01H 13/06; H01H 13/023; H01H 21/08; H01H 19/025

See application file for complete search history.

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Primary Examiner — Elizabeth J Oswecki

(74) *Attorney, Agent, or Firm* — Fitch, Even, Tabin & Flannery, L.L.P.

(57) **CLAIM**

The ornamental design for a sealing material ring for a semiconductor manufacturing apparatus, as shown and described.

DESCRIPTION

FIG. 1 is a front, bottom, and right side perspective view of a sealing material ring for a semiconductor manufacturing apparatus, showing our new design; FIG. 2 is a front elevational view thereof; FIG. 3 is a rear elevational view thereof; FIG. 4 is a top plan view thereof; FIG. 5 is a bottom plan view thereof;

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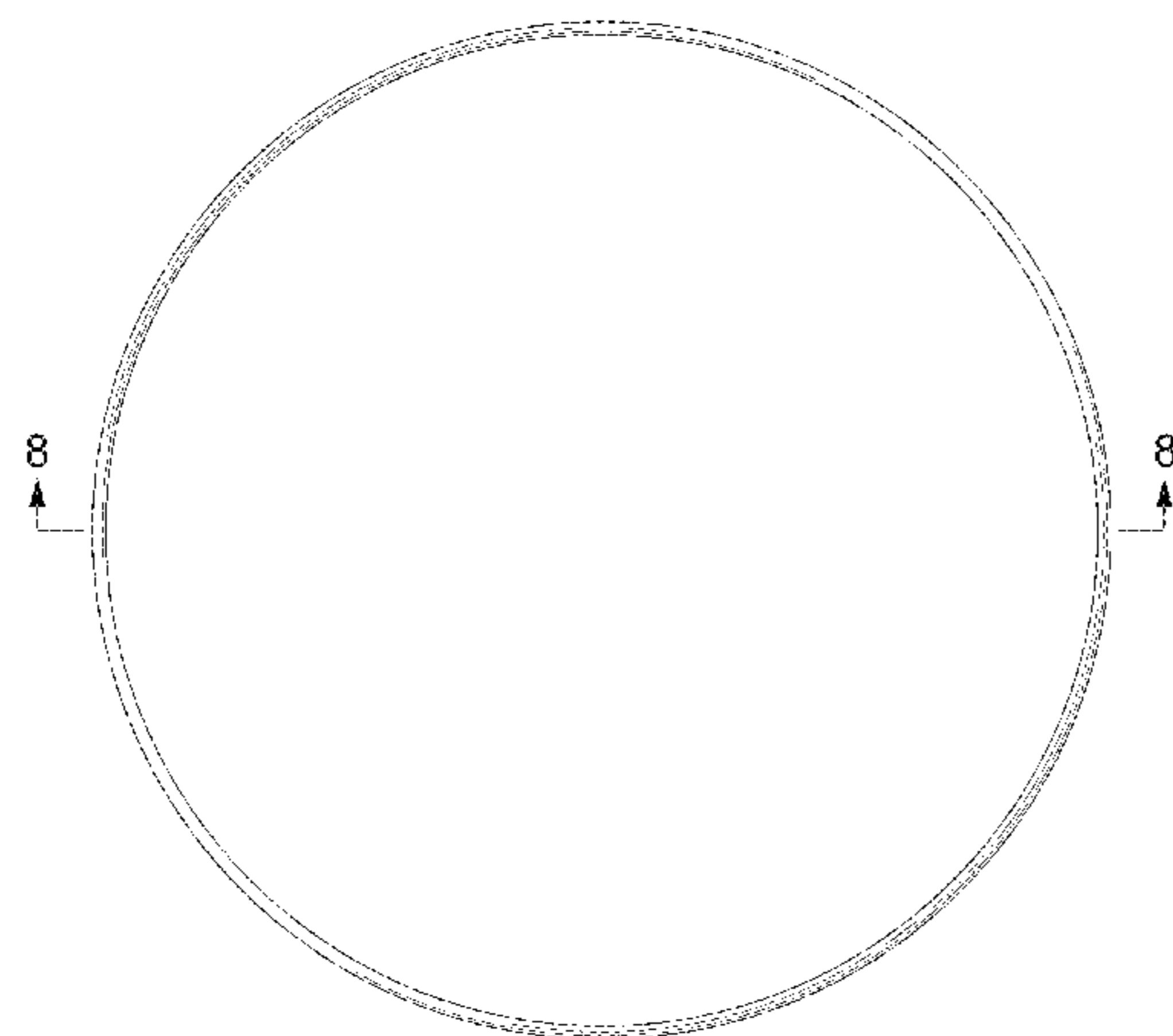


FIG. 6 is a left side elevational view thereof;
 FIG. 7 is a right side elevational view thereof;
 FIG. 8 is a cross-sectional view taken along line 8-8 in FIG. 2 thereof; and,
 FIG. 9 is an enlarged cross-sectional view taken along line 9-9 in FIG. 8.

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1 Claim, 5 Drawing Sheets

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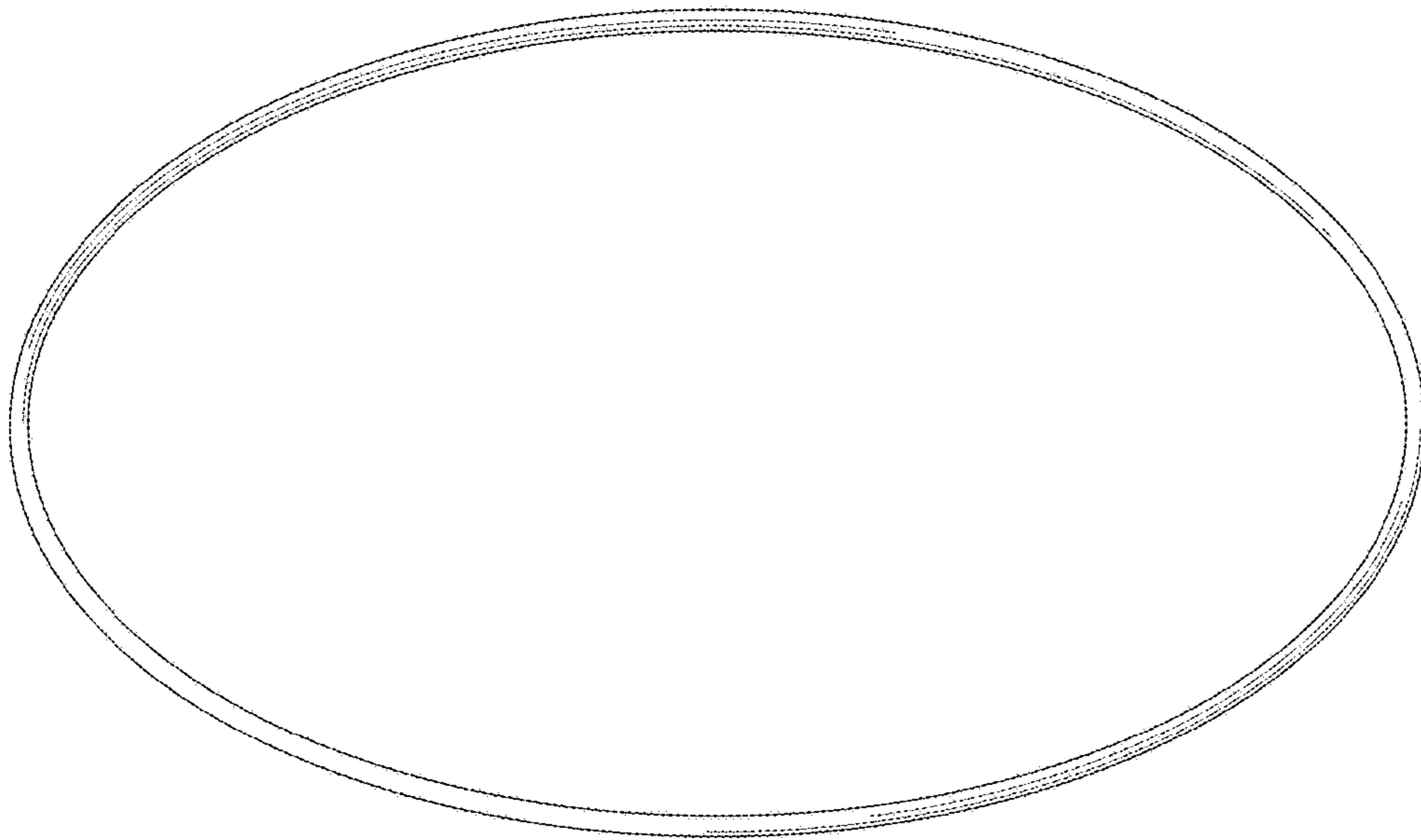


FIG. 1

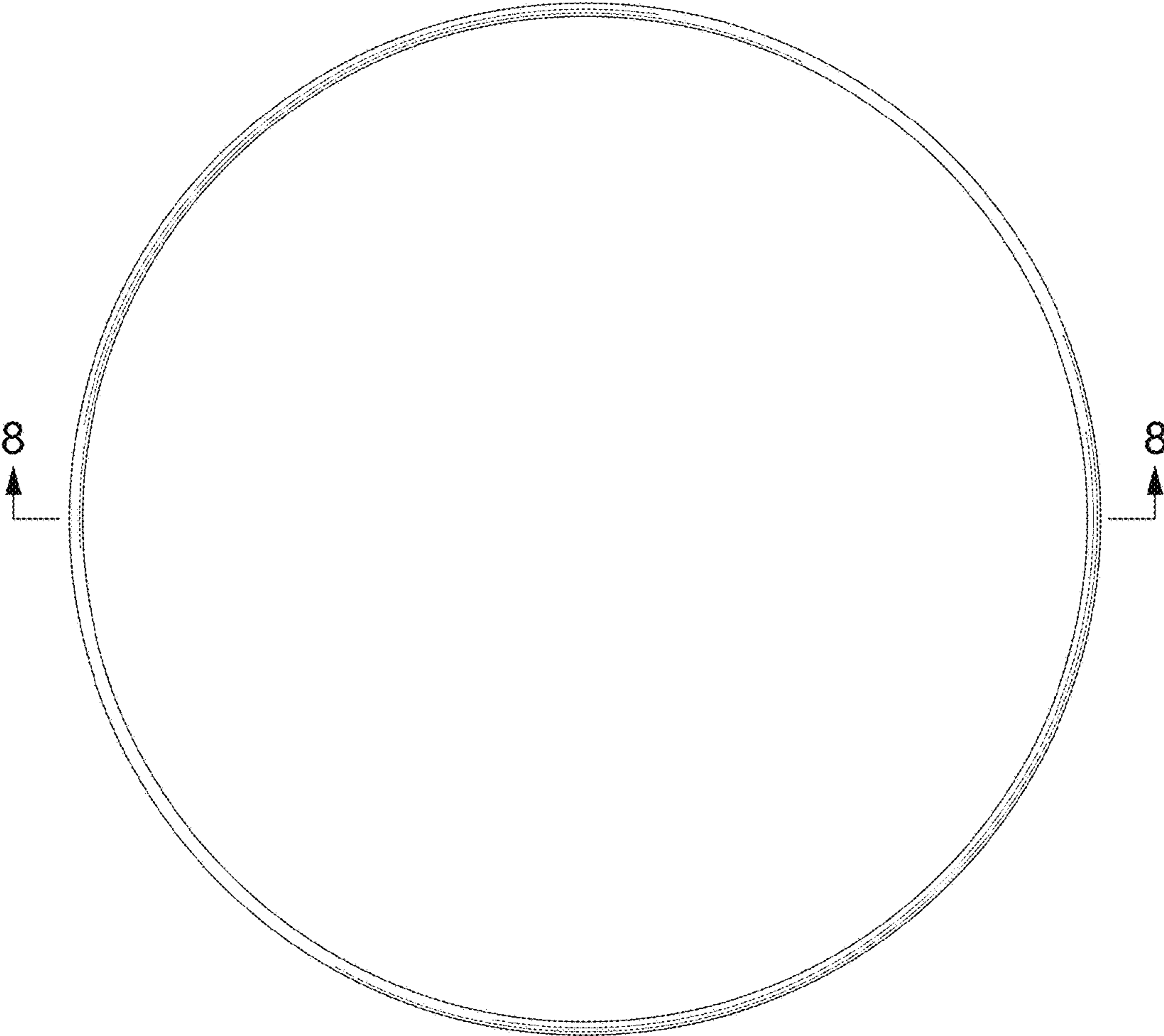


FIG. 2

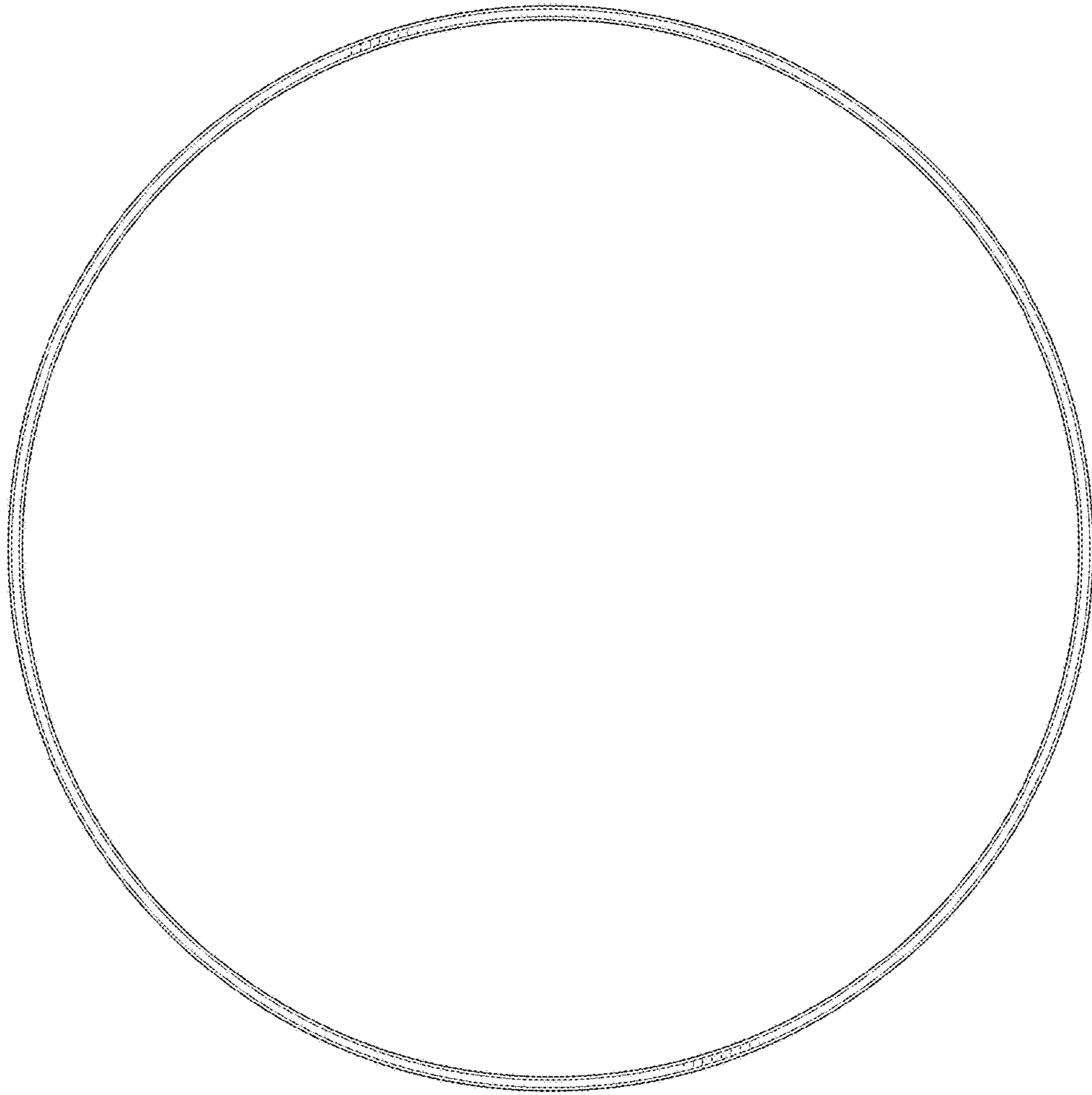


FIG. 3

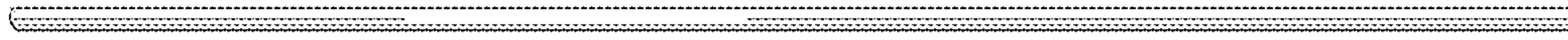


FIG. 4

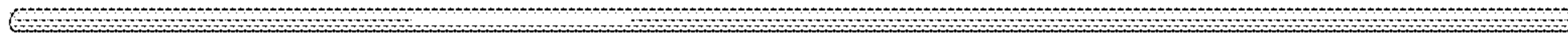


FIG. 5

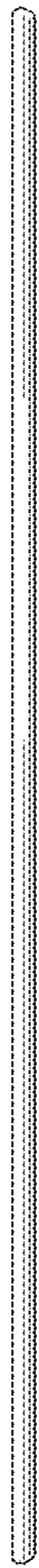


FIG. 6



FIG. 7

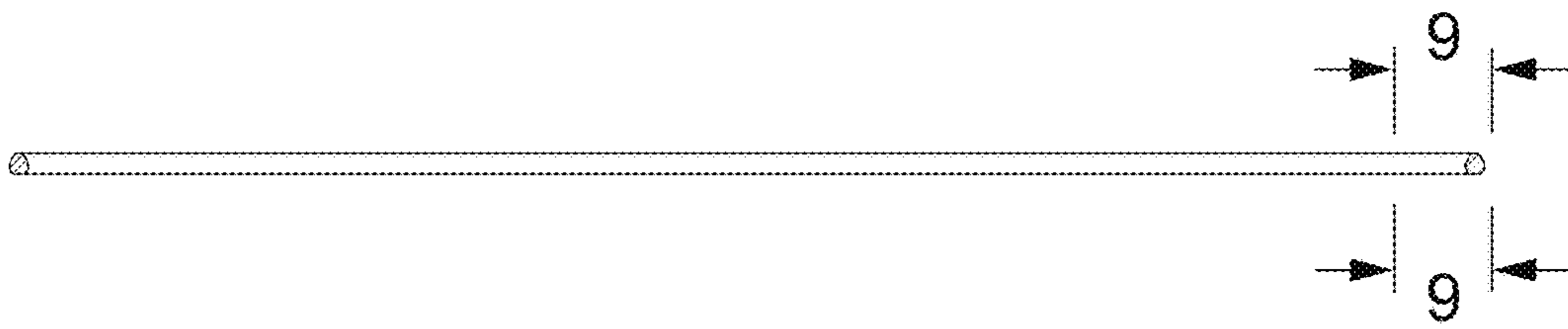


FIG. 8

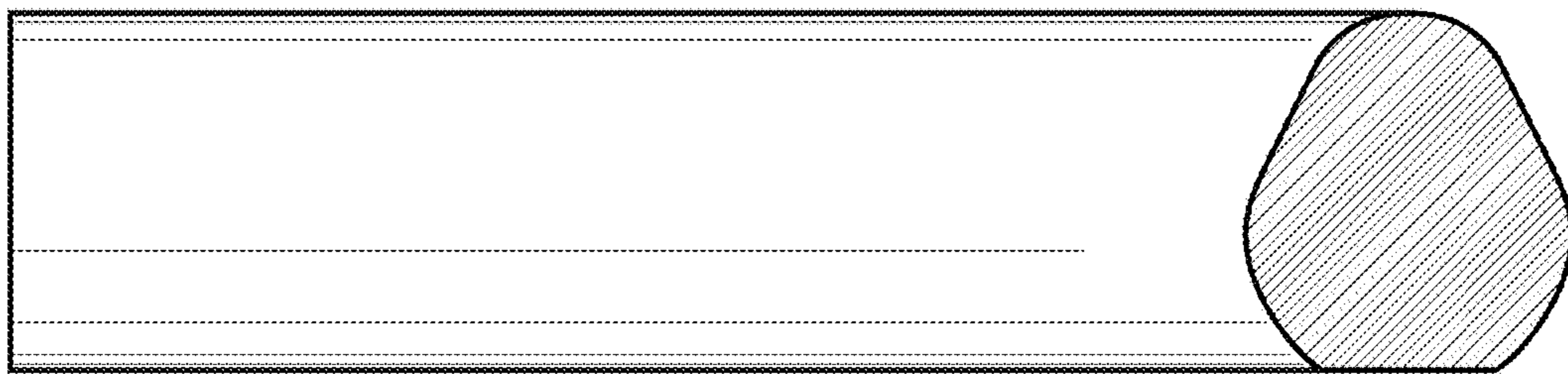


FIG. 9