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(12) **United States Design Patent**
Paleczny et al.

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(45) **Date of Patent:** **** Oct. 1, 2019**

(54) **PROCESSOR-CONTROLLED TRANSACTION CARD WITH ELECTRONIC DISPLAY**

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B42D 15/045; B42D 15/047; B42D
25/00; B42D 25/20; B42D 25/23; B42D
25/26; B42D 25/29; B42D 25/30; B42D
25/40; B42D 25/22; B42D 2033/02;
B42D 2033/04; B42D 2033/08; B42D
2033/10; B42D 2033/16; B42D 2033/18;
B42D 2033/20; B42D 2033/28; B42D
2033/30

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See application file for complete search history.

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(**) Term: **15 Years**

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(57) **CLAIM**

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The ornamental design for a processor-controlled transac-
tion card with electronic display, as shown and described.

(51) **LOC (12) Cl.** **19-08**

(52) **U.S. Cl.**

DESCRIPTION

USPC **D19/10**

(58) **Field of Classification Search**

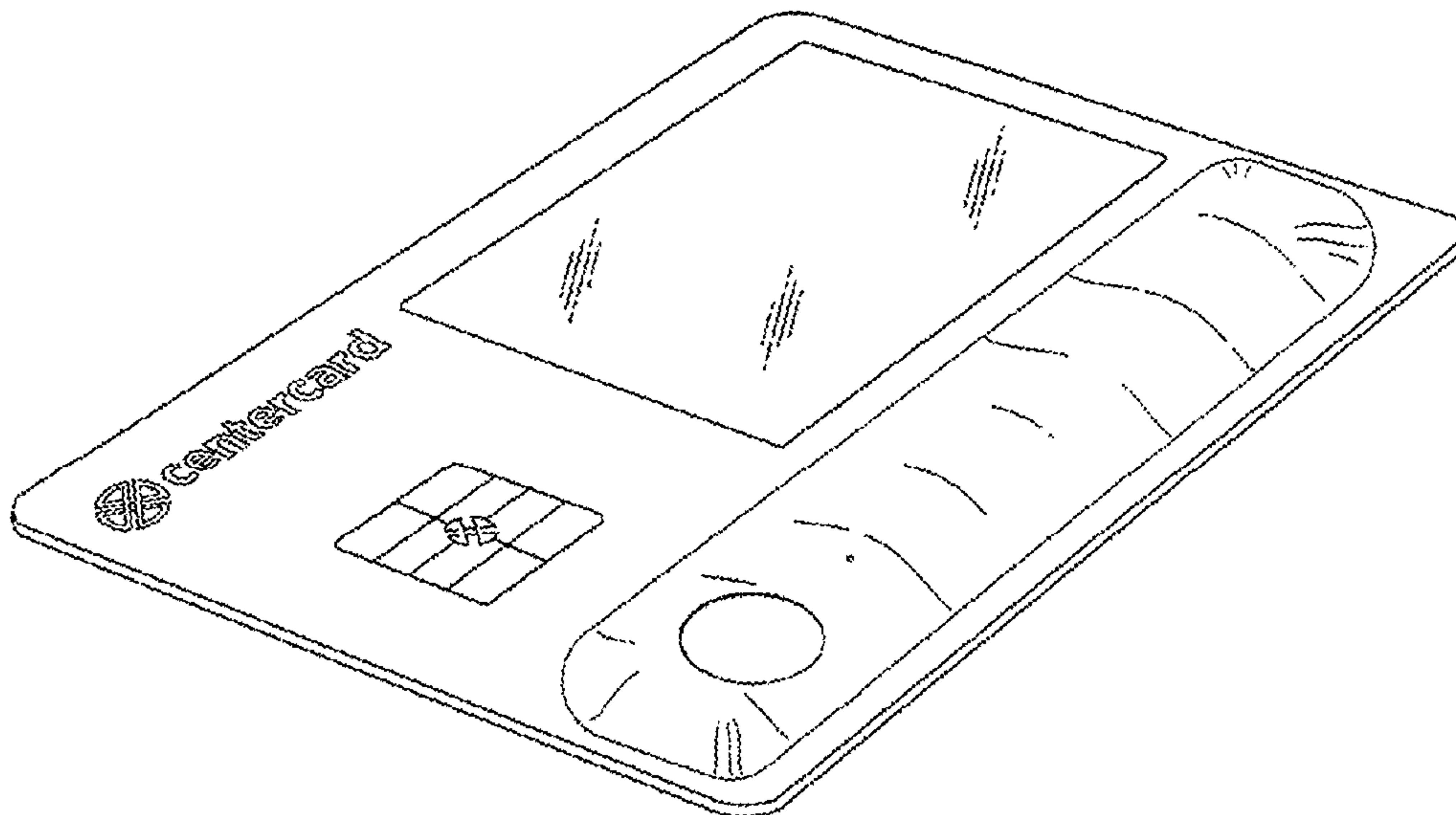
USPC D19/1-10, 20-34; 40/124.01-124.15,
40/661, 776, 726; 283/72, 75-76,
283/105-107, 82, 70, 904, 74; 206/449;
D21/385; D20/10-11, 22, 27, 40, 42;
360/2; 235/380, 382

FIG. 1 is a top plan view of the processor-controlled
transaction card with electronic display showing our new
design;

FIG. 2 is a top perspective view thereof; and,

FIG. 3 is a side view thereof.

1 Claim, 1 Drawing Sheet



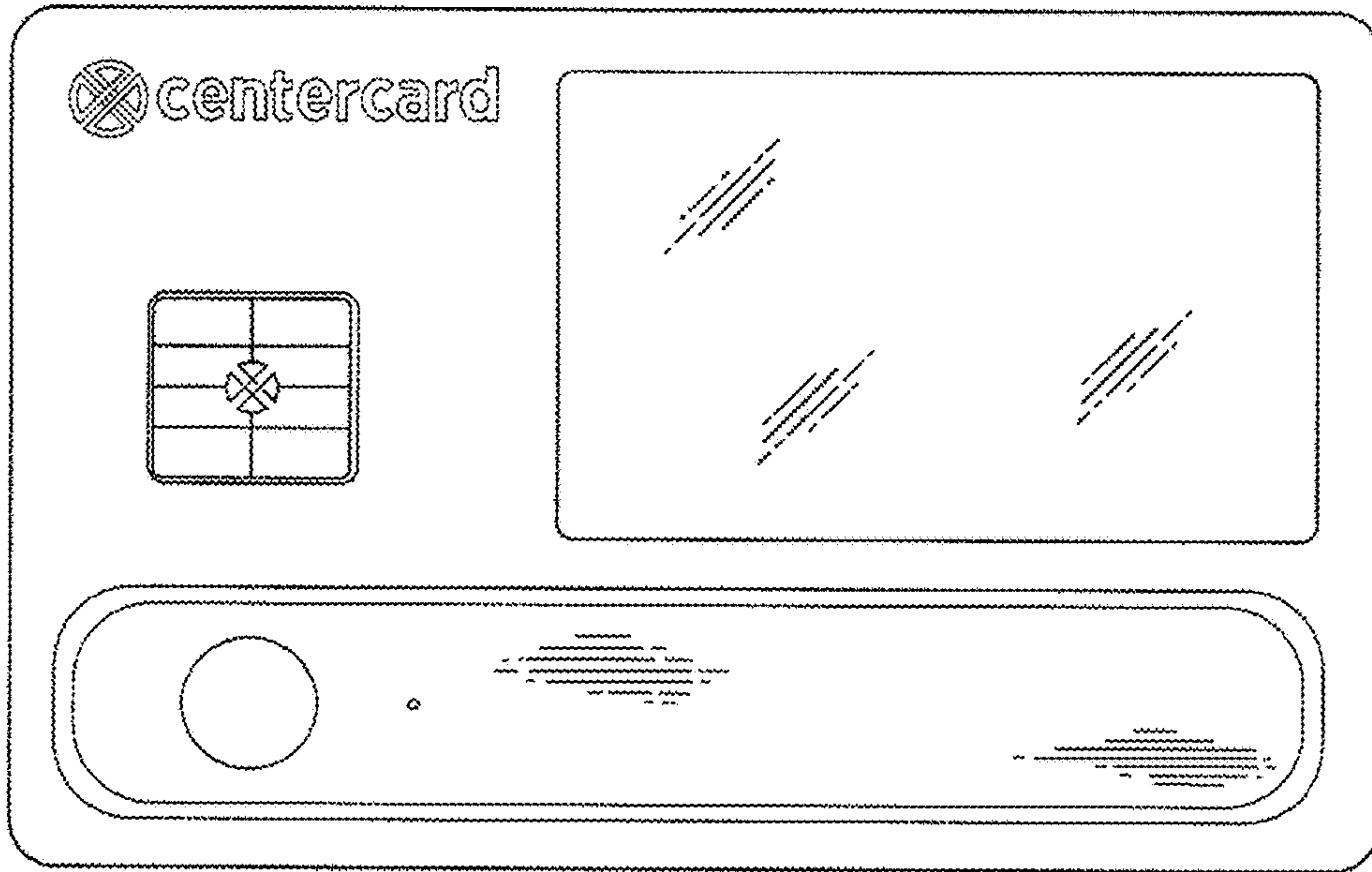


FIG. 1

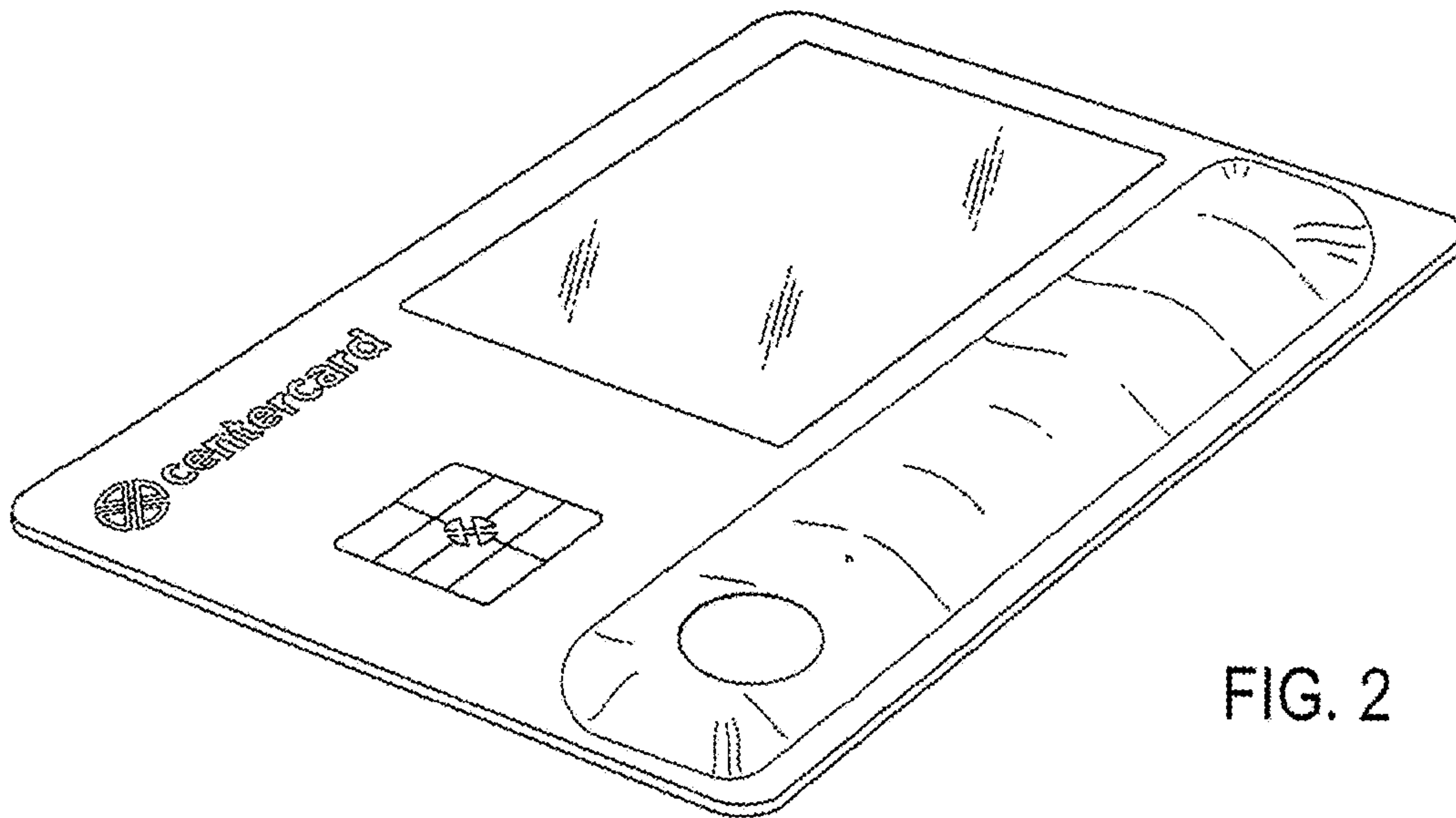


FIG. 2

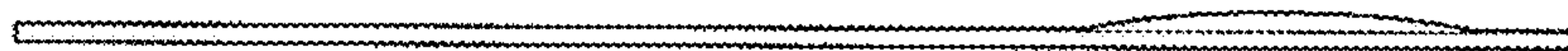


FIG. 3