

US00D861519S

(12) **United States Design Patent**  
**Peters**

(10) **Patent No.:** **US D861,519 S**

(45) **Date of Patent:** **\*\* Oct. 1, 2019**

(54) **ELECTRICAL SYSTEM TESTER**

D793,256 S \* 8/2017 Iseli ..... D10/75  
9,753,088 B2 \* 9/2017 Ashtekar ..... G01R 31/3272

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\* cited by examiner

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(\*\*) Term: **15 Years**

(57) **CLAIM**

(21) Appl. No.: **29/637,185**

The ornamental design for an electrical system tester, as shown and described.

(22) Filed: **Feb. 14, 2018**

(51) **LOC (12) Cl.** ..... **10-04**

(52) **U.S. Cl.**  
USPC ..... **D10/78**

(58) **Field of Classification Search**

USPC ..... D10/75  
CPC . G06F 1/28; G06F 1/30; G06F 1/3203; G06F  
17/12; G01D 9/00; G01D 9/005; H01M  
10/44; H01M 10/46; H01M 10/48; G01R  
31/025; G01R 31/04; G01R 19/0007;  
G01R 27/20; H02H 3/00; H02H 9/08

See application file for complete search history.

**DESCRIPTION**

FIG. 1 is a perspective view of an electrical system tester depicting a new design;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a side view thereof;

FIG. 5 is an opposite side view thereof;

FIG. 6 is a top view thereof; and,

FIG. 7 is a bottom view thereof.

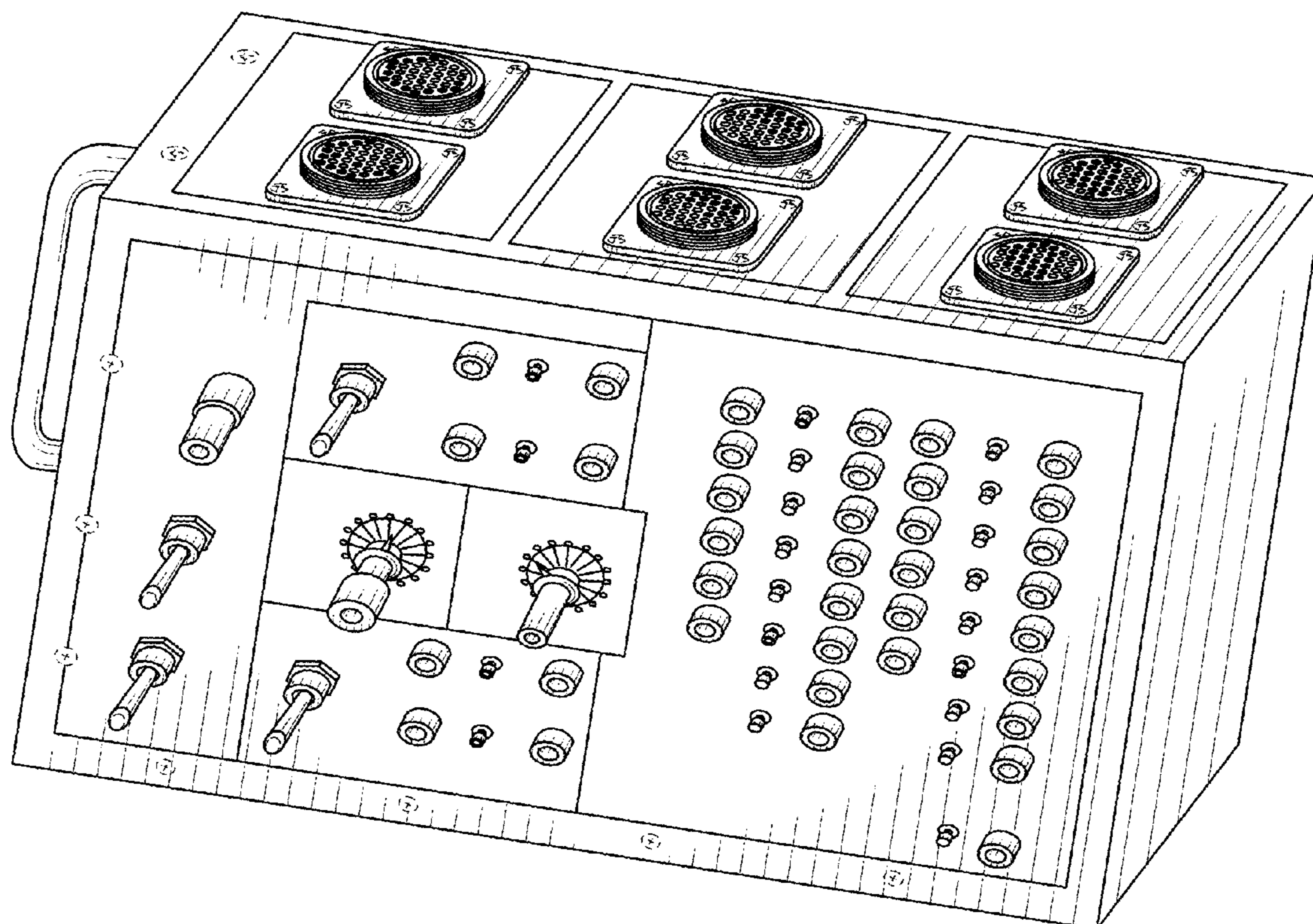
The broken line portion of the figure drawings is included to show unclaimed subject matter only and forms no part of the claimed design.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

D300,123 S \* 3/1989 Nichols ..... D10/102  
D343,799 S \* 2/1994 Spinks ..... D10/75

**1 Claim, 5 Drawing Sheets**



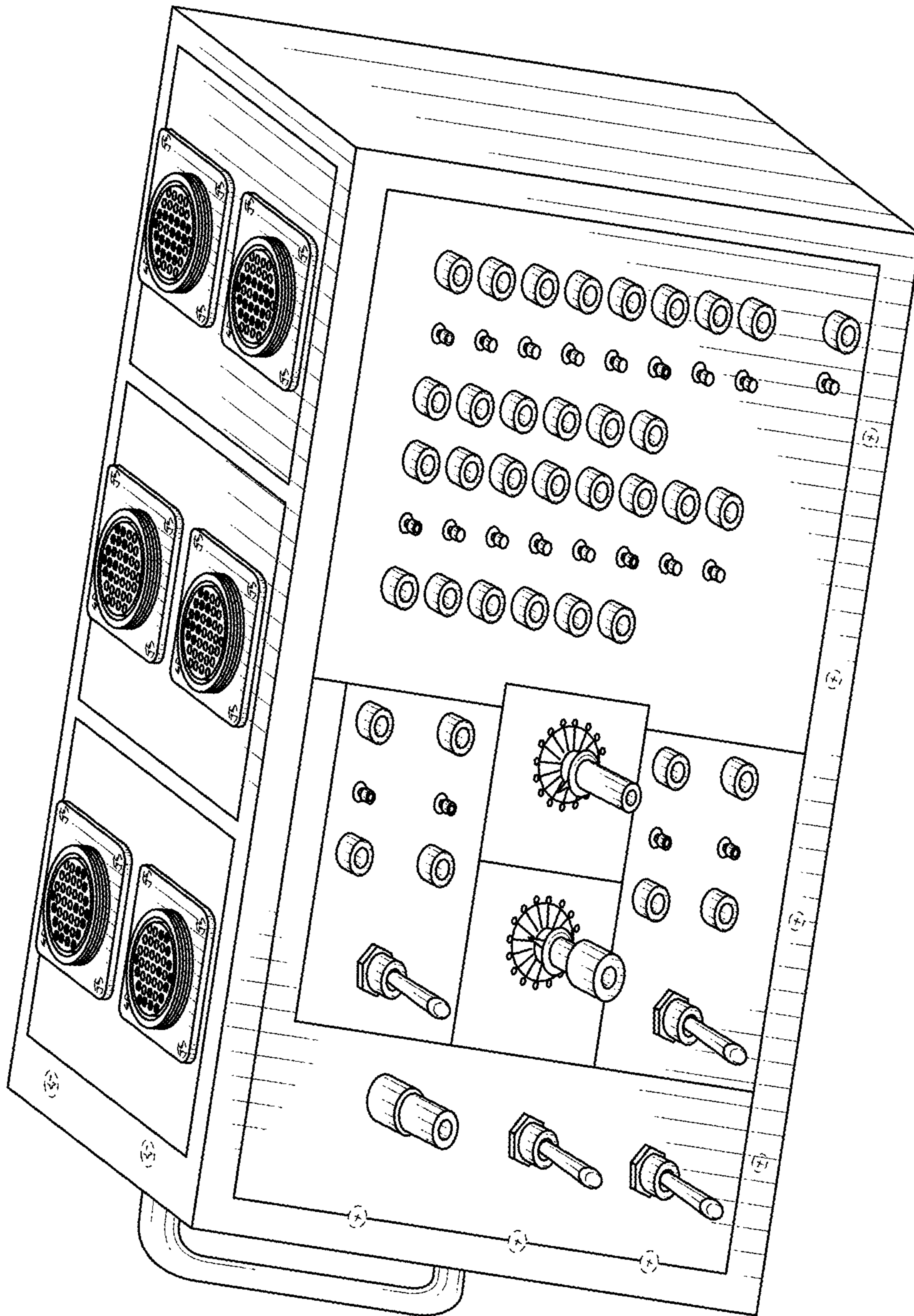


FIG. 1

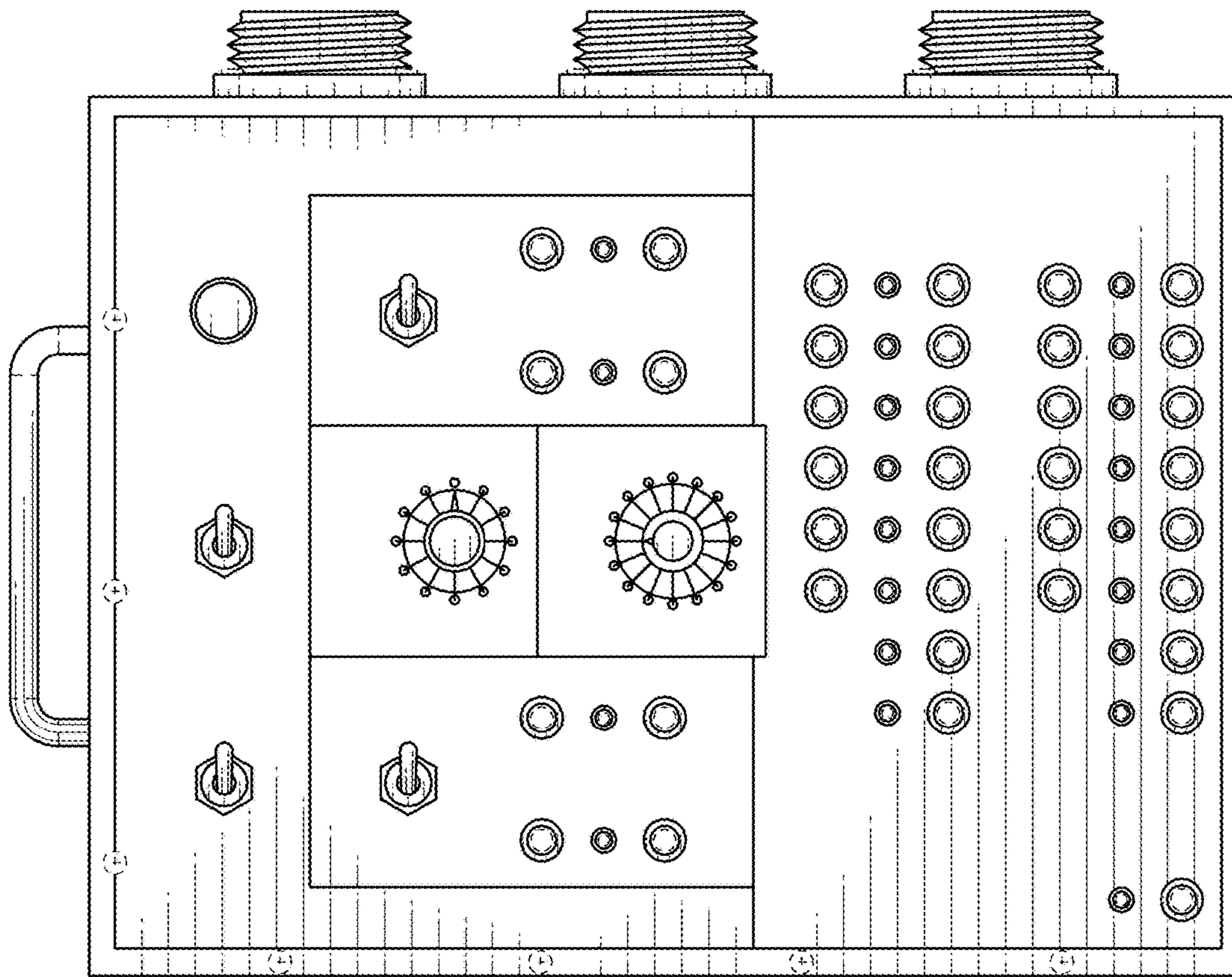


FIG. 2

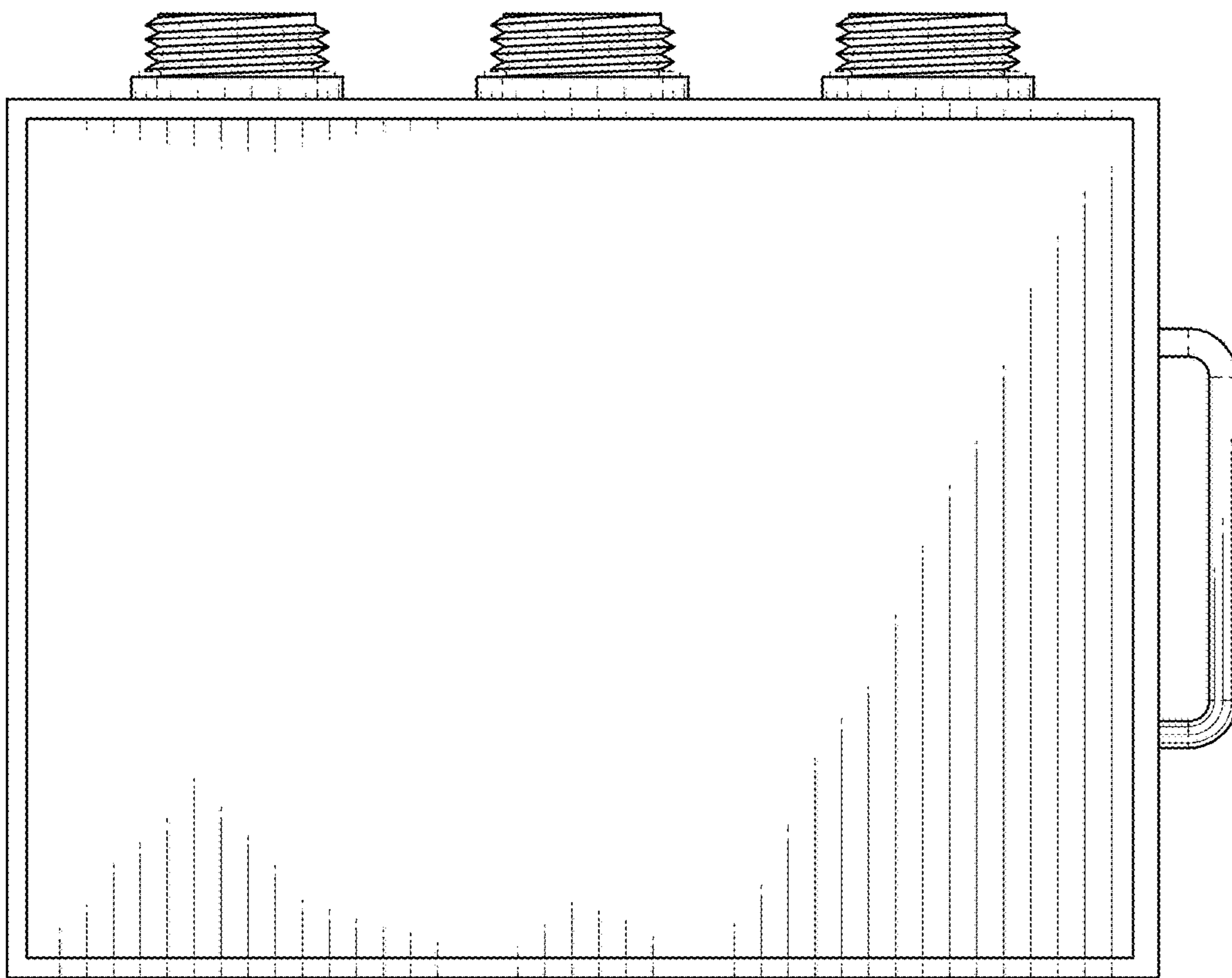


FIG. 3

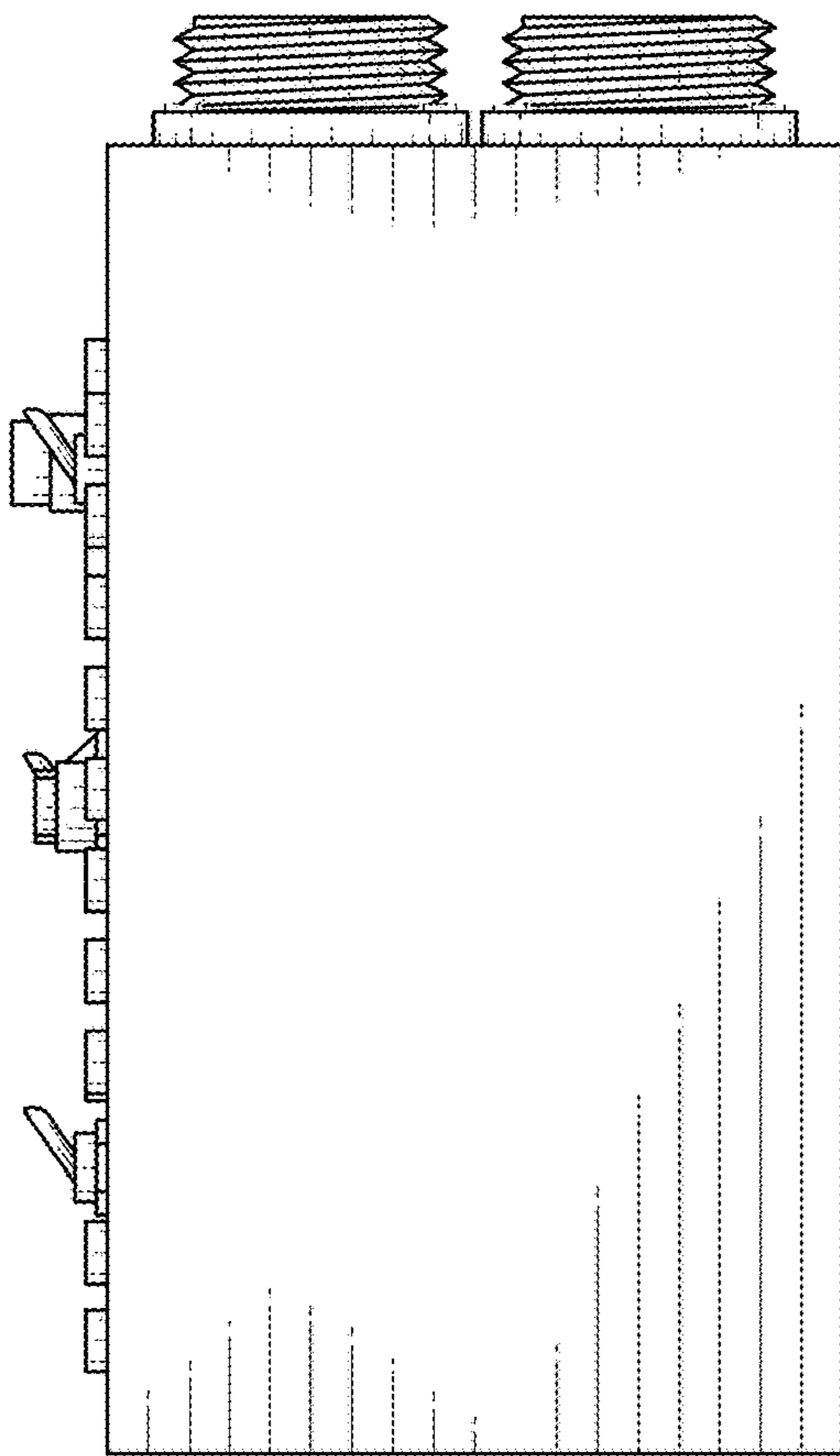


FIG. 4

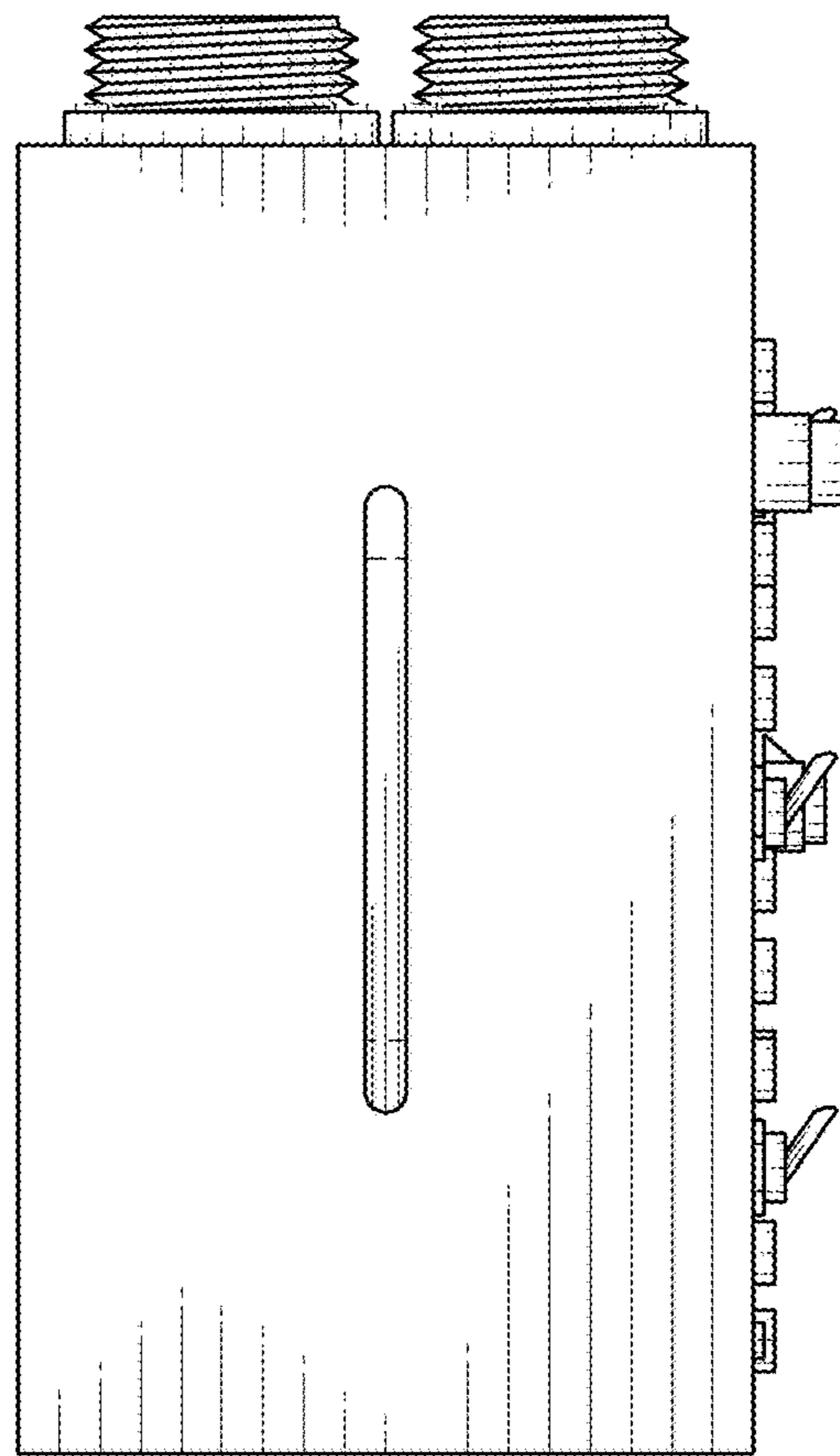


FIG. 5

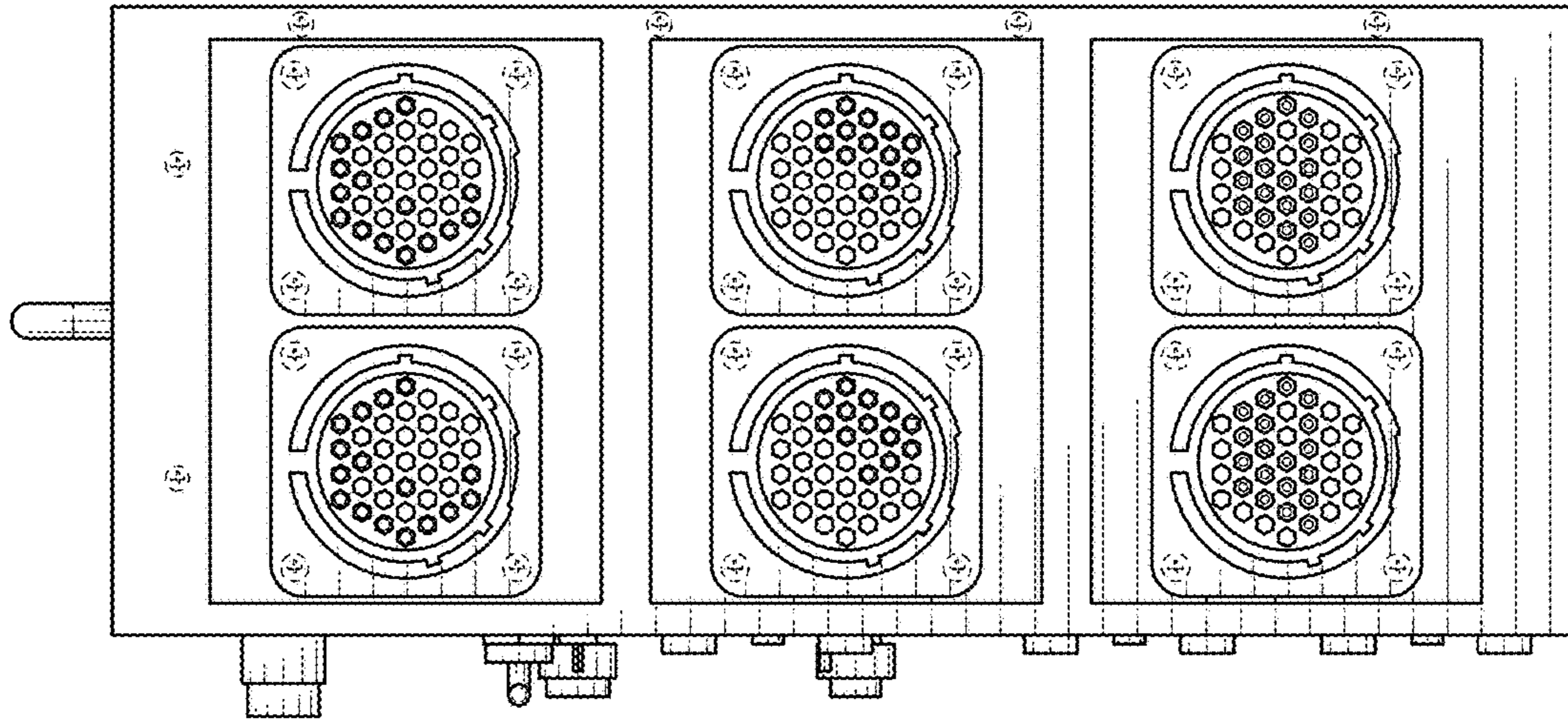


FIG. 6

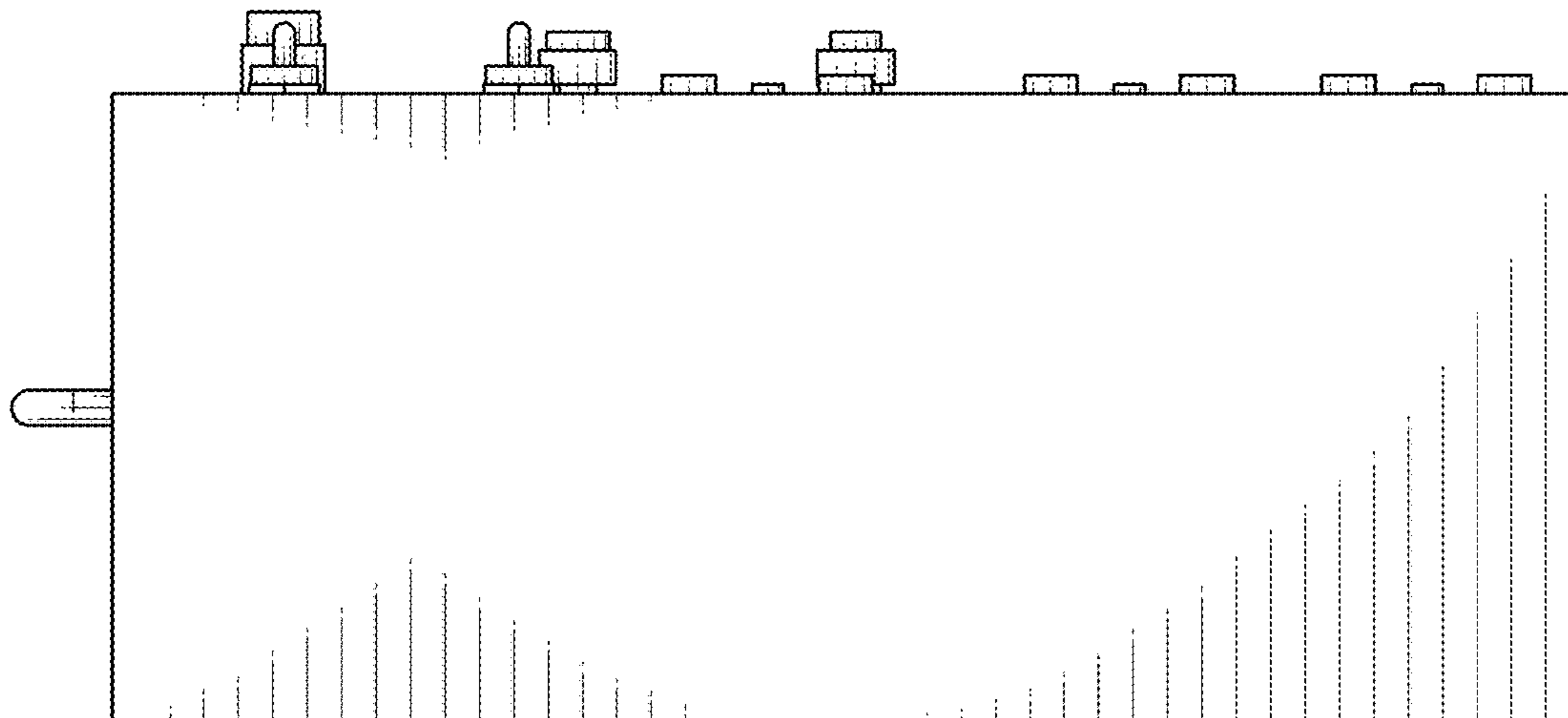


FIG. 7