



US00D857678S

(12) **United States Design Patent**
Vardi(10) **Patent No.:** US D857,678 S
(45) **Date of Patent:** ** Aug. 27, 2019(54) **GATEWAY ELECTRONIC DEVICE**(71) Applicant: **ETROG SYSTEMS LTD.**, Jerusalem (IL)(72) Inventor: **Eyal Dov Vardi**, Bet Nir (IL)(73) Assignee: **ETROG SYSTEMS LTD**, Jerusalem (IL)(***) Term: **15 Years**(21) Appl. No.: **29/643,765**(22) Filed: **Apr. 11, 2018**(51) LOC (12) Cl. **14-03**

(52) U.S. Cl.

USPC **D14/242**; D14/358(58) **Field of Classification Search**

USPC ... D13/158, 159, 123, 124, 162, 162.1, 168, D13/173, 184, 199; D14/356, 357, 358, D14/388, 432, 433, 496, 217, 218, 240, D14/242, 299, 125, 155, 168, 175, 300, D14/301, 302, 314, 348, 355, 365; D24/107, 167, 185, 186

CPC H04W 88/00; H04W 88/02; H04W 88/08; H04W 88/12; H04W 88/16; H04M 1/23; H04M 1/26; H04M 11/06; H04M 13/00; H04L 2/2803; H04L 2012/5618

See application file for complete search history.

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(57)

CLAIM

The ornamental design for a gateway electronic device, as shown and described.

DESCRIPTION

FIG. 1 is perspective view of a gateway electronic device showing my new design;

FIG. 2 is a front view thereof;

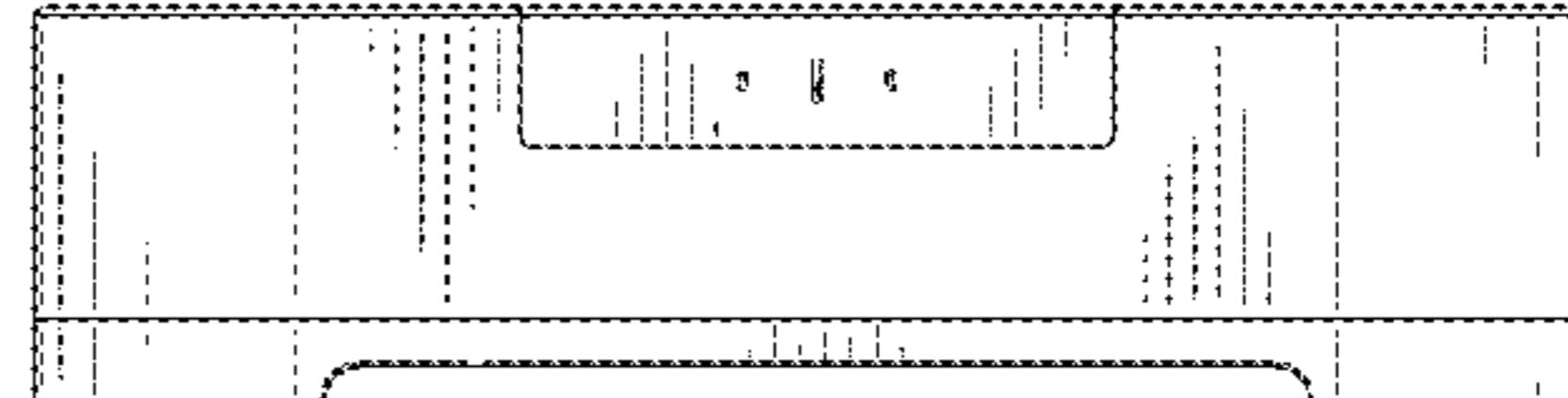
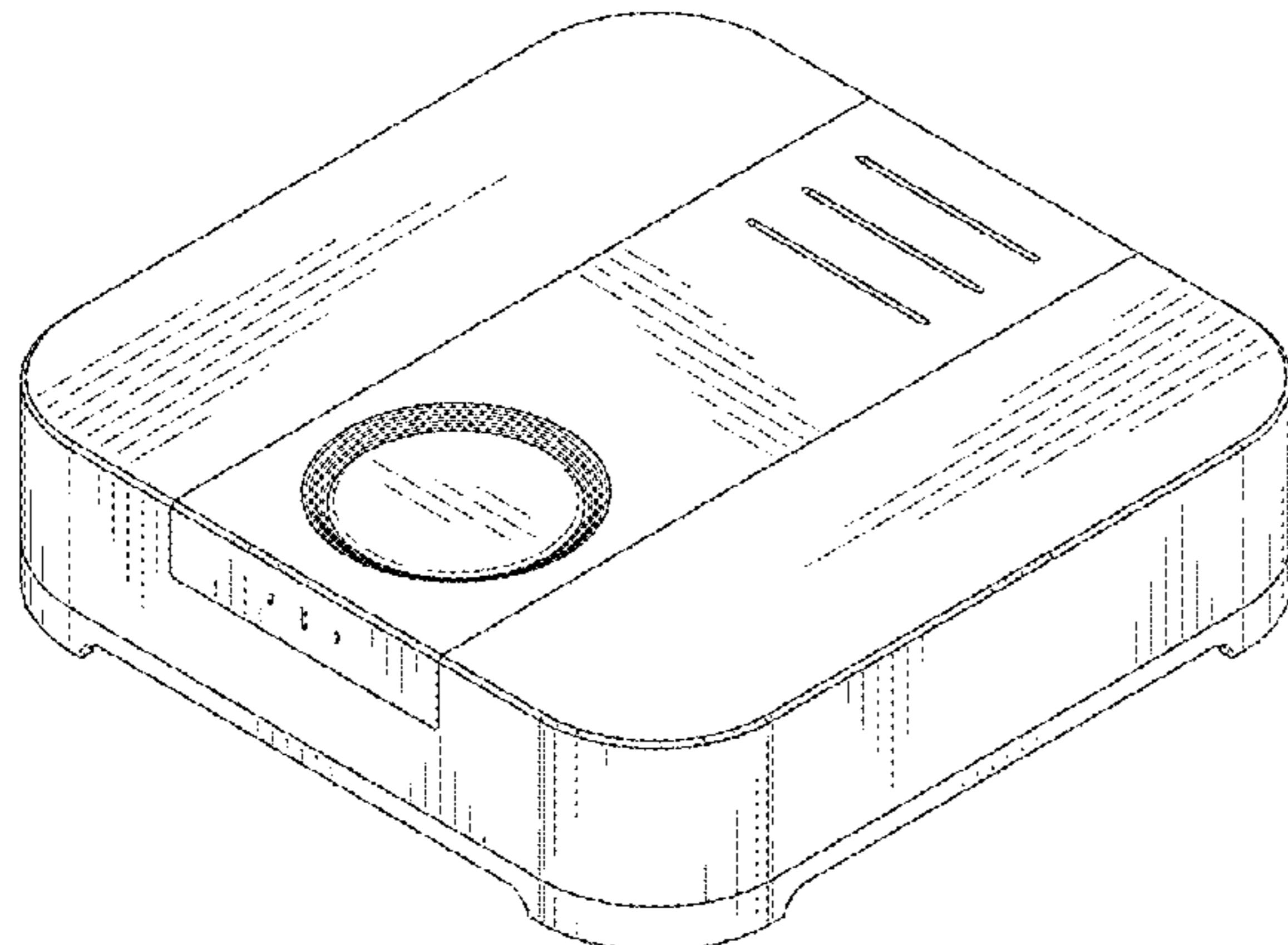
FIG. 3 is a rear view thereof;

FIG. 4 is a side view thereof;

FIG. 5 is an opposite side view thereof;

FIG. 6 is a top view thereof; and,

FIG. 7 is a bottom view thereof.

1 Claim, 5 Drawing Sheets

(56)

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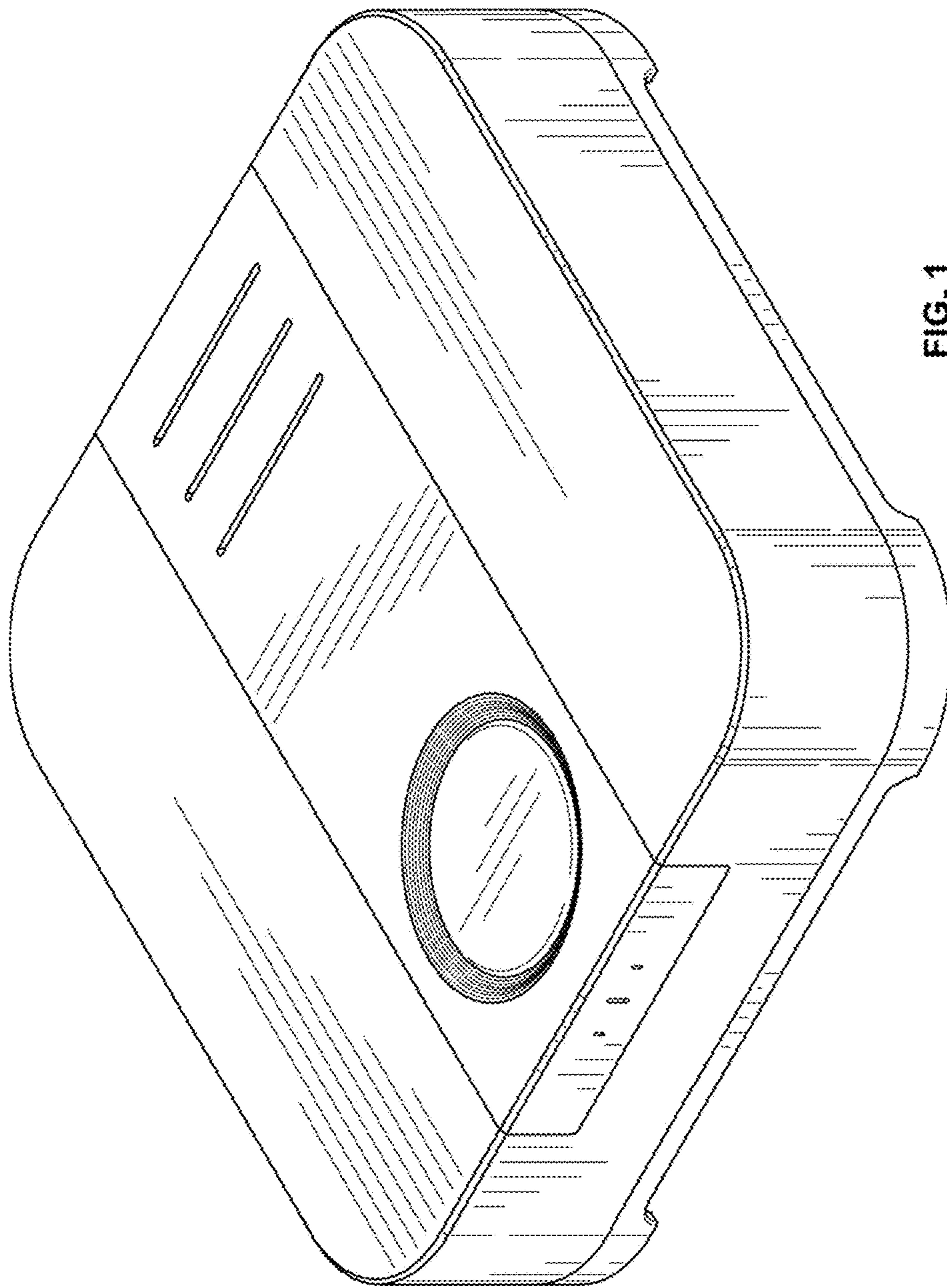


FIG. 1

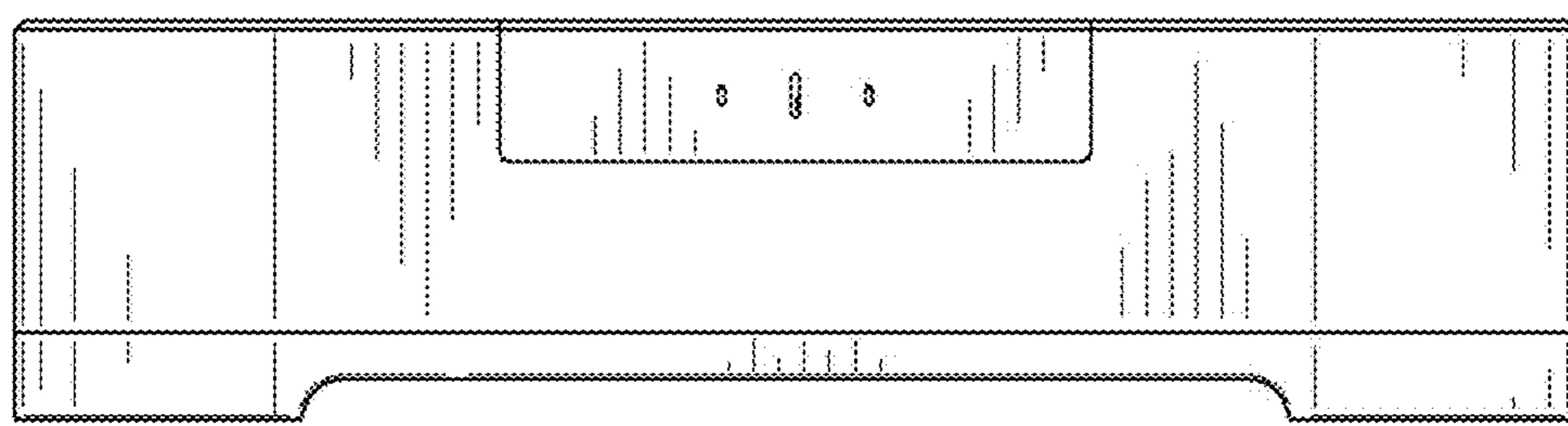


FIG. 2

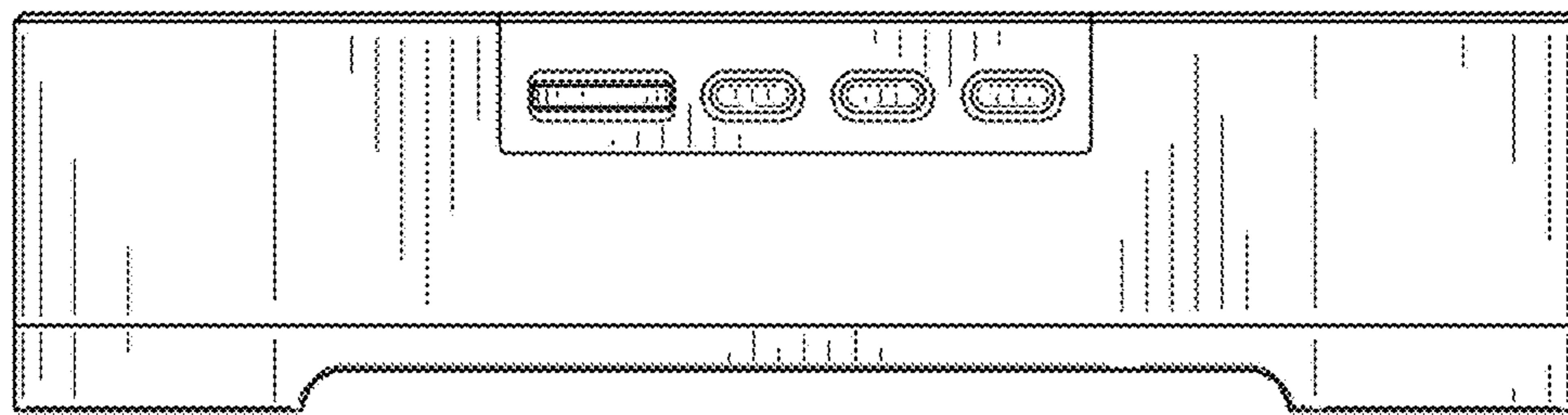


FIG. 3

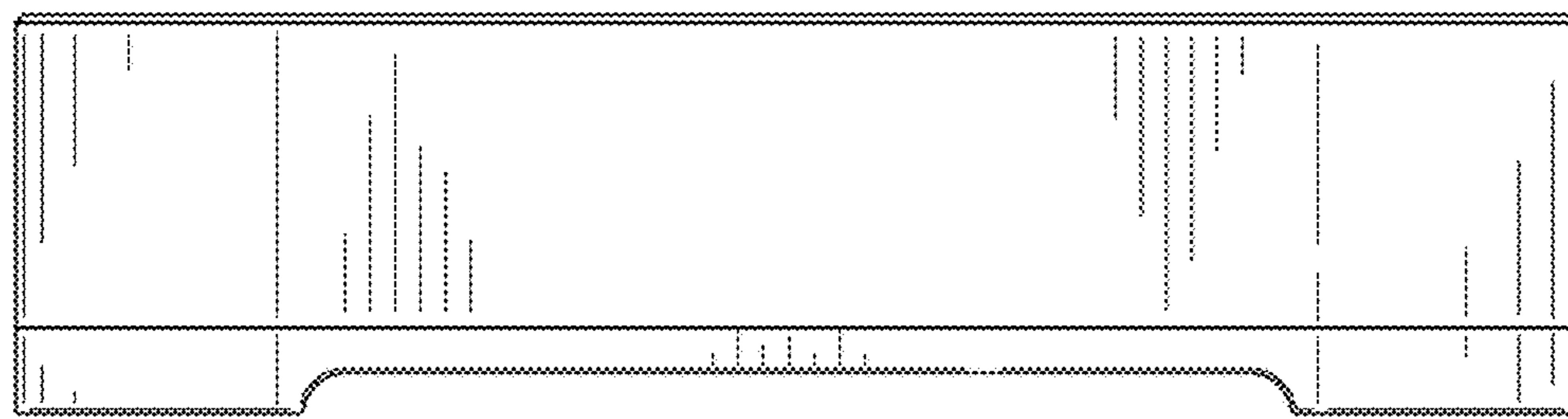


FIG. 4

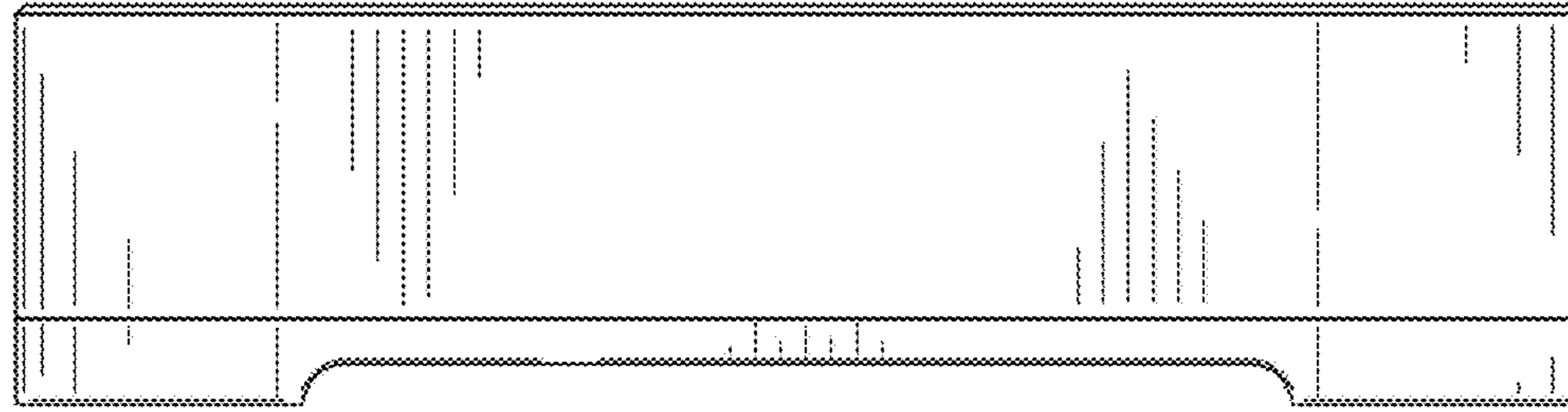


FIG. 5

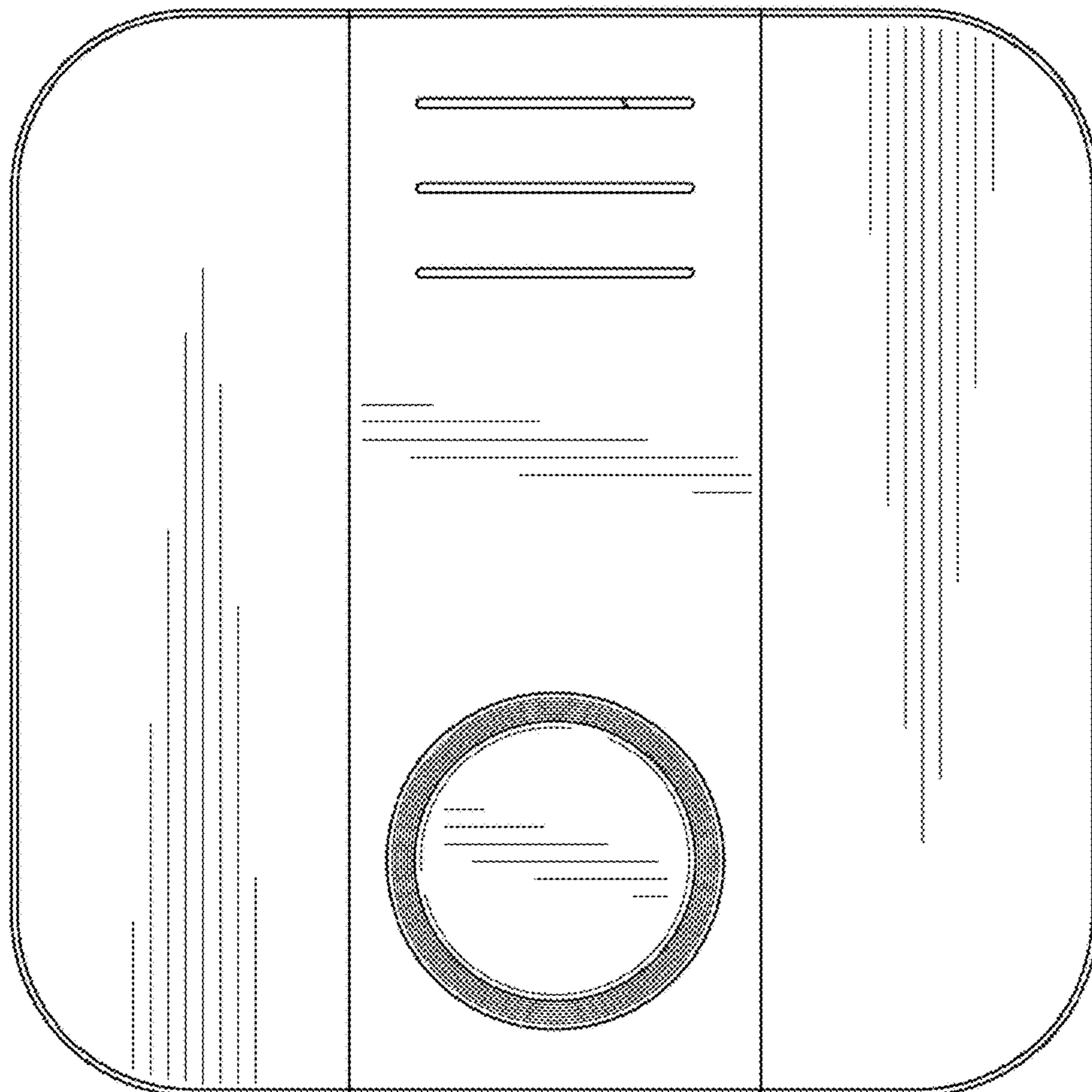


FIG. 6

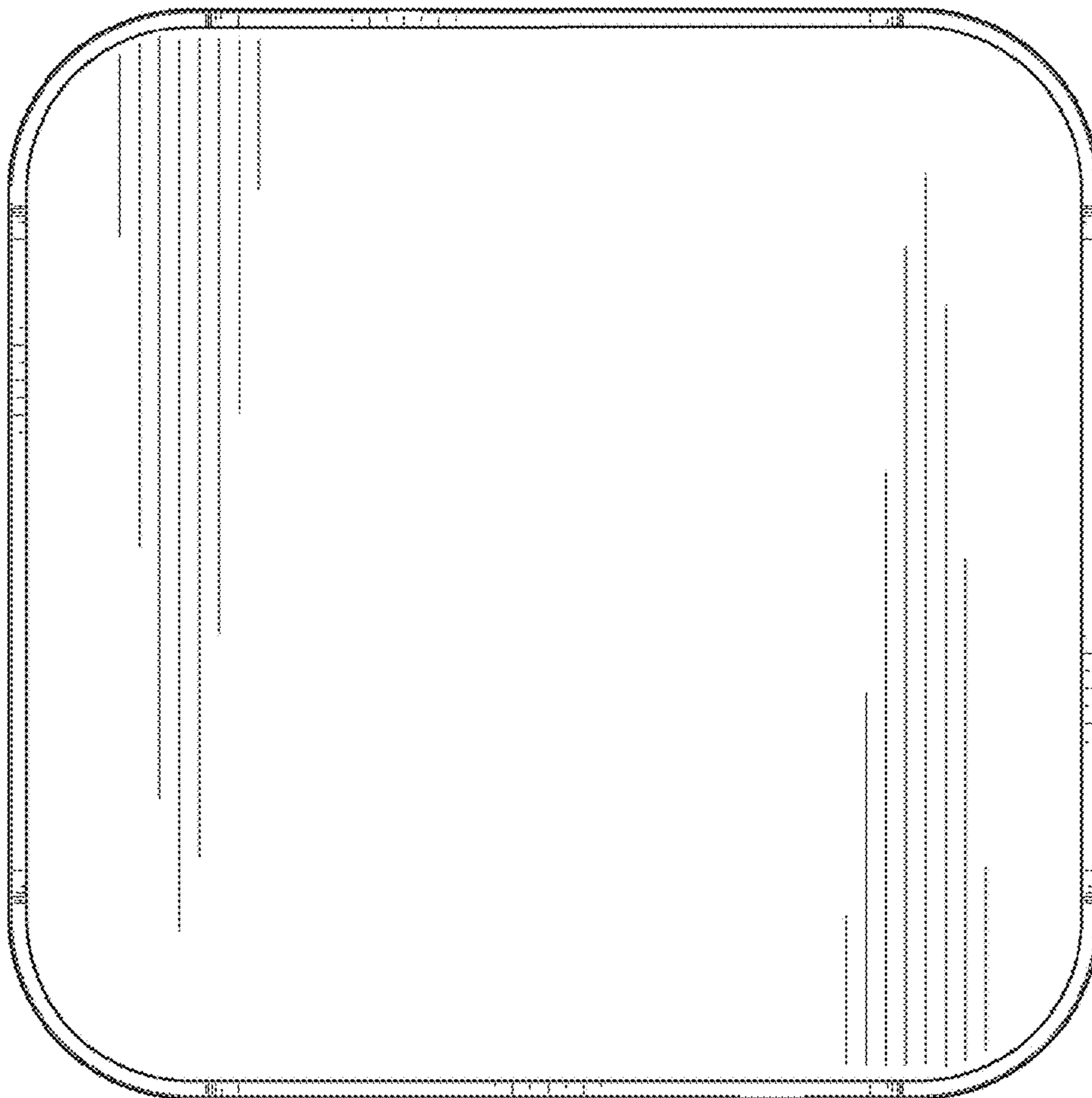


FIG. 7