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(12) **United States Design Patent** (10) **Patent No.:** **US D855,623 S**
Elias et al. (45) **Date of Patent:** **** *Aug. 6, 2019**

(54) **ELECTRONIC DEVICE MOUNT**
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(*) Notice: This patent is subject to a terminal disclaimer.
(**) Term: **15 Years**

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Related U.S. Application Data

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(51) **LOC (12) Cl.** **08-07**
(52) **U.S. Cl.**
USPC **D14/447**
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See application file for complete search history.

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(57) **CLAIM**

The ornamental design for an electronic device mount, as shown and described.

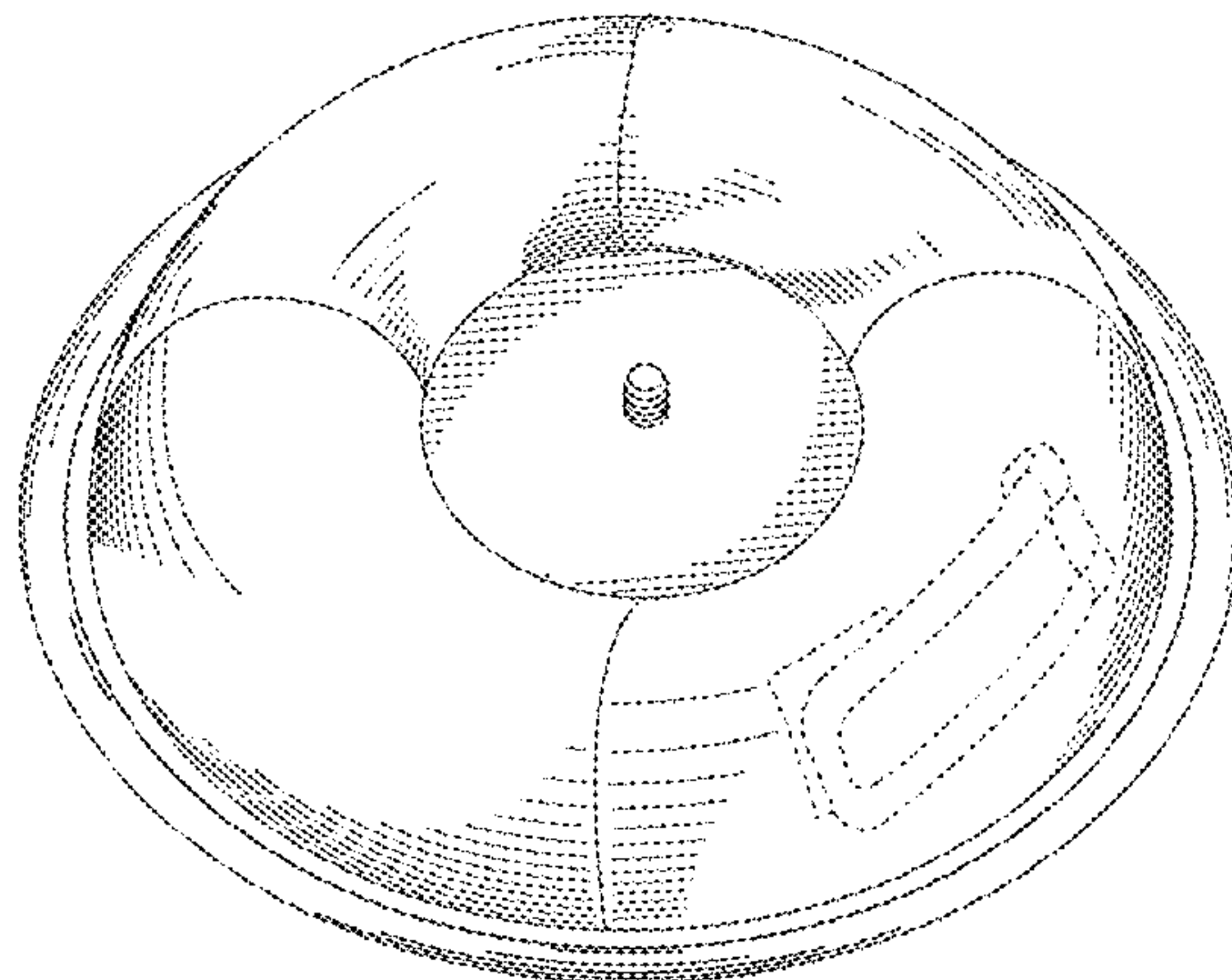
DESCRIPTION

FIG. 1 is a perspective view of the top, front and right sides of a toaster shown in a typical use configuration; FIG. 2 is a front view of the electronic device mount; FIG. 3 is a rear view of the electronic device mount; FIG. 4 is a left side view of the electronic device mount; FIG. 5 is right side view of the electronic device mount; and, FIG. 6 is a top view of the electronic device mount. The bottom of the electronic device mount is flat and devoid of surface ornamentation. The broken-lines in the above-described figures are for illustrative purposes only, and form no part of the claimed design.

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1 Claim, 4 Drawing Sheets



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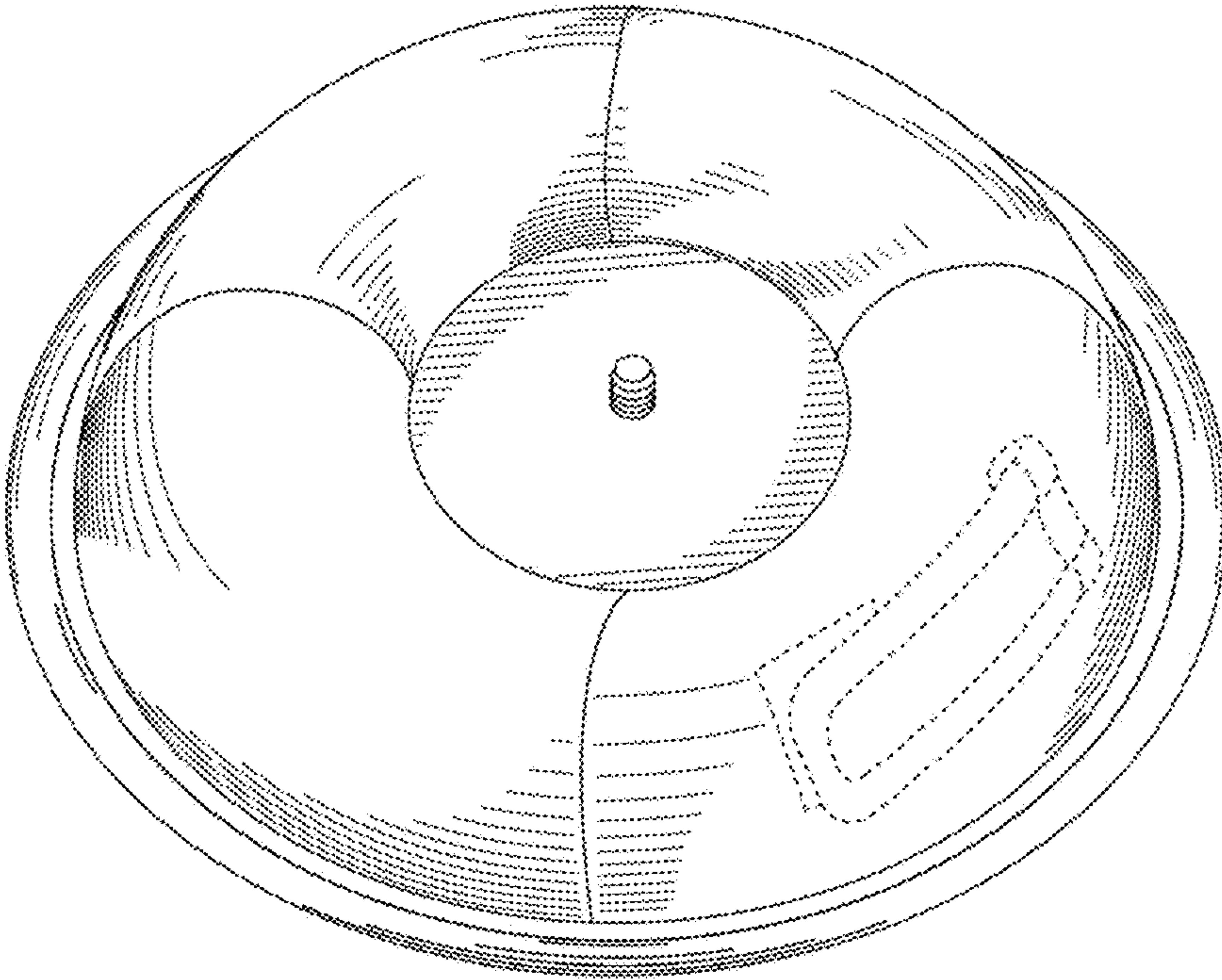


FIG. 1

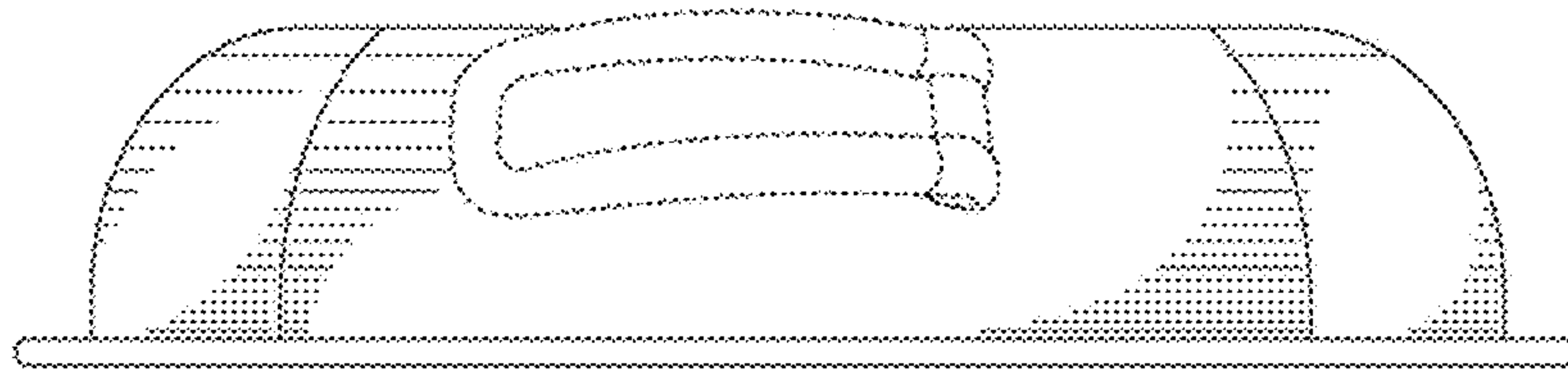


FIG. 2

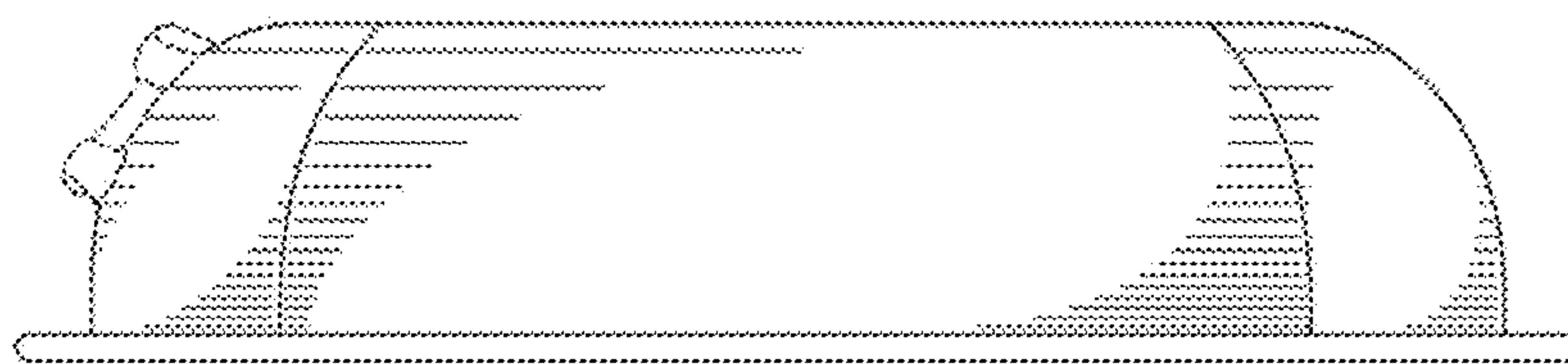


FIG. 3

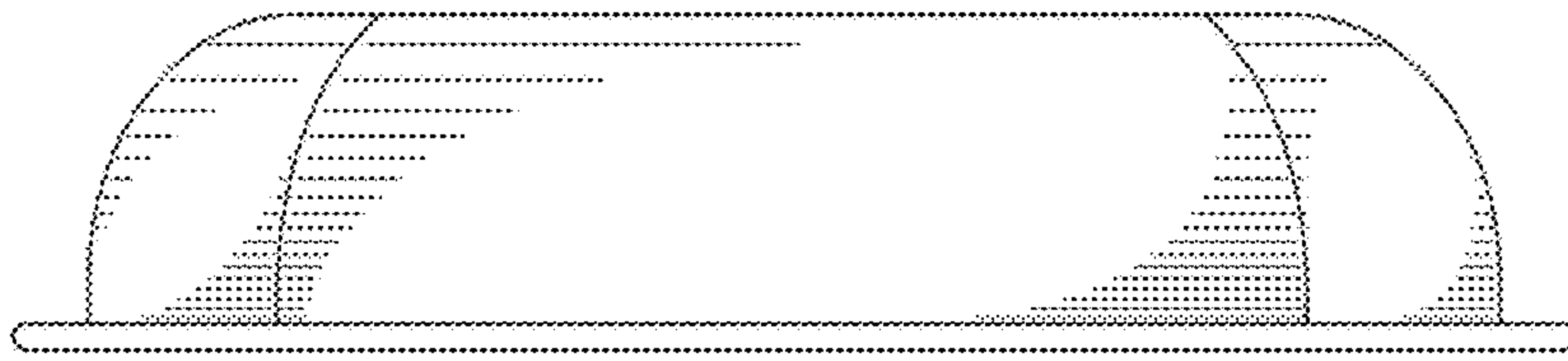


FIG. 4

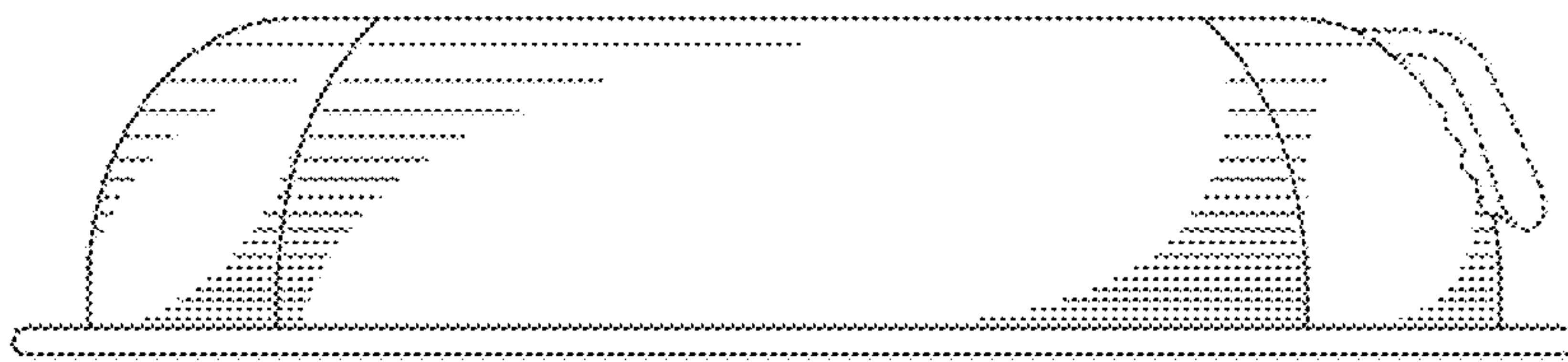


FIG. 5

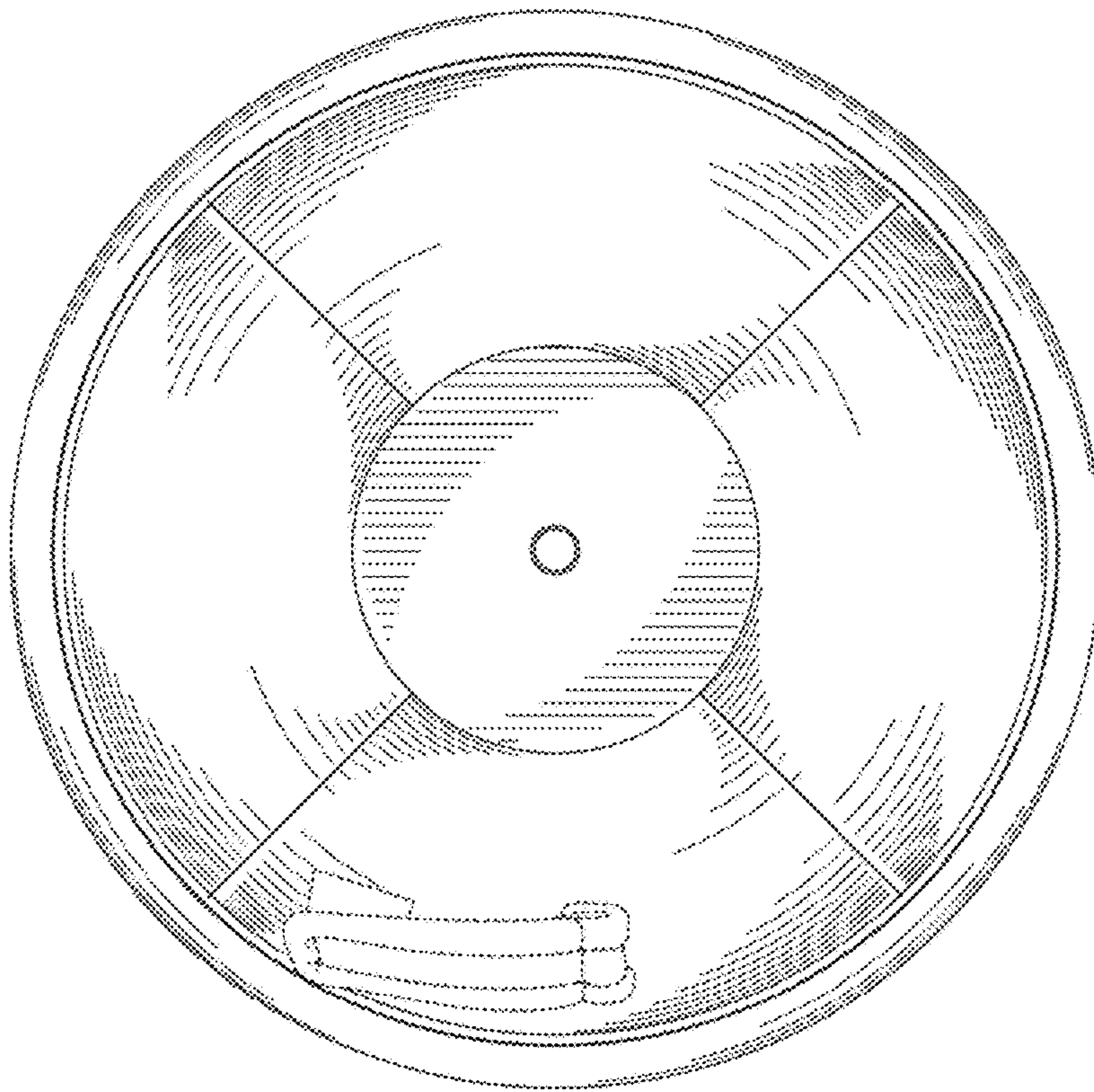


FIG. 6