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(12) **United States Design Patent** (10) **Patent No.:** **US D855,597 S**
Lee et al. (45) **Date of Patent:** **** Aug. 6, 2019**

(54) **ELECTRONIC DEVICE**

(56) **References Cited**

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(**) Term: **15 Years**

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(21) Appl. No.: **29/597,772**

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Related U.S. Application Data

(63) Continuation of application No. 29/539,073, filed on
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(30) **Foreign Application Priority Data**

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(51) **LOC (12) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/248**

(58) **Field of Classification Search**
USPC D14/138 R, 138 G, 248, 250, 341, 203.1,
D14/203.3, 203.7, 218; D21/329;
D10/50, 65, 104.1
CPC G06F 1/1626; G06F 1/1652; H04M 1/027;
H04M 1/0202; H04M 1/0279; H04M
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H04M 1/0266; H04M 1/0268; H04M
1/0295

See application file for complete search history.

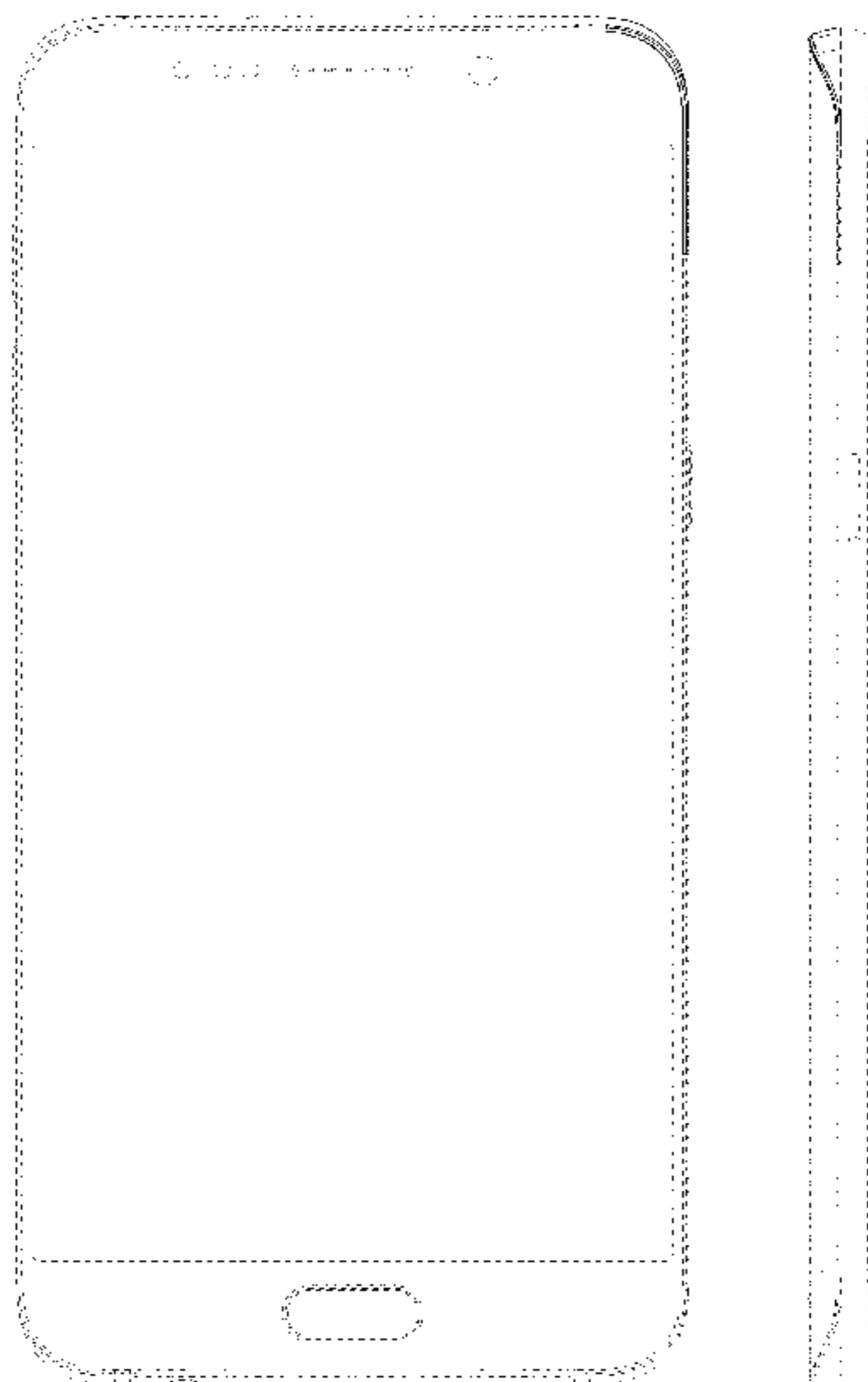
(57) **CLAIM**

The ornamental design for an electronic device, as shown
and described.

DESCRIPTION

FIG. 1 is a front perspective view of an electronic device
showing our new design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a left side elevation view thereof;
FIG. 5 is a right side elevation view thereof;
FIG. 6 is a top plan view thereof; and,
FIG. 7 is a bottom plan view thereof.
The dashed broken lines depict portions of the electronic
device that form no part of the claimed design.
The dot-dash broken lines represent a boundary line that
forms no part of the claimed design.

1 Claim, 5 Drawing Sheets



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FIG.1

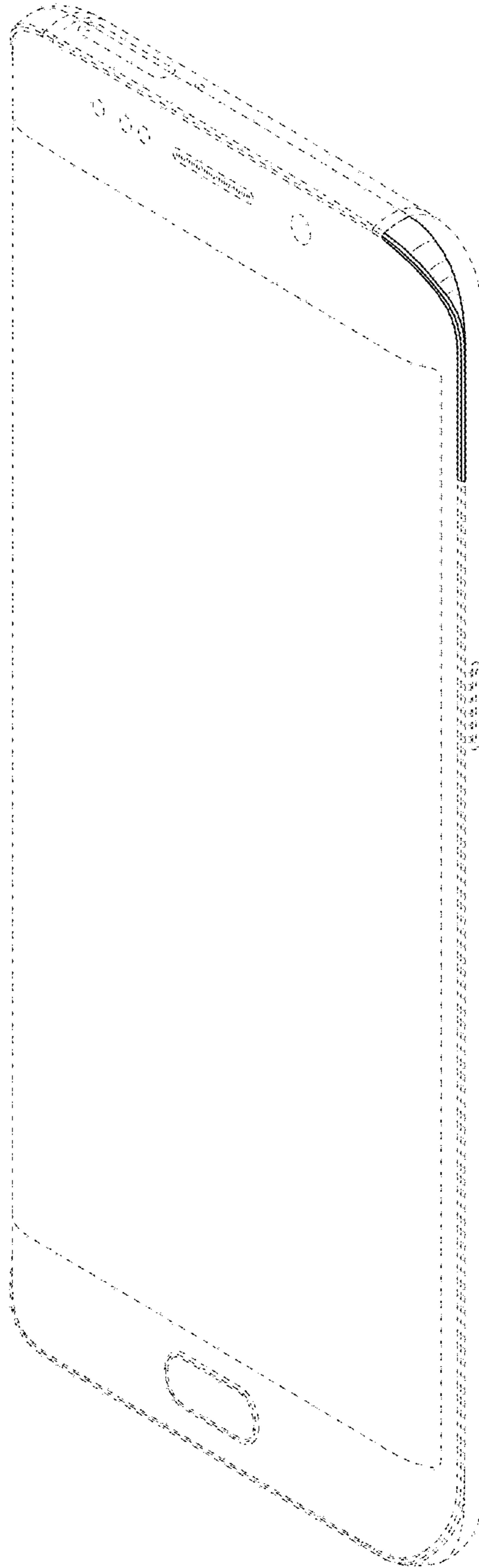


FIG.2



FIG.3

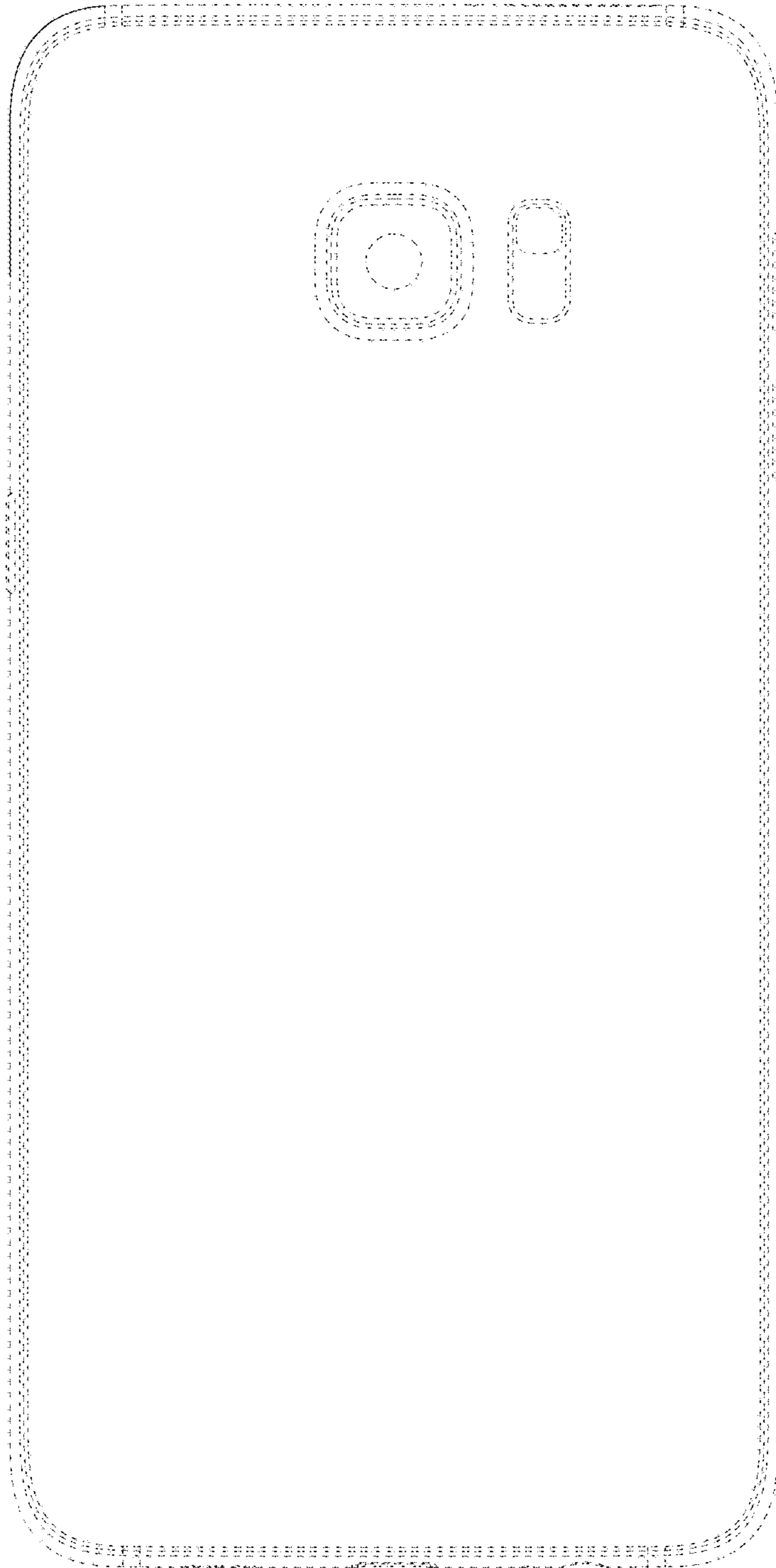


FIG.4

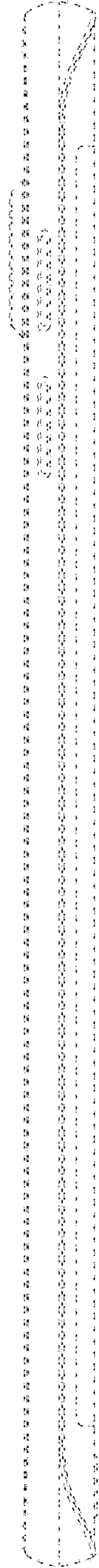


FIG.5

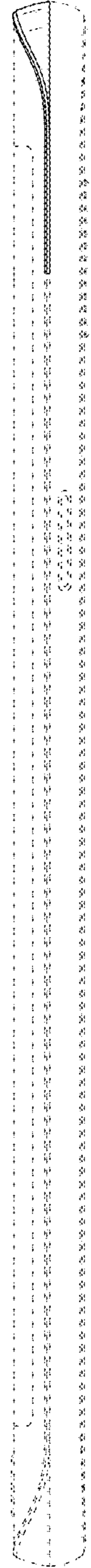


FIG.6

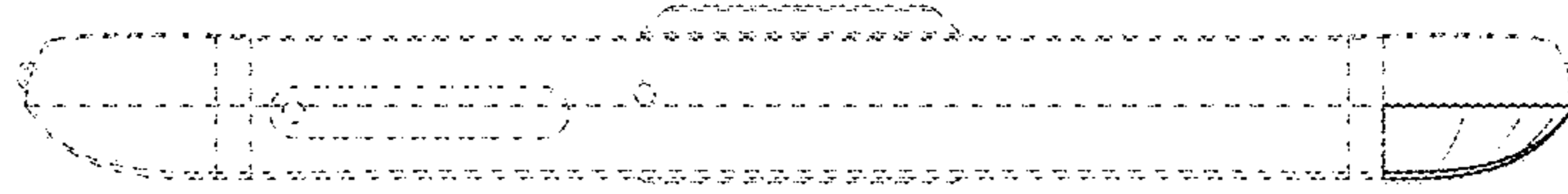


FIG.7

