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(12) **United States Design Patent** (10) **Patent No.:** **US D854,524 S**
Choi et al. (45) **Date of Patent:** **** Jul. 23, 2019**

(54) **ELECTRONIC DEVICE**

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(**) Term: **15 Years**

(Continued)

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(51) **LOC (11) Cl.** **14-03**

Assistant Examiner — Tracey J Bell

(52) **U.S. Cl.**
USPC **D14/248**

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(58) **Field of Classification Search**
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D14/138 C, 138 G, 138 R, 248, 315–318,
D14/341, 342, 345, 346, 203.1, 203.3,
D14/203.7, 250, 218, 371, 496, 388, 389;
D21/329; D10/65, 104.1, 50; D20/43
CPC .. H04M 1/027; H04M 1/0202; H04M 1/0279;
H04M 1/0235; H04M 1/0237; H04M
1/0239; H04M 1/0266; H04M 1/0268;
H04M 1/0295; G06K 9/00006; G06F
1/1652; G06F 1/1626

See application file for complete search history.

(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of an electronic device showing our new design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a left side elevation view thereof;
FIG. 5 is a right side elevation view thereof;
FIG. 6 is a top plan view thereof; and,
FIG. 7 is a bottom plan view thereof.
The evenly-spaced dashed broken lines in the figures depict portions of the electronic device that form no part of the claimed design. The unevenly-spaced dot-dash broken lines in the figures define boundaries of the claimed design and form no part of the claimed design.

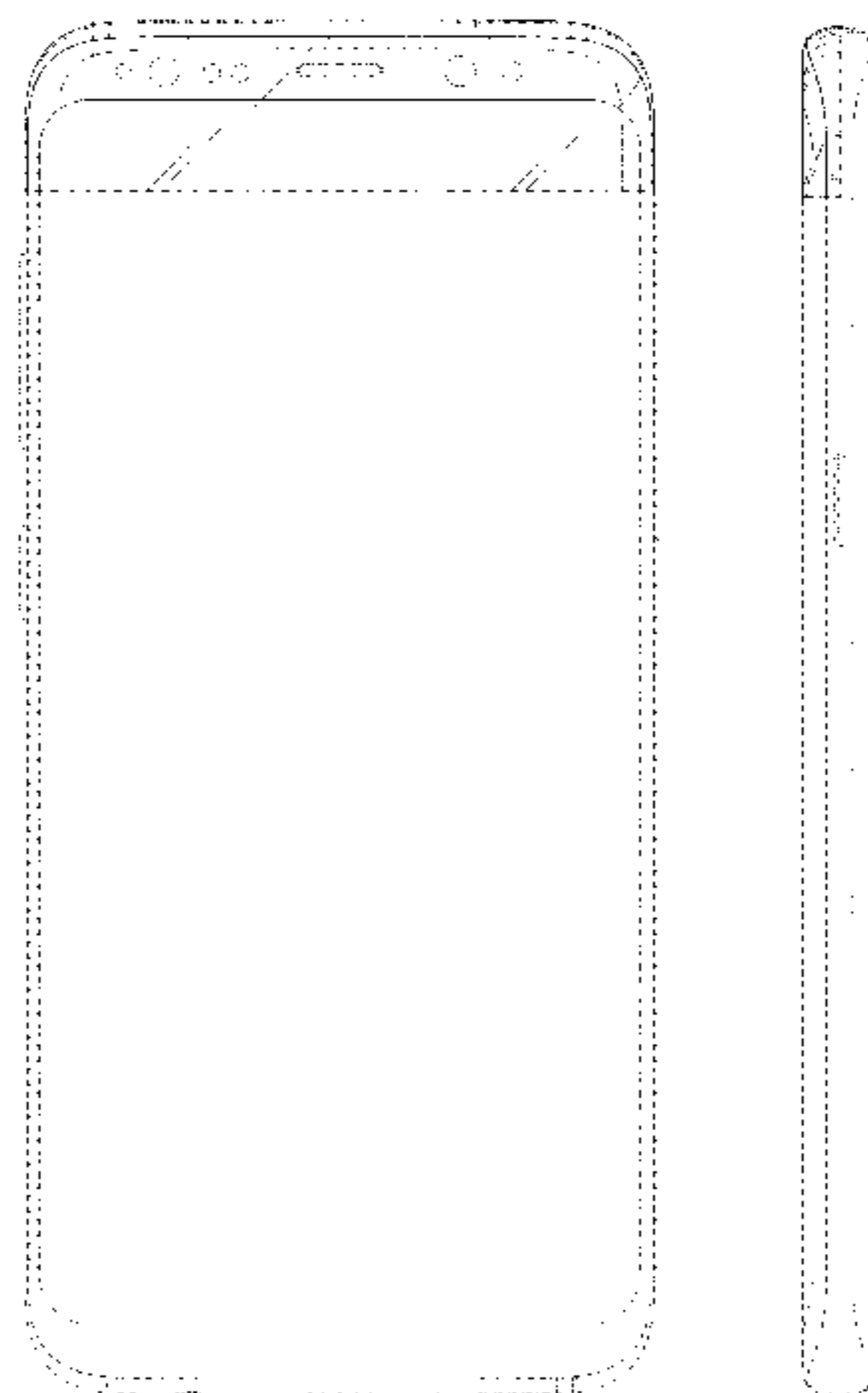
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1 Claim, 7 Drawing Sheets



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FIG. 1

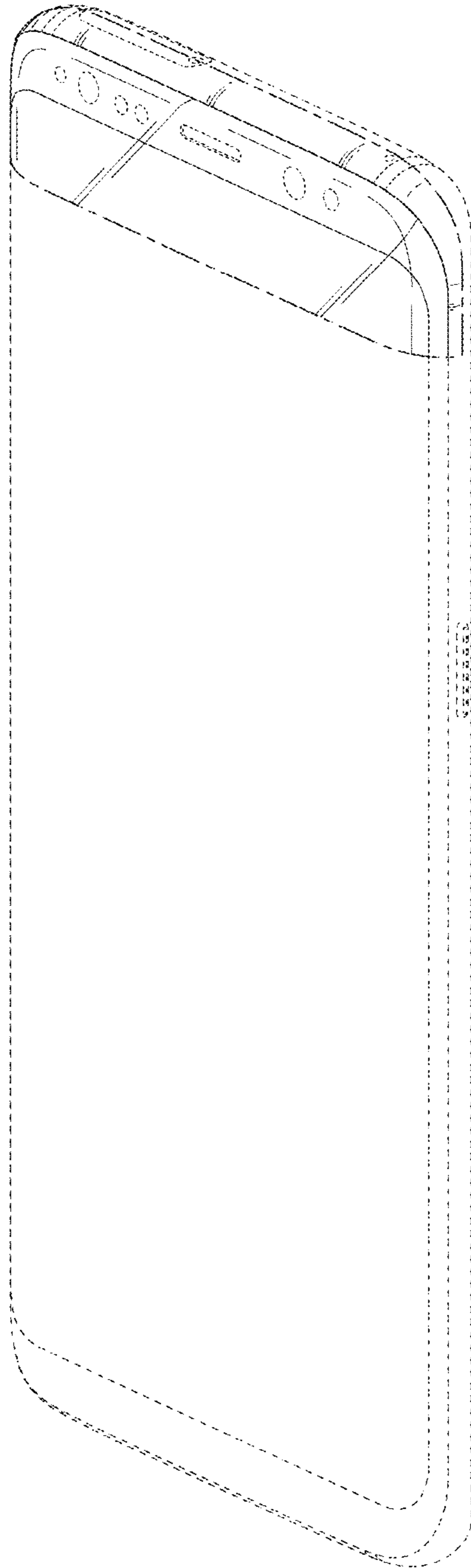


FIG. 2



FIG. 3

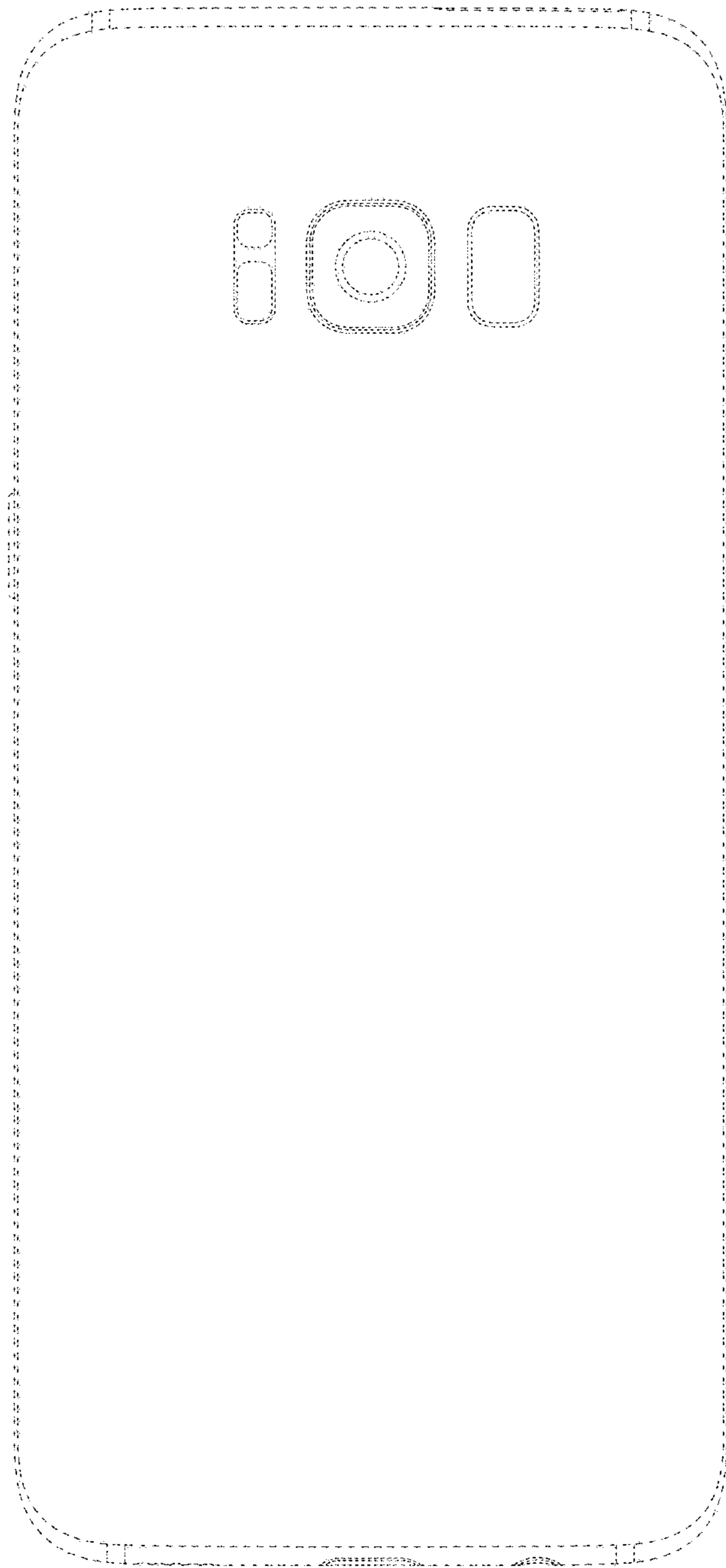


FIG. 4

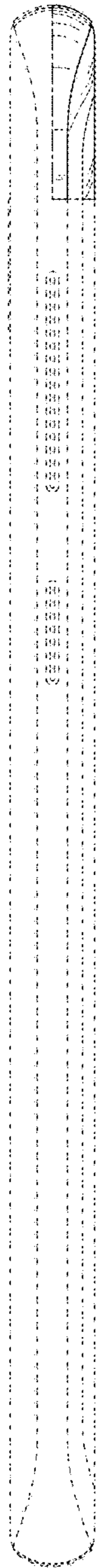


FIG. 5

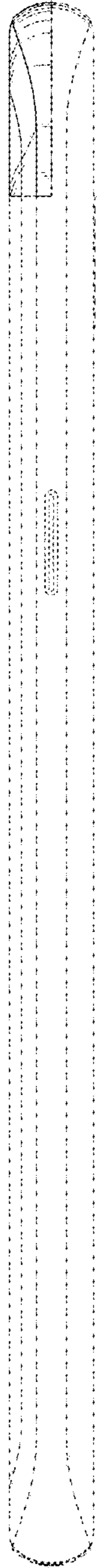


FIG. 6

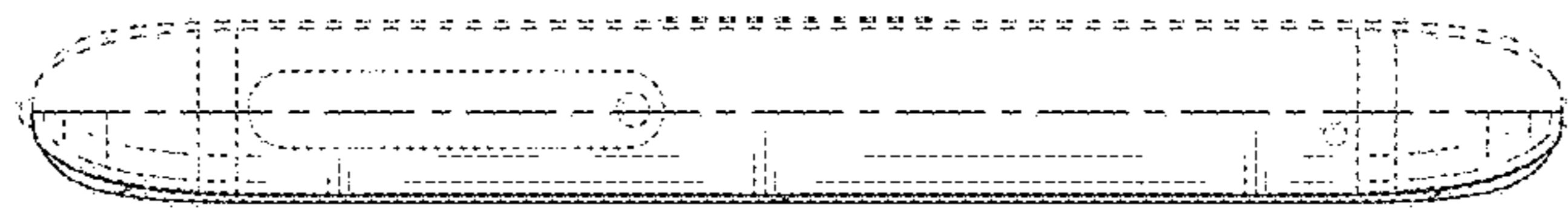


FIG. 7

