



US00D854195S

(12) **United States Design Patent** (10) **Patent No.:** **US D854,195 S**
Lee et al. (45) **Date of Patent:** **** Jul. 16, 2019**

(54) **LIGHT EMITTING DIODE PACKAGE**

(71) Applicant: **Genesis Photonics Inc.**, Tainan (TW)

(72) Inventors: **Hao-Chung Lee**, Taoyuan (TW);
Xun-Xain Zhan, Tainan (TW);
Yu-Feng Lin, Tainan (TW)

(73) Assignee: **Genesis Photonics Inc.**, Tainan (TW)

(**) Term: **15 Years**

(21) Appl. No.: **29/598,780**

(22) Filed: **Mar. 29, 2017**

(30) **Foreign Application Priority Data**

Sep. 29, 2016 (TW) 105305755

(51) **LOC (11) Cl.** **26-04**

(52) **U.S. Cl.**
USPC **D26/1**

(58) **Field of Classification Search**
USPC D26/1-4; D13/180

(Continued)

(56) **References Cited**

U.S. PATENT DOCUMENTS

D522,466 S 6/2006 Sumitani
D570,506 S 6/2008 Uemoto et al.

(Continued)

FOREIGN PATENT DOCUMENTS

JP D1300883 5/2007
TW D109667 3/2006

(Continued)

OTHER PUBLICATIONS

“Notice of Allowance of Related U.S. Appl. No. 29/598,773”, dated
Mar. 27, 2018, pp. 1-17.

(Continued)

Primary Examiner — Marcus A Jackson

(74) *Attorney, Agent, or Firm* — JCIPRNET

(57) **CLAIM**

The ornamental design for a light emitting diode package, as
shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a first embodiment of the
light emitting diode package showing our new design;

FIG. 2 is a front view of the light emitting diode package of
FIG. 1;

FIG. 3 is a rear view of the light emitting diode package of
FIG. 1;

FIG. 4 is a left view of the light emitting diode package of
FIG. 1;

FIG. 5 is a right view of the light emitting diode package of
FIG. 1;

FIG. 6 is a top view of the light emitting diode package of
FIG. 1;

FIG. 7 is a bottom view of the light emitting diode package
of FIG. 1;

FIG. 8 is a perspective view of a second embodiment of the
light emitting diode package showing our new design;

FIG. 9 is a front view of the light emitting diode package of
FIG. 8;

FIG. 10 is a rear view of the light emitting diode package of
FIG. 8;

FIG. 11 is a left view of the light emitting diode package of
FIG. 8;

FIG. 12 is a right view of the light emitting diode package
of FIG. 8;

FIG. 13 is a top view of the light emitting diode package of
FIG. 8;

FIG. 14 is a bottom view of the light emitting diode package
of FIG. 8;

FIG. 15 is a perspective view of a third embodiment of the
light emitting diode package showing our new design;

FIG. 16 is a front view of the light emitting diode package
of FIG. 15;

FIG. 17 is a rear view of the light emitting diode package of
FIG. 15;

(Continued)

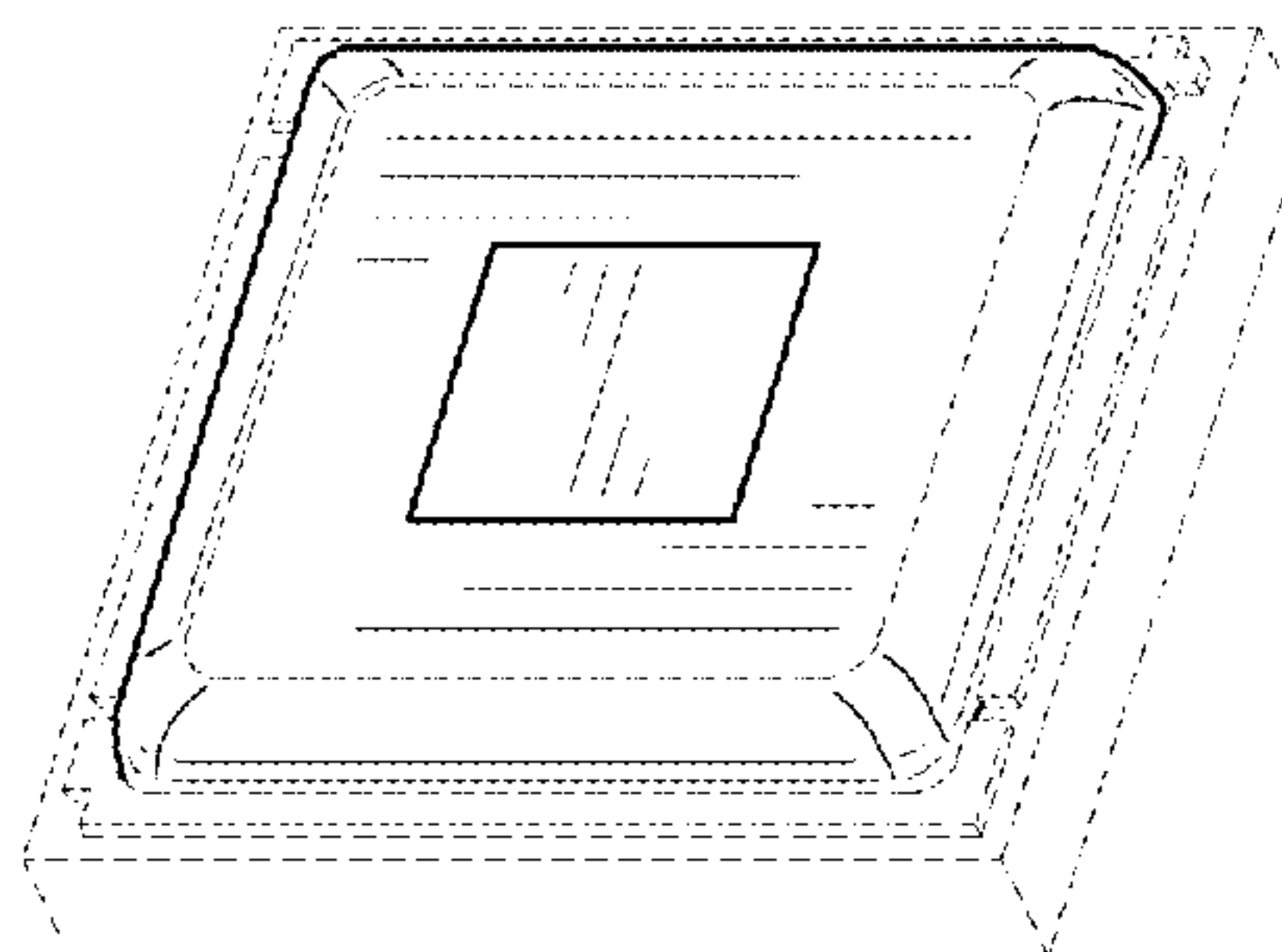


FIG. 18 is a left view of the light emitting diode package of FIG. 15;
 FIG. 19 is a right view of the light emitting diode package of FIG. 15;
 FIG. 20 is a top view of the light emitting diode package of FIG. 15;
 FIG. 21 is a bottom view of the light emitting diode package of FIG. 15;
 FIG. 22 is a perspective view of a fourth embodiment of the light emitting diode package showing our new design;
 FIG. 23 is a front view of the light emitting diode package of FIG. 22;
 FIG. 24 is a rear view of the light emitting diode package of FIG. 22;
 FIG. 25 is a left view of the light emitting diode package of FIG. 22;
 FIG. 26 is a right view of the light emitting diode package of FIG. 22;
 FIG. 27 is a top view of the light emitting diode package of FIG. 22;
 FIG. 28 is a bottom view of the light emitting diode package of FIG. 22;
 FIG. 29 is a perspective view of a fifth embodiment of the light emitting diode package showing our new design;
 FIG. 30 is a front view of the light emitting diode package of FIG. 29;
 FIG. 31 is a rear view of the light emitting diode package of FIG. 29;
 FIG. 32 is a left view of the light emitting diode package of FIG. 29;
 FIG. 33 is a right view of the light emitting diode package of FIG. 29;
 FIG. 34 is a top view of the light emitting diode package of FIG. 29; and,
 FIG. 35 is a bottom view of the light emitting diode package of FIG. 29.

The broken lines in the drawings are included for the purpose of illustration and form no part of the claimed design.

1 Claim, 20 Drawing Sheets

(58) Field of Classification Search

CPC H01R 5/00; H01R 13/46; H01R 13/514;
 H01R 31/048; H01R 31/02; H01J 5/00;
 H01J 5/16; H01J 1/02; H01J 15/00; H01J
 5/48; H01J 5/50; H01J 19/54; F21V 5/00;
 F21V 13/00; F21V 29/004
 See application file for complete search history.

(56) References Cited

U.S. PATENT DOCUMENTS

D575,245	S	8/2008	Yen
D611,436	S	3/2010	Ho
D625,694	S	10/2010	Morikawa et al.
D634,863	S *	3/2011	Leung D26/1
D636,356	S	4/2011	Yamada

D636,900	S	4/2011	Chou et al.
D646,645	S	10/2011	Chen et al.
D646,646	S	10/2011	Chen et al.
D646,647	S	10/2011	Chen et al.
D647,491	S	10/2011	Chen et al.
D647,492	S	10/2011	Imai et al.
D665,519	S	8/2012	Oyaizu
D667,145	S *	9/2012	Bierhuizen D26/1
D671,674	S	11/2012	Cho
8,531,126	B2	9/2013	Kaihotsu
8,581,284	B2	11/2013	Seko
D714,744	S	10/2014	Hsing Chen et al.
8,878,221	B2	11/2014	Lee et al.
8,901,596	B2	12/2014	Huang
D733,670	S	7/2015	Hsing Chen et al.
D749,051	S	2/2016	Clark et al.
D761,213	S	7/2016	Lee et al.
D761,214	S	7/2016	Lee et al.
D762,596	S	8/2016	Lee et al.
D772,181	S	11/2016	Lee et al.
9,515,055	B2	12/2016	Britt et al.
D790,487	S	6/2017	Lee et al.
D796,456	S	9/2017	Lee et al.
9,953,956	B2	4/2018	Lee et al.
D825,499	S	8/2018	Liu et al.
2006/0049422	A1	3/2006	Shoji
2006/0131600	A1	6/2006	Nakaoka et al.
2009/0014739	A1	1/2009	Chiang et al.
2011/0186874	A1	8/2011	Shum
2012/0007076	A1	1/2012	Cho
2014/0239320	A1	8/2014	Miyoshi et al.
2016/0284666	A1	9/2016	Lee et al.
2018/0190881	A1	7/2018	Lin

FOREIGN PATENT DOCUMENTS

TW	D125767	11/2008
TW	D136583	8/2010
TW	D141207	6/2011
TW	D141209	6/2011

OTHER PUBLICATIONS

“Notice of Allowance of Related U.S. Appl. No. 29/638,942”, dated Nov. 7, 2018, pp. 1-23.

“Notice of Allowance of Taiwan Related Application, application No. 105305761”, dated Sep. 20, 2017, p. 1-p. 5.

“Notice of Allowance of Taiwan Counterpart Application,” dated Sep. 20, 2017, p. 1-p. 5.

Hao-Chung Lee et al., “Light Emitting Diode Module”, Unpublished U.S. Appl. No. 29/522,731, filed Apr. 2, 2015.

“Notice of Allowance of US Related Application, U.S. Appl. No. 29/522,731”, dated Mar. 9, 2016, p. 1-p. 11.

“Notice of Allowance of Taiwan Related Application, application No. 104301753”, dated Nov. 18, 2015, p. 1-p. 3.

Hao-Chung Lee et al., “Light Emitting Diode Package”, Unpublished U.S. Appl. No. 29/522,733, filed Apr. 2, 2015.

“Notice of Allowance of Taiwan Related Application, application No. 10401752”, dated Nov. 18, 2015, p. 1-p. 3.

“Notice of Allowance of US Related Application, U.S. Appl. No. 29/522,733”, dated Mar. 2, 2016, p. 1-p. 11.

Chuan-Yu Liu et al., “Light Emitting Diode Package”, Unpublished U.S. Appl. No. 29/598,778, filed Mar. 28, 2017.

Hao-Chung Lee et al., “Light Emitting Diode Module”, Unpublished U.S. Appl. No. 29/598,781, filed Mar. 29, 2017.

* cited by examiner

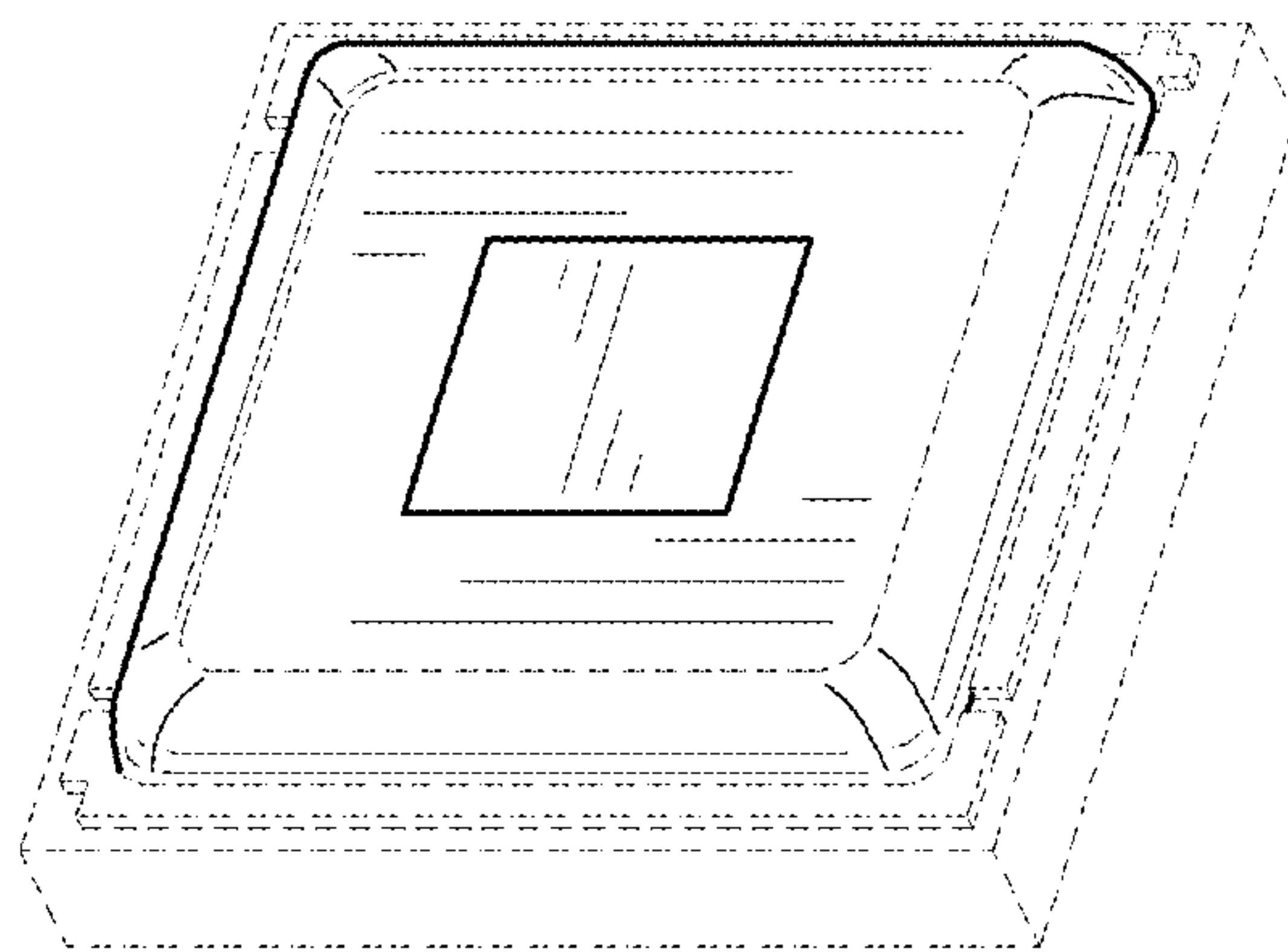


FIG. 1

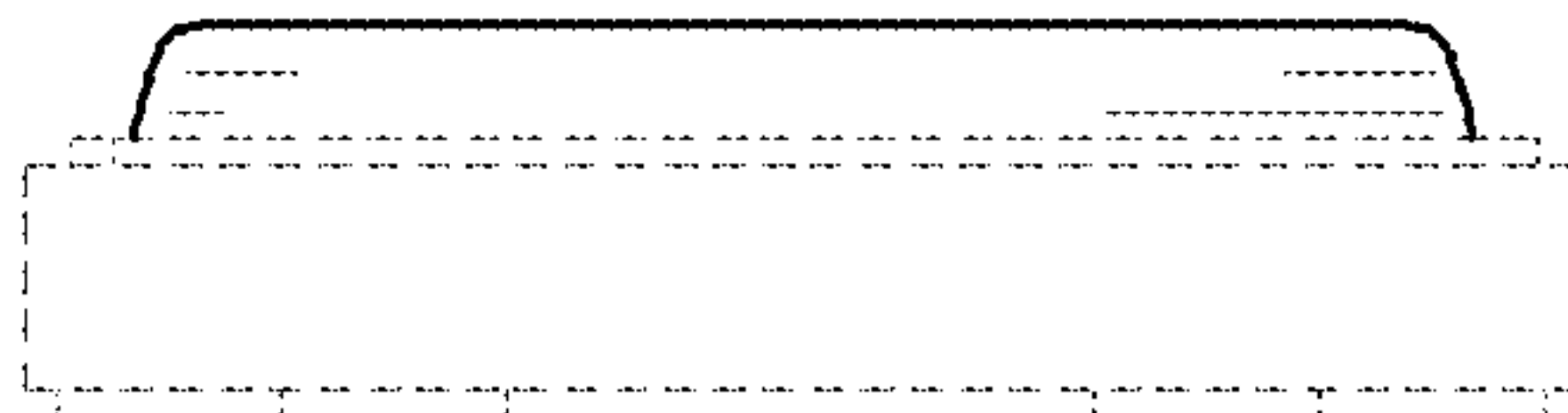


FIG. 2

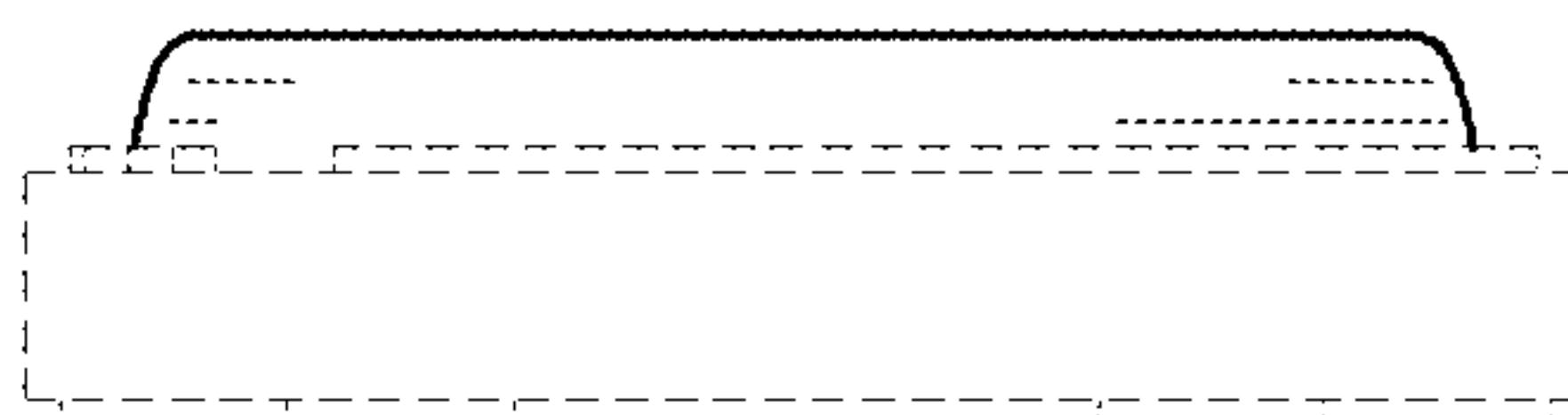


FIG. 3

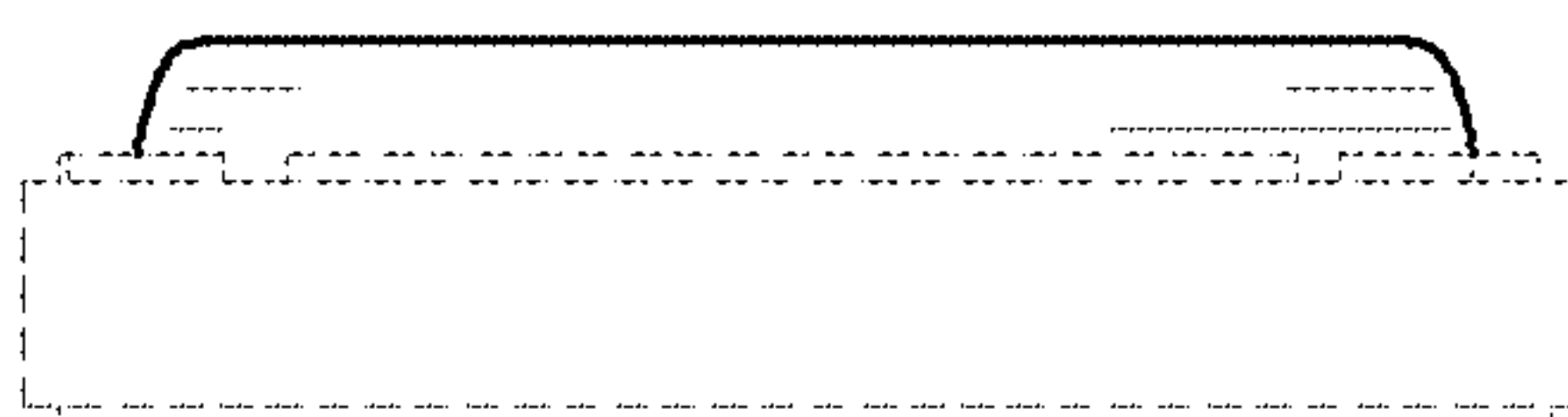


FIG. 4

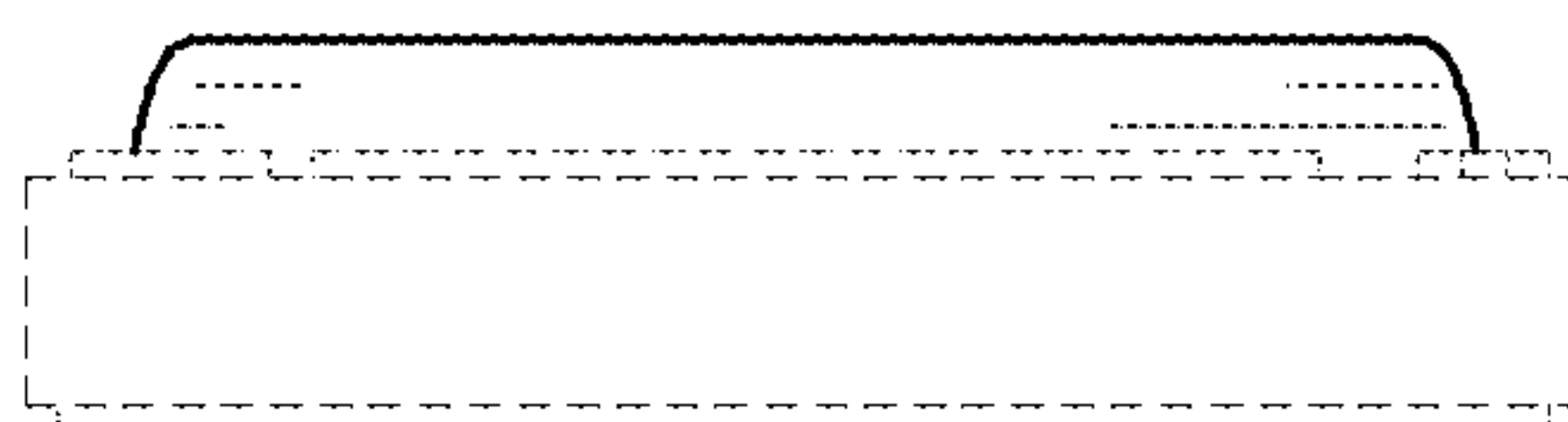


FIG. 5

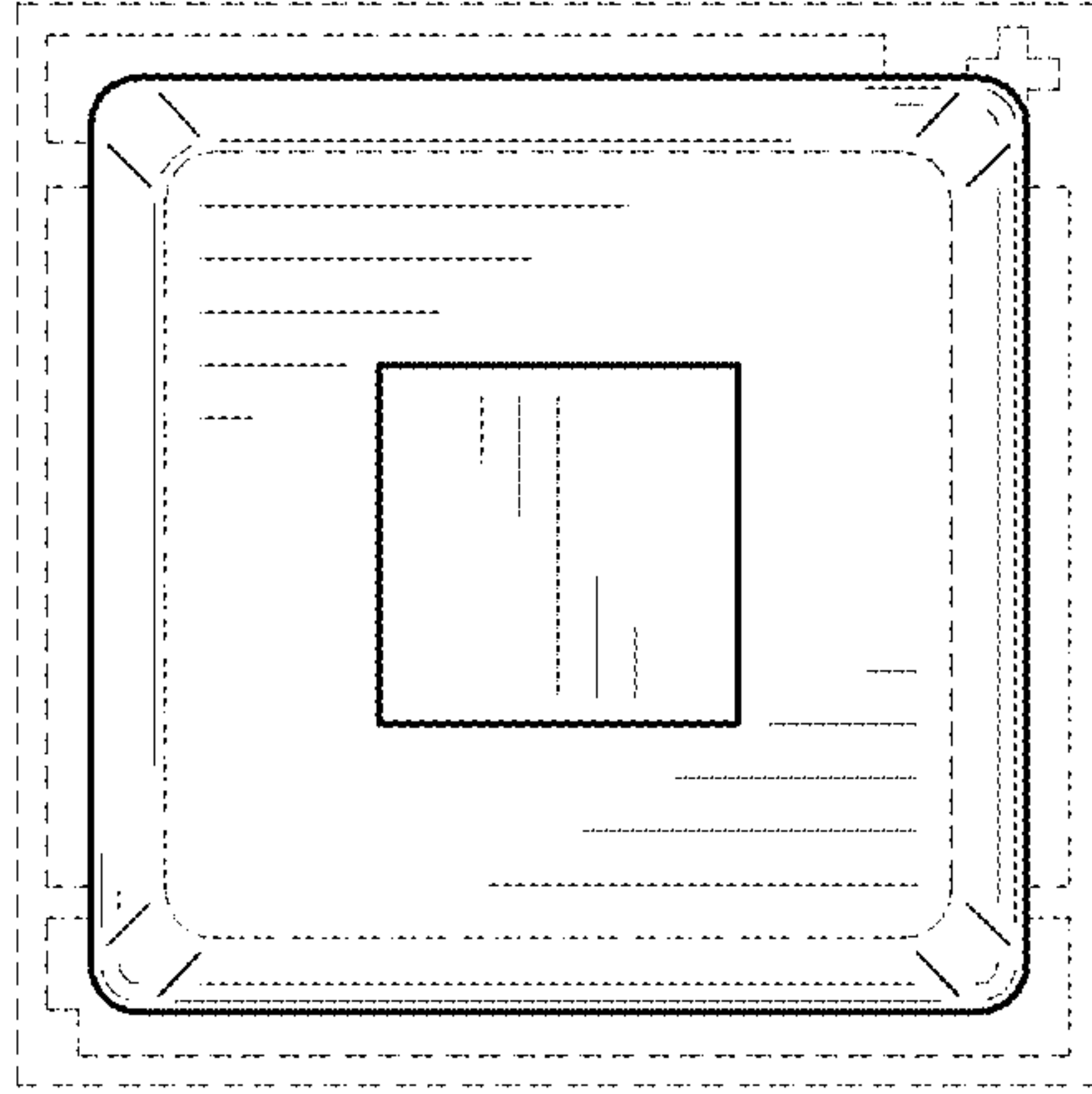


FIG. 6

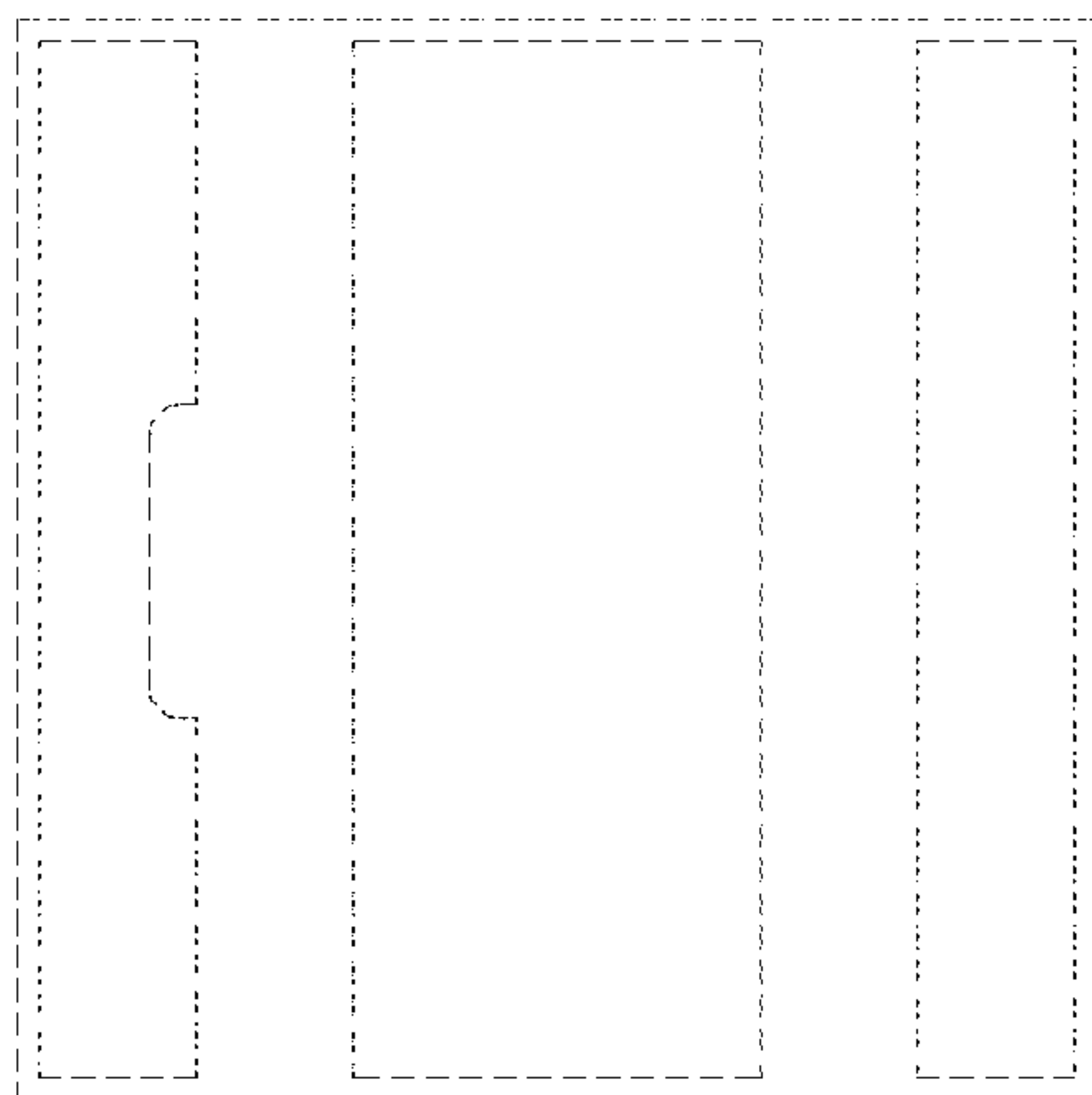


FIG. 7

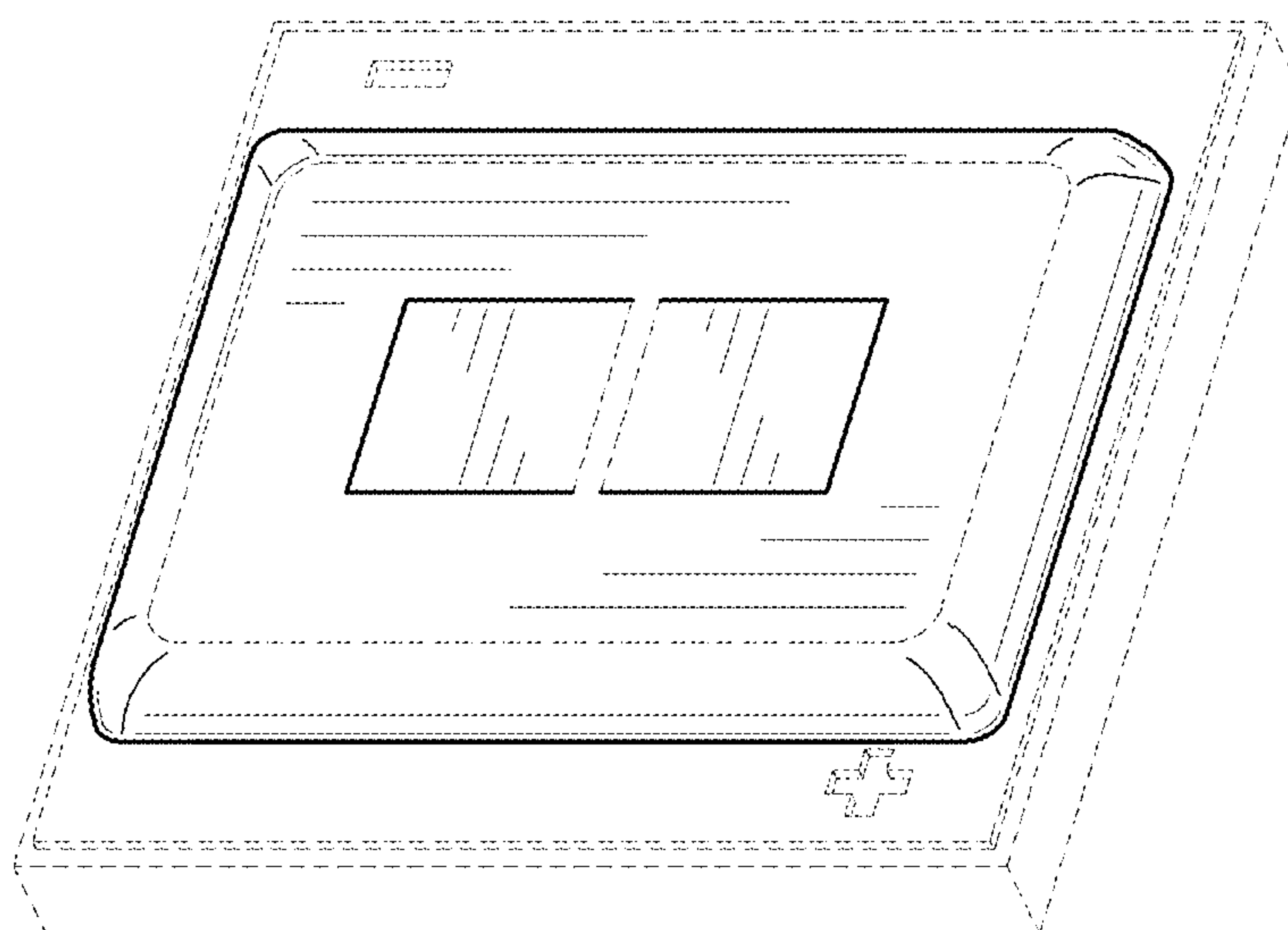


FIG. 8

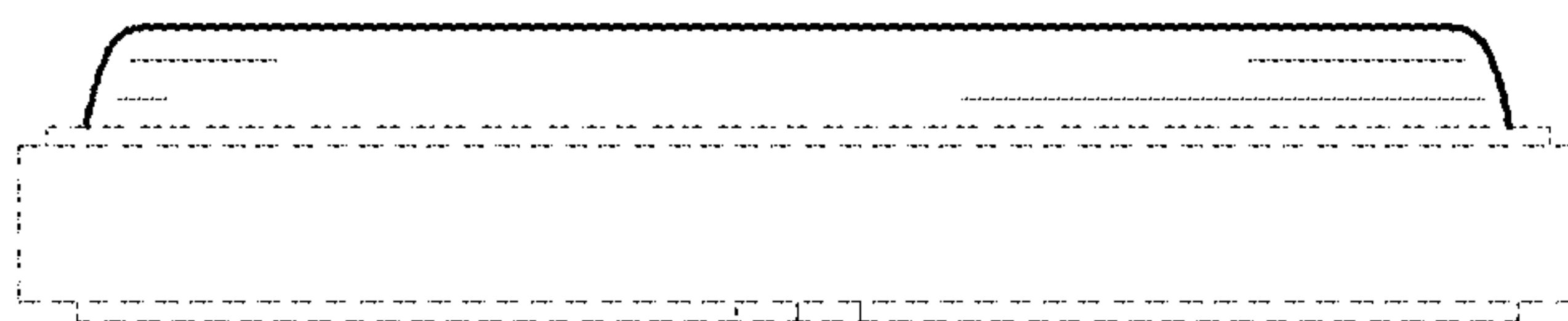


FIG. 9

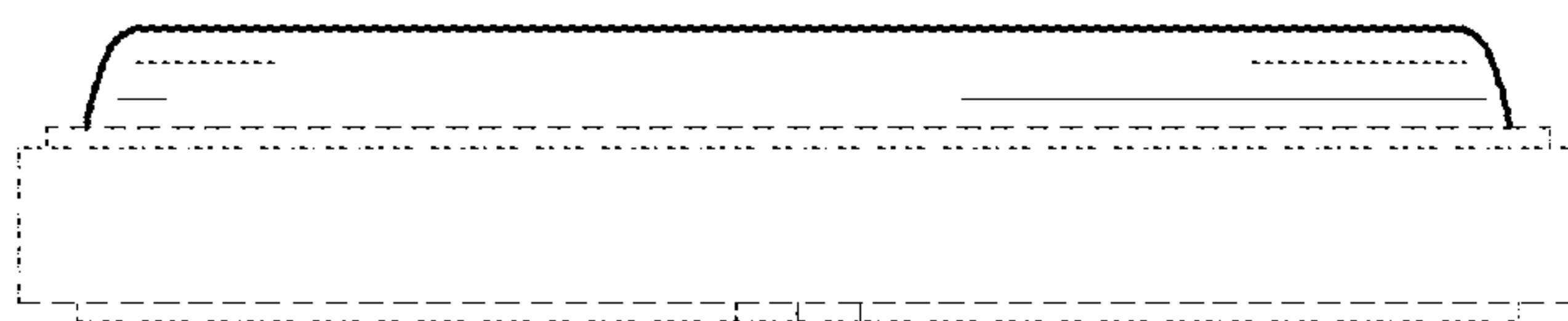


FIG. 10

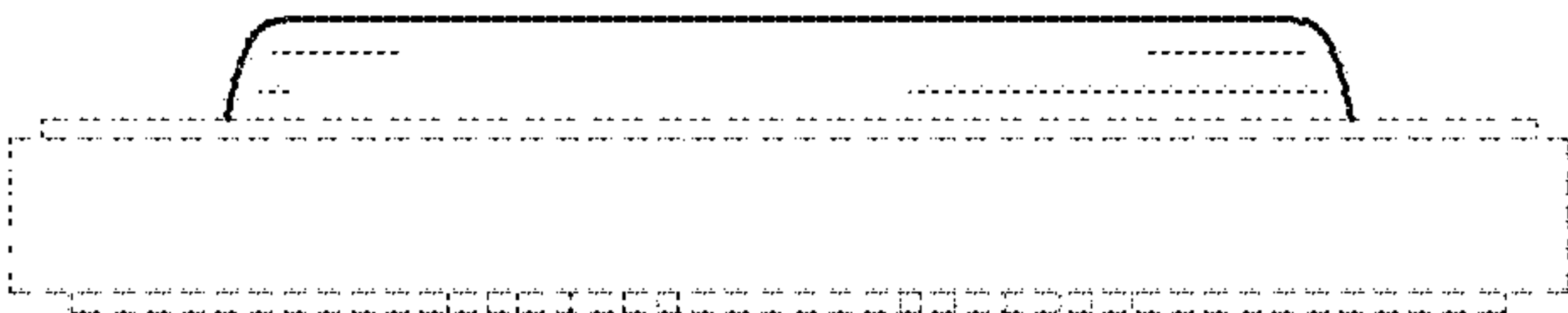


FIG. 11

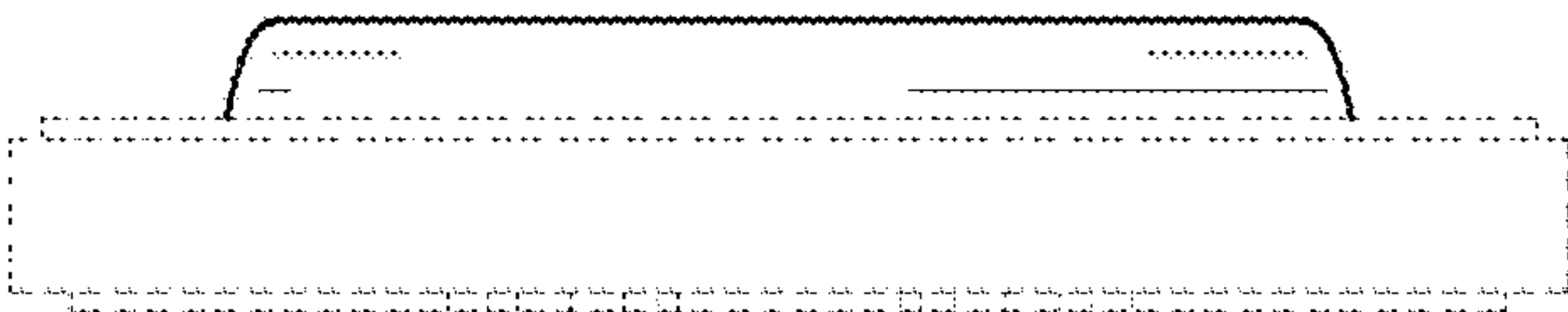


FIG. 12

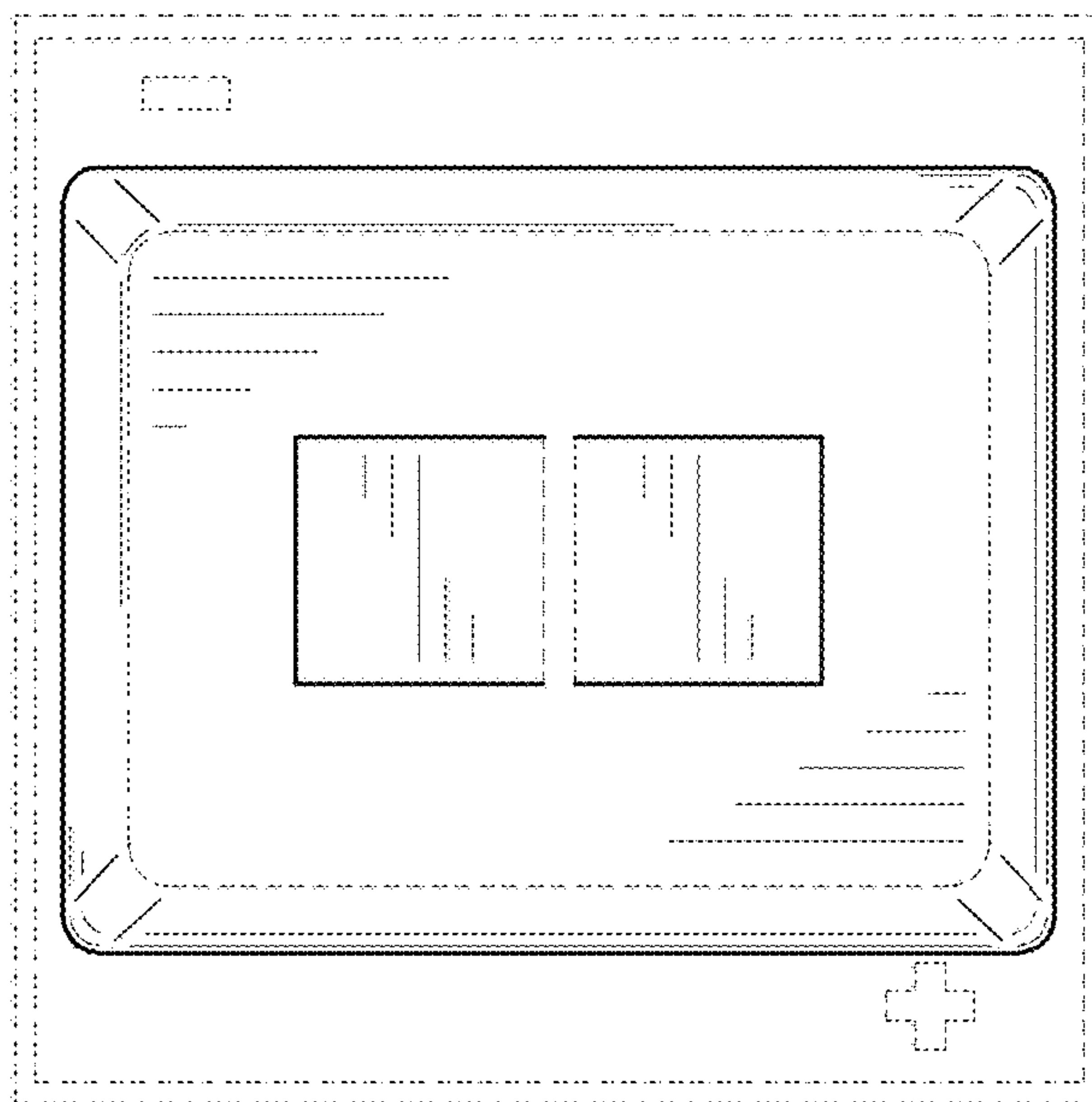


FIG. 13

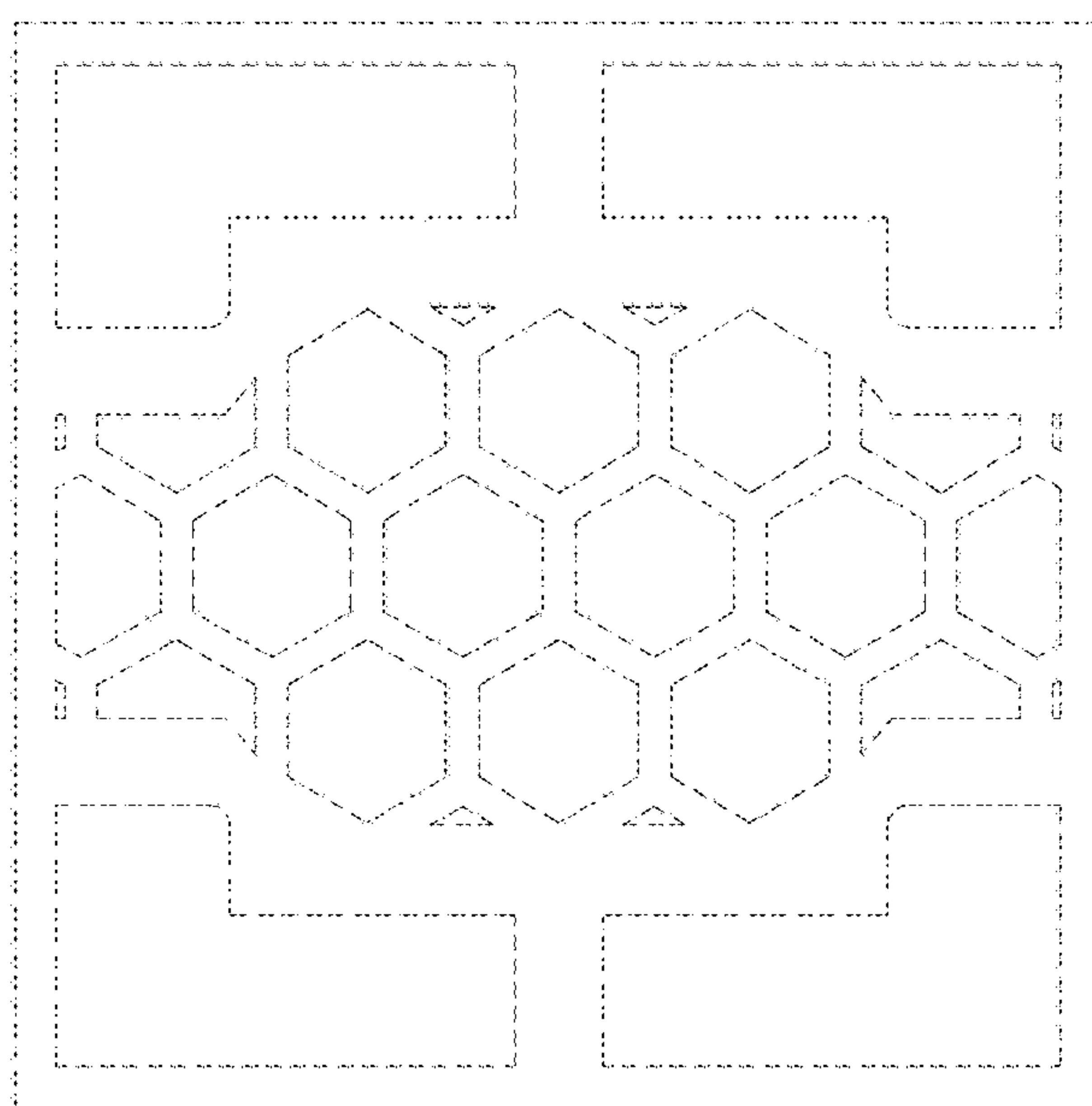


FIG. 14

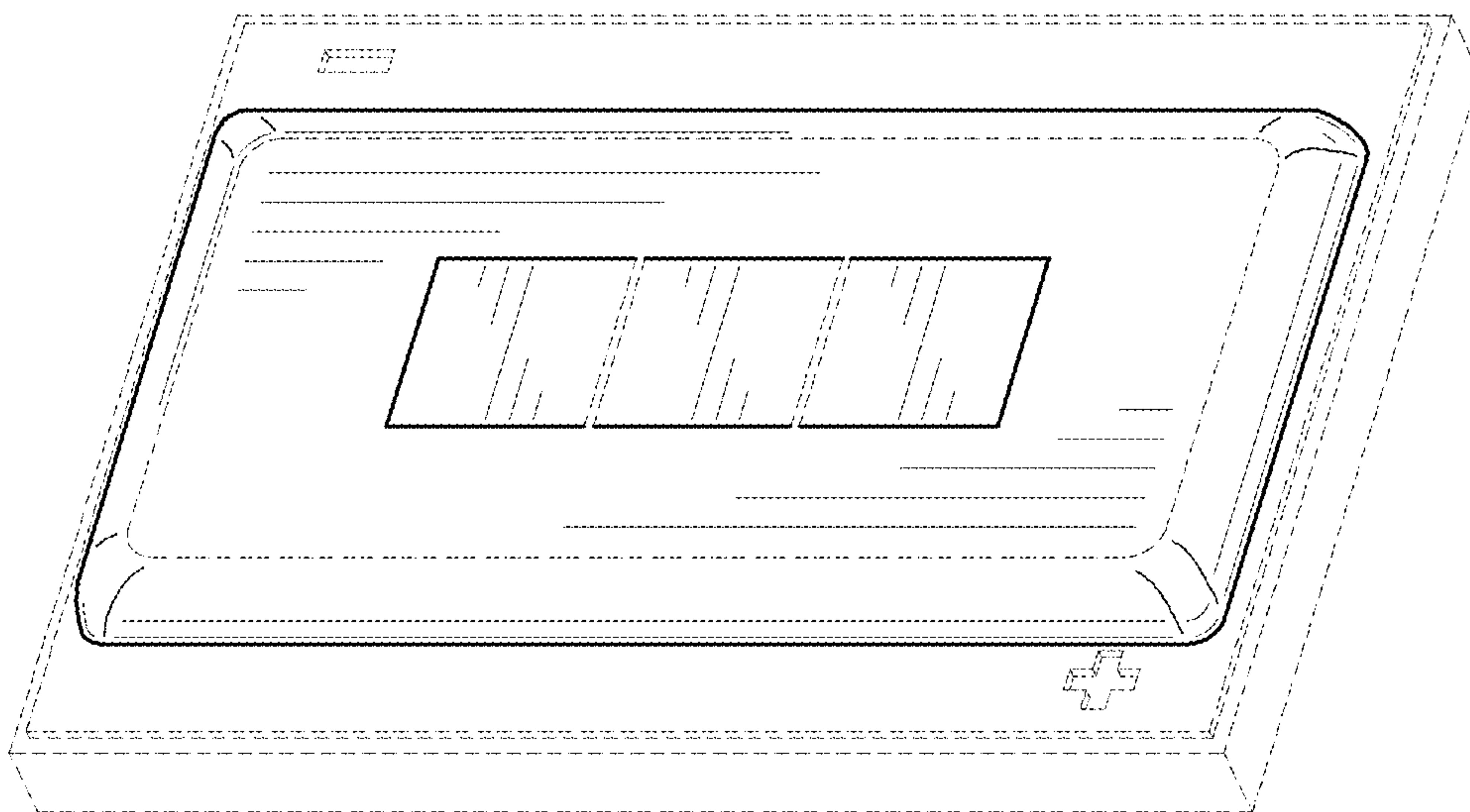


FIG. 15

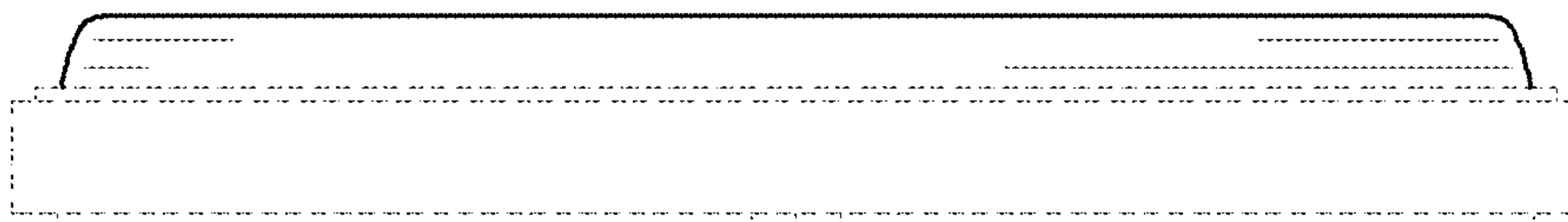


FIG. 16

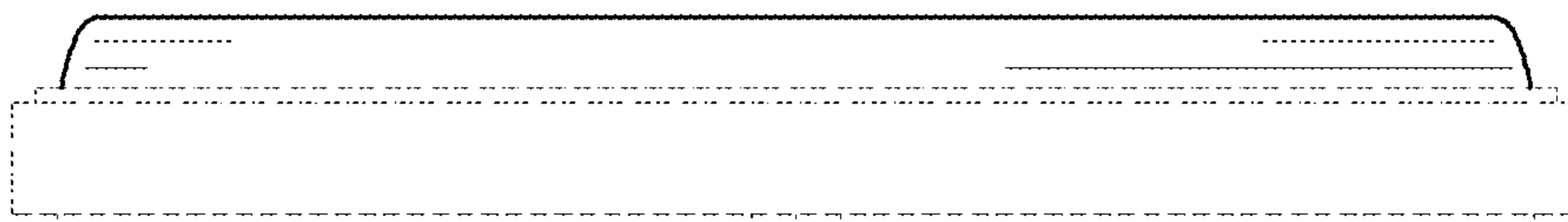


FIG. 17

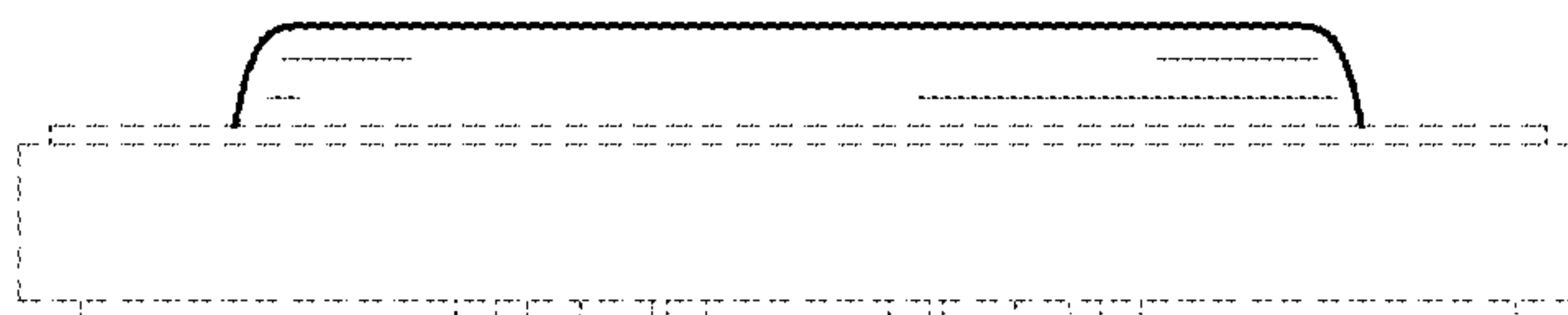


FIG. 18

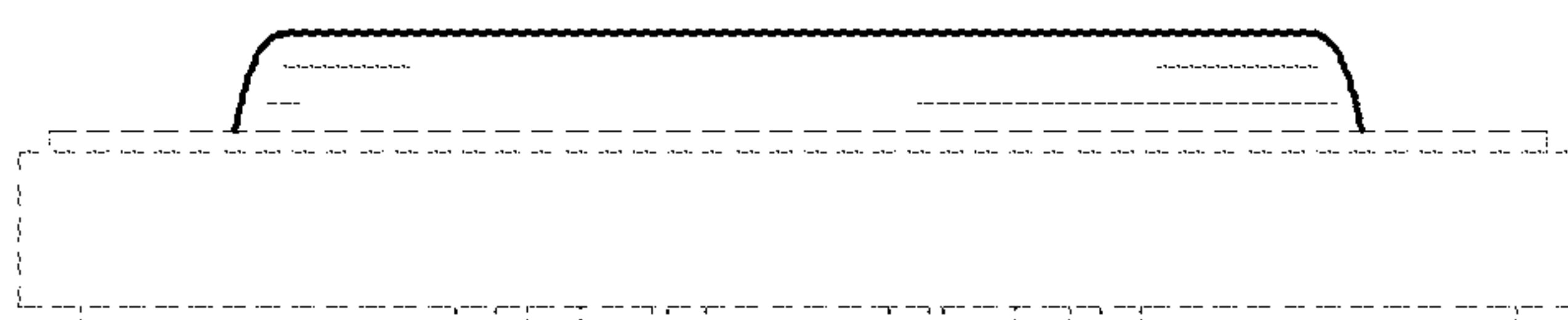


FIG. 19

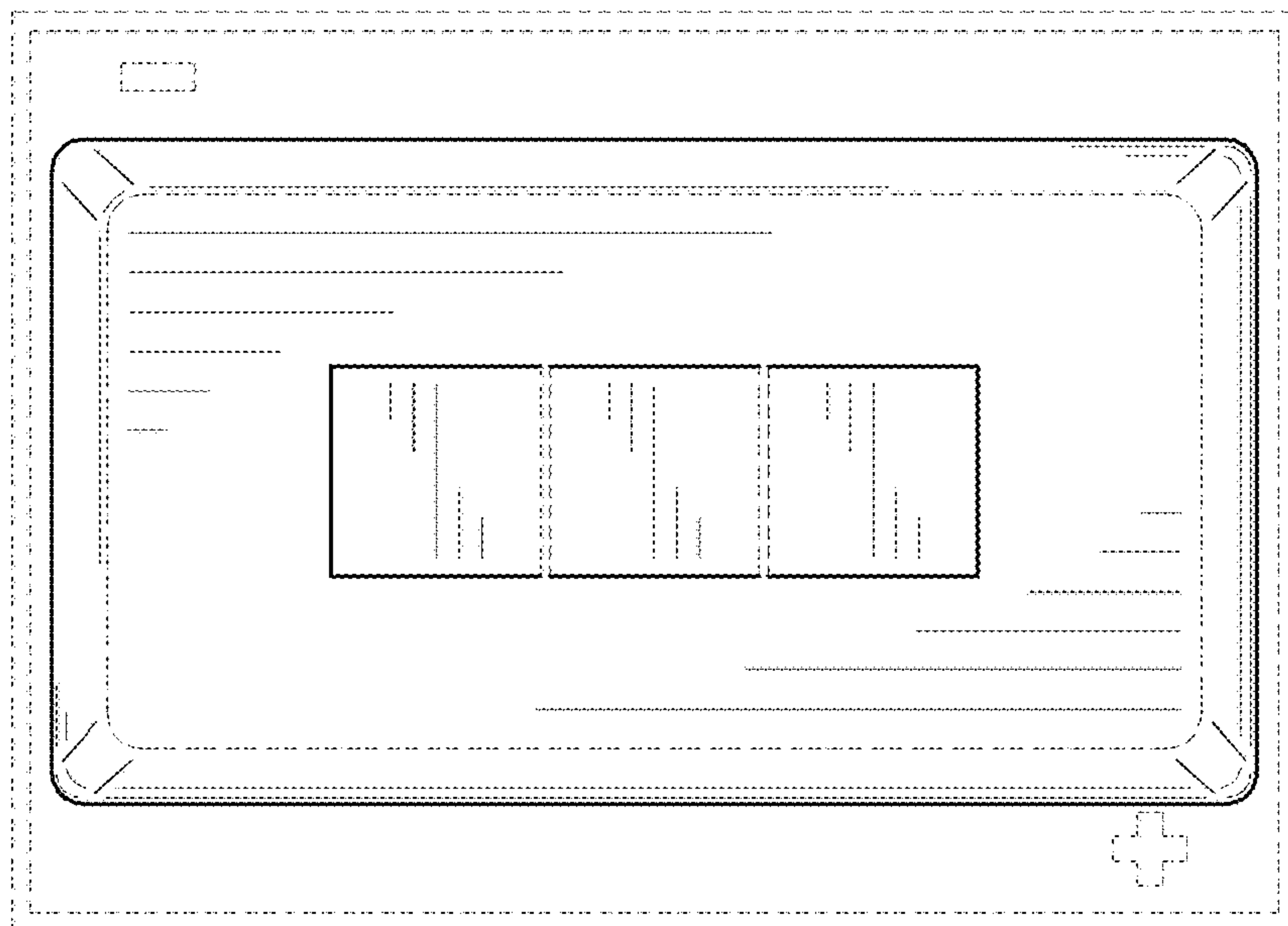


FIG. 20

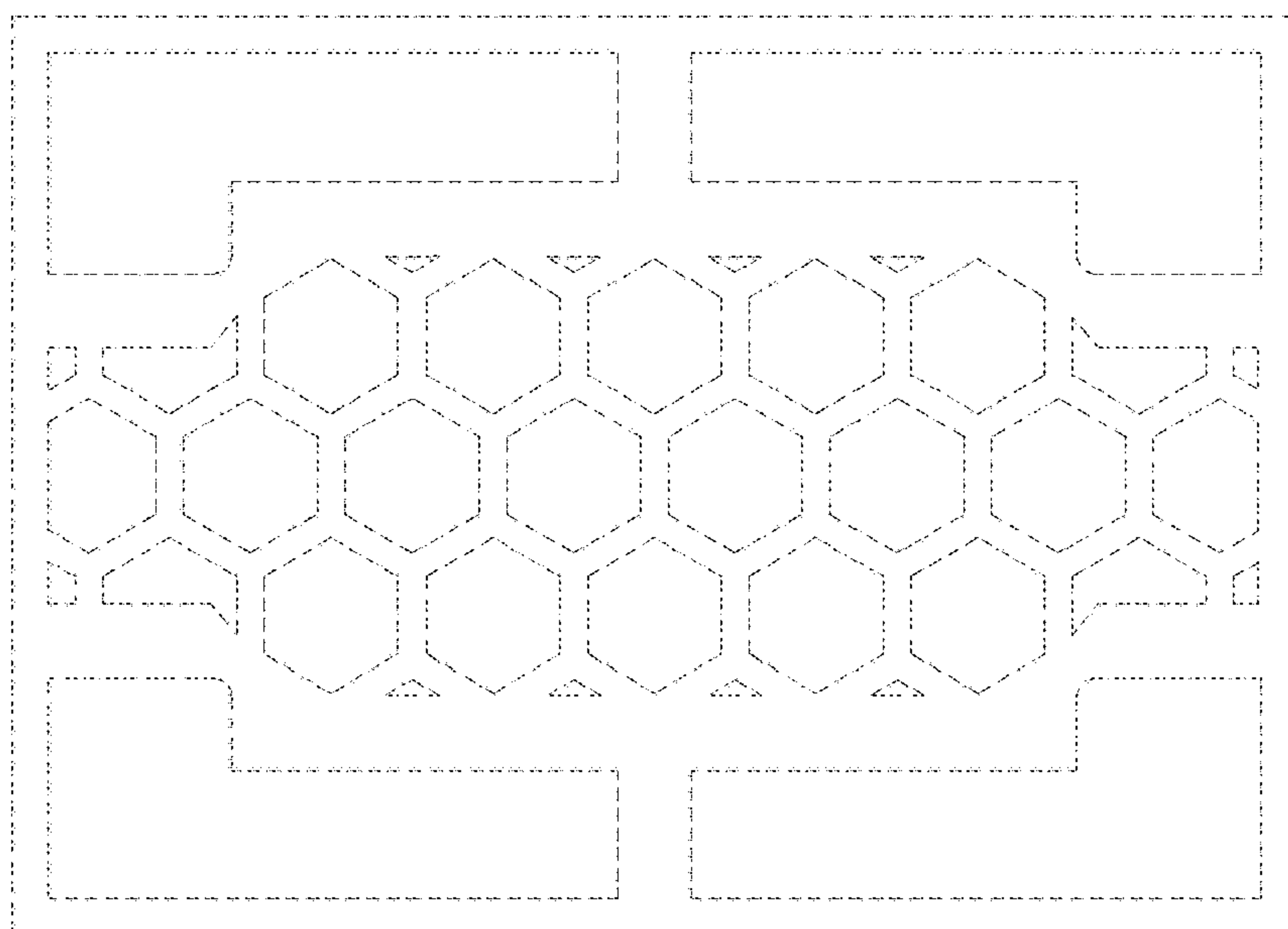


FIG. 21

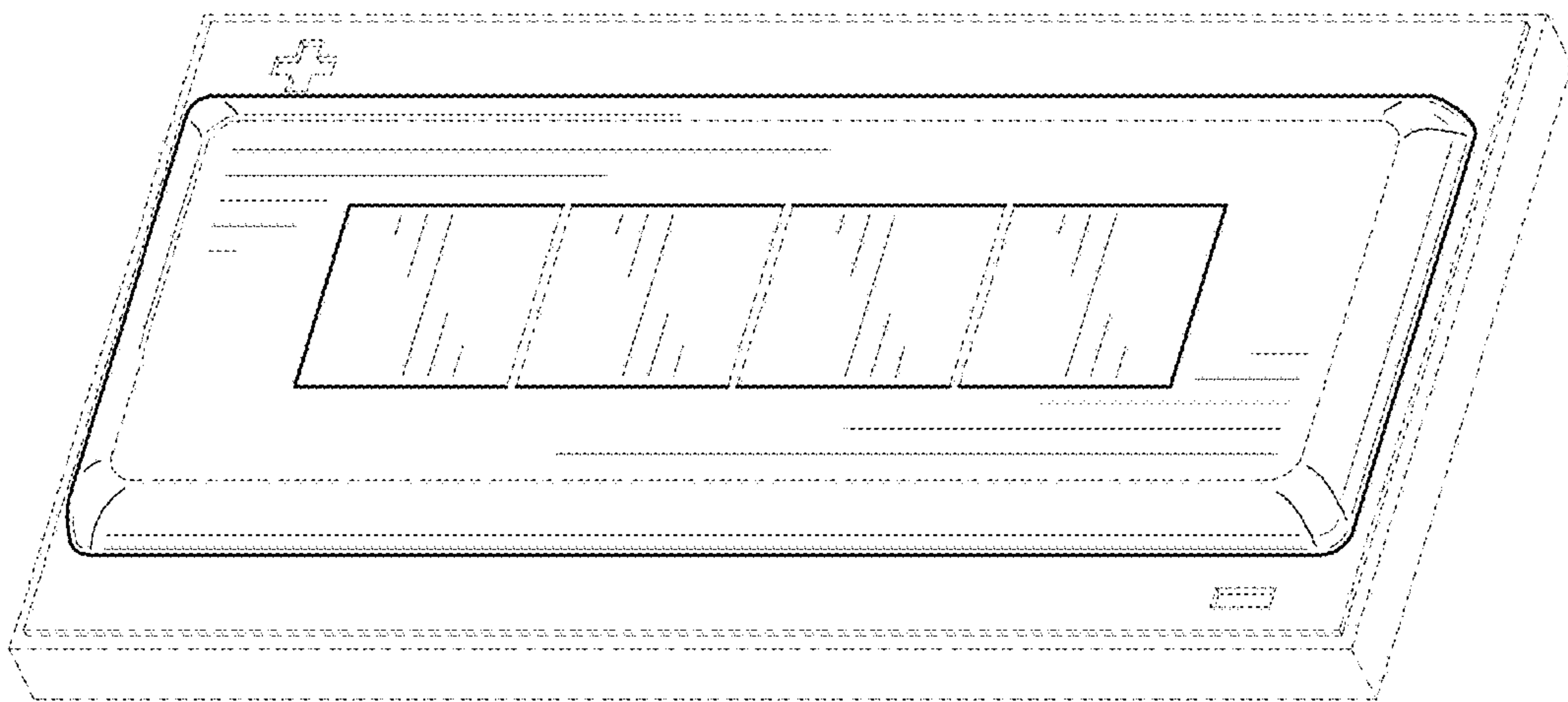


FIG. 22

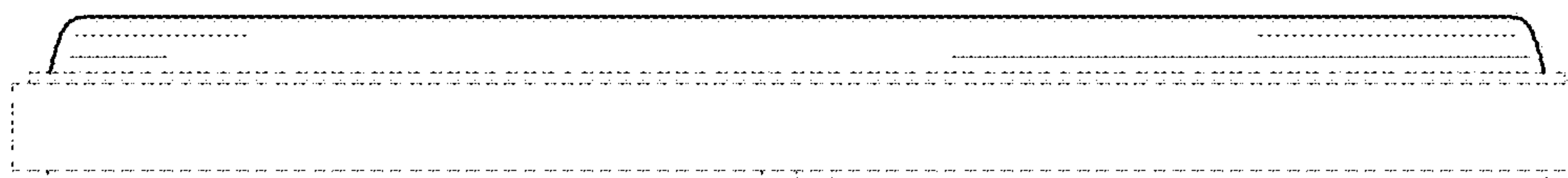


FIG. 23

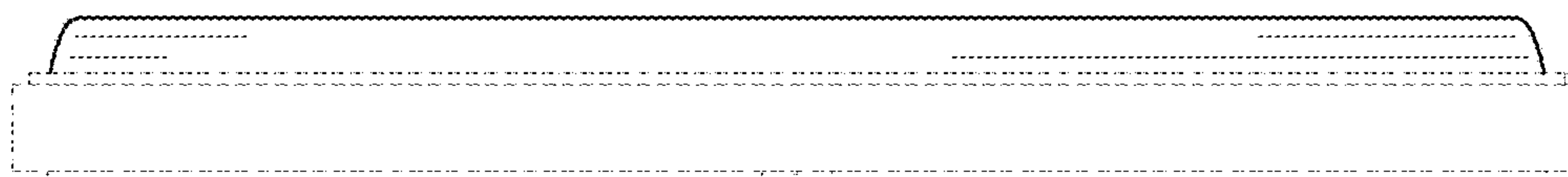


FIG. 24

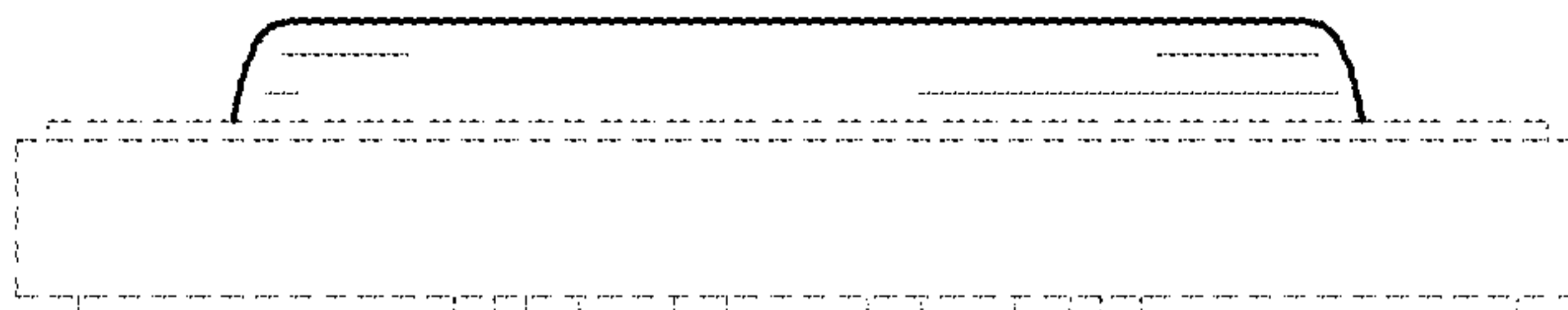


FIG. 25

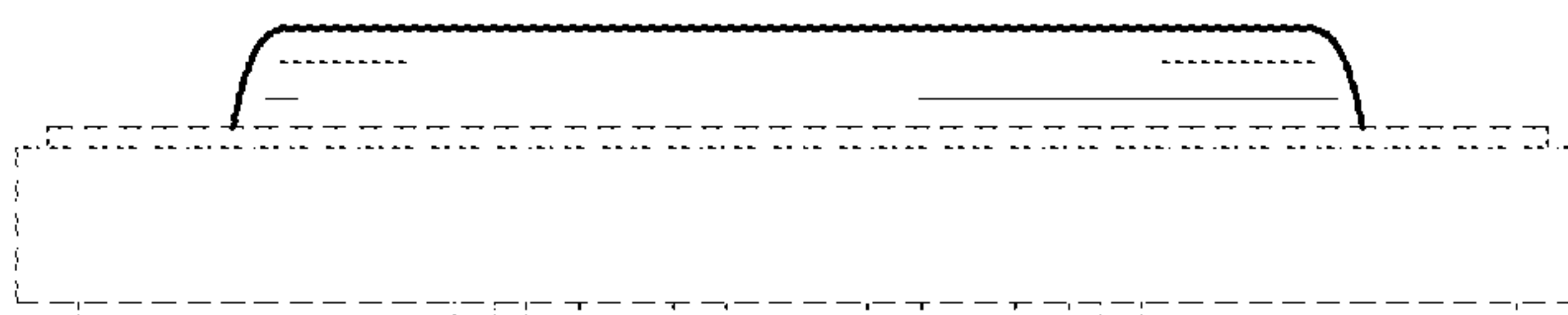


FIG. 26

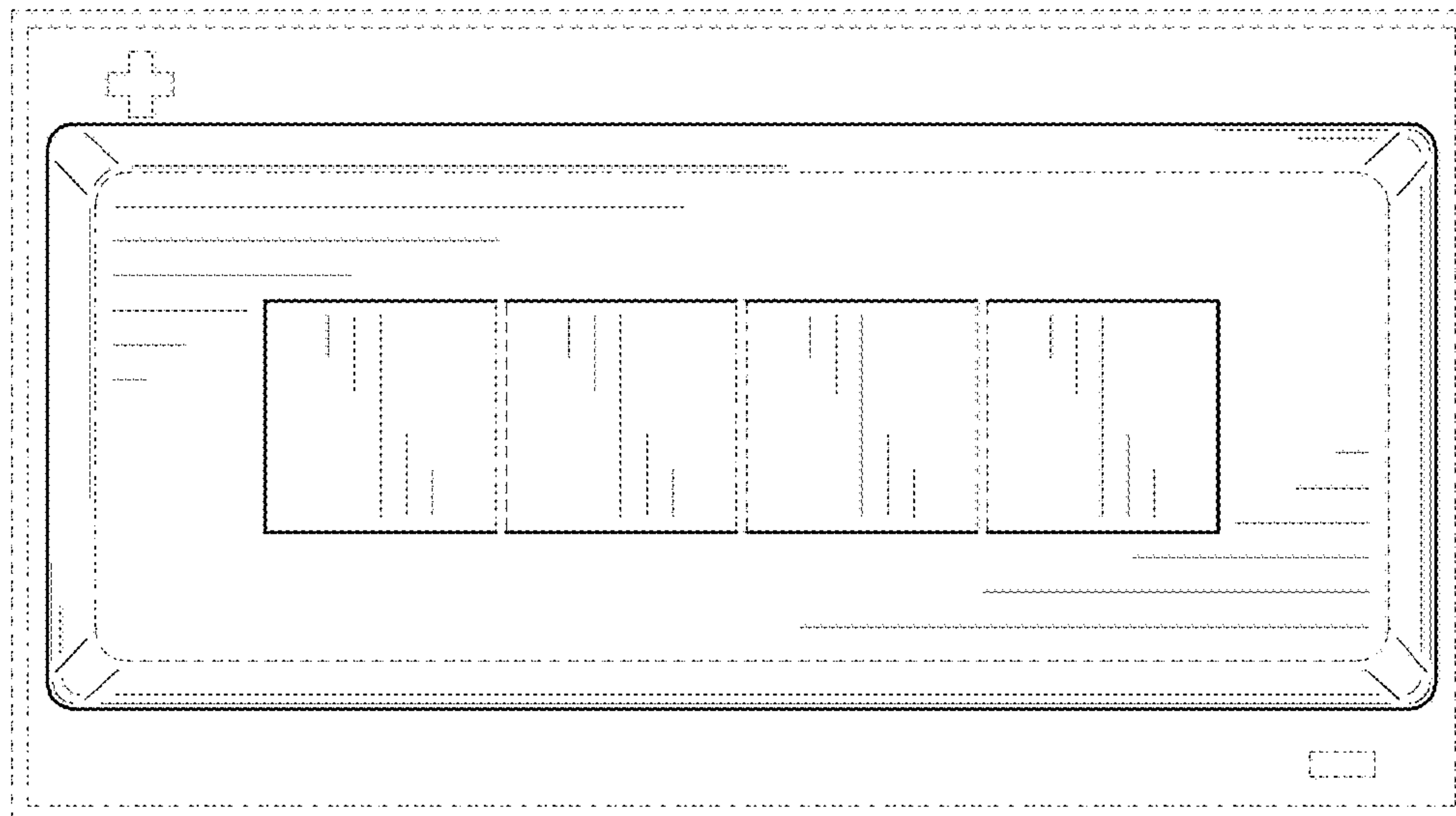


FIG. 27

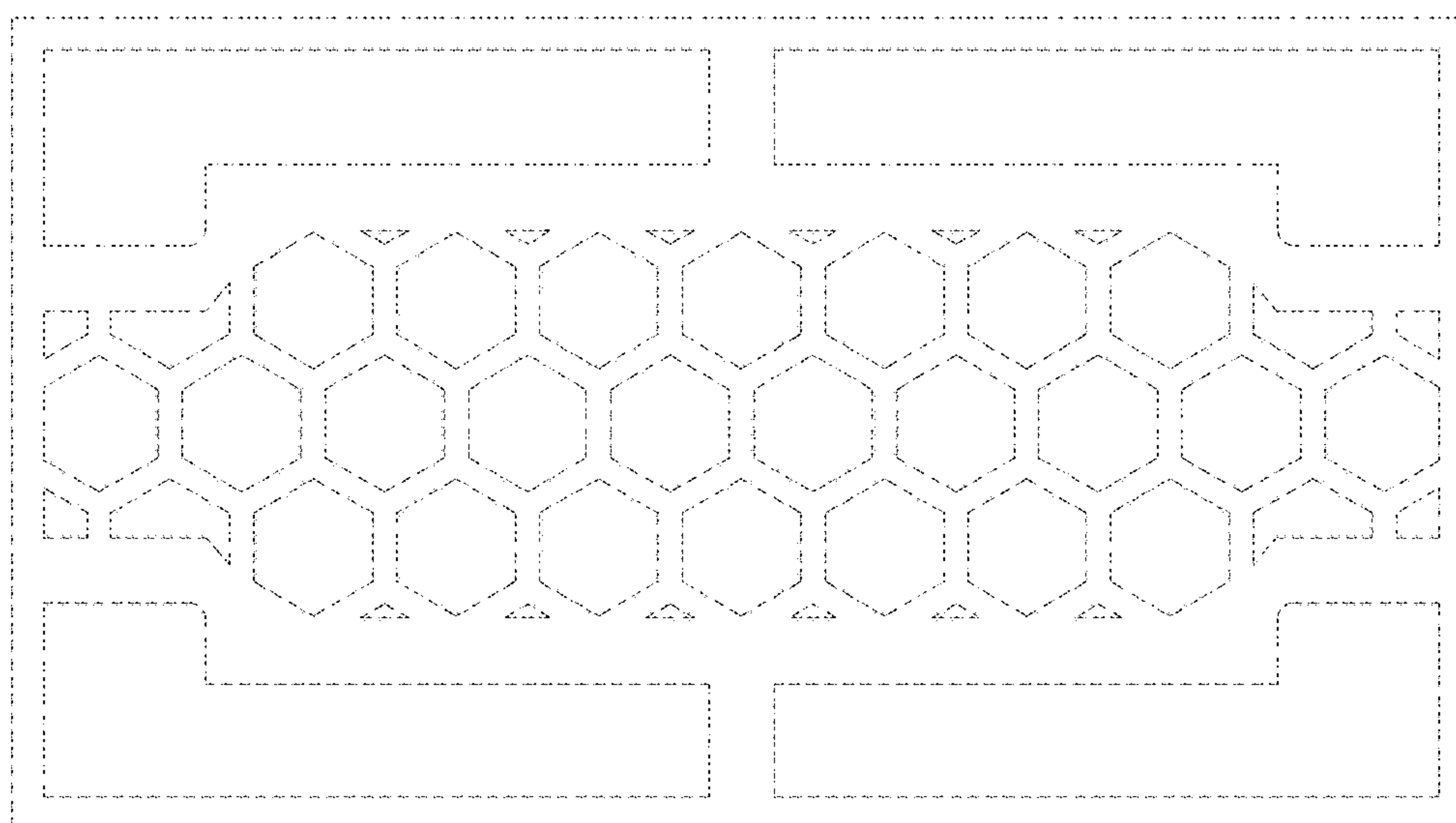


FIG. 28

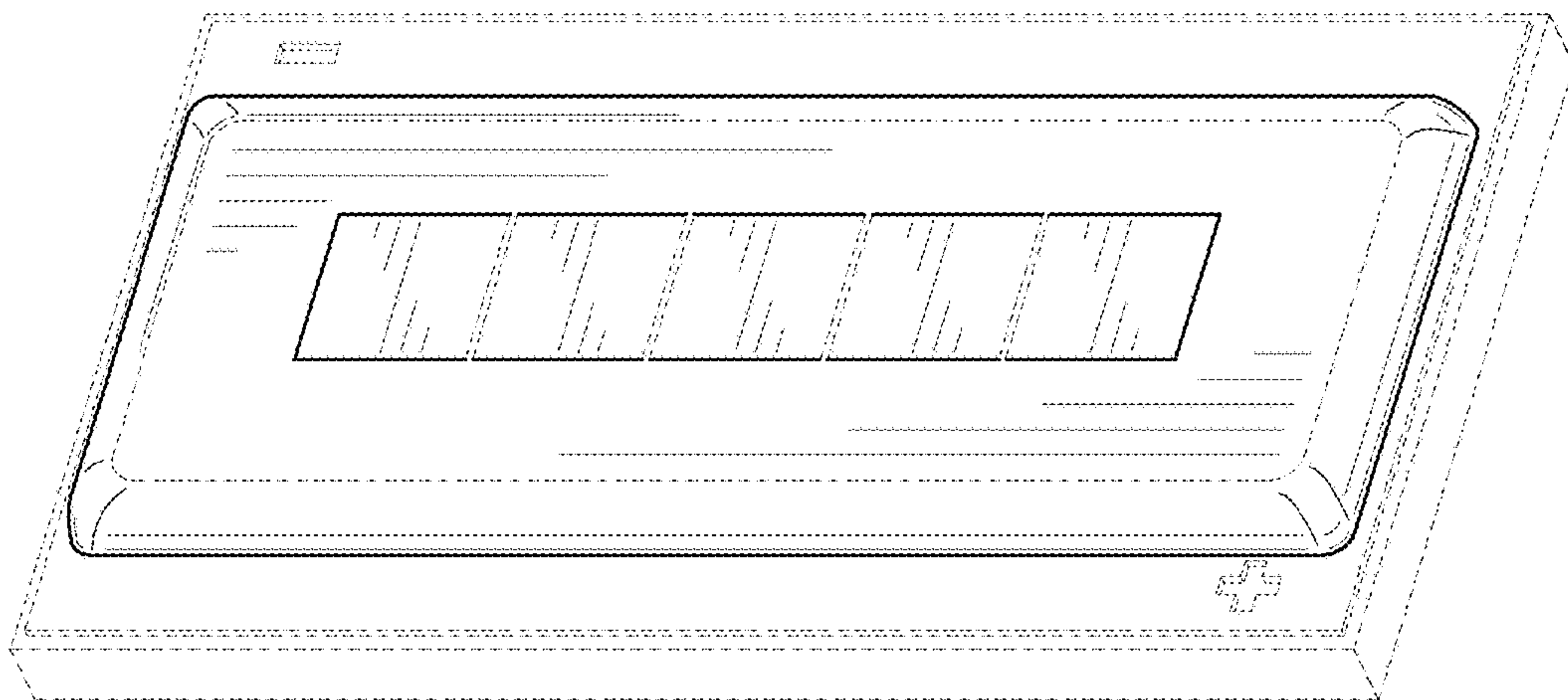


FIG. 29

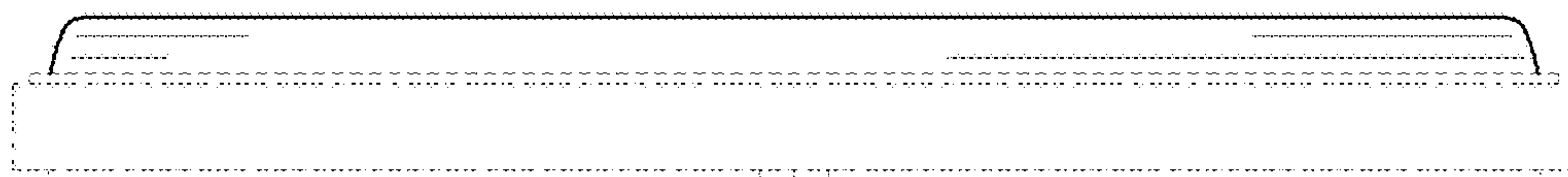


FIG. 30

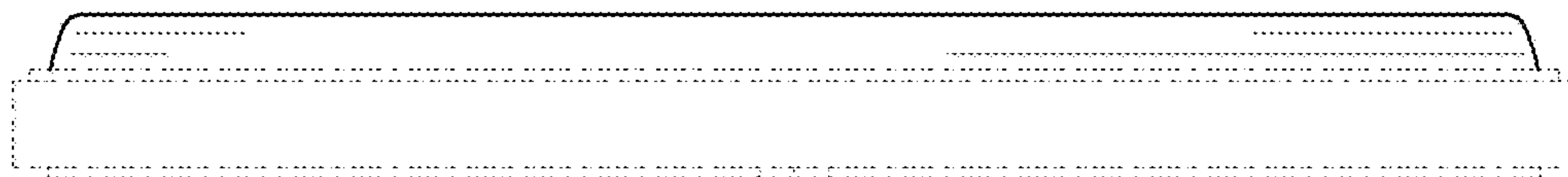


FIG. 31

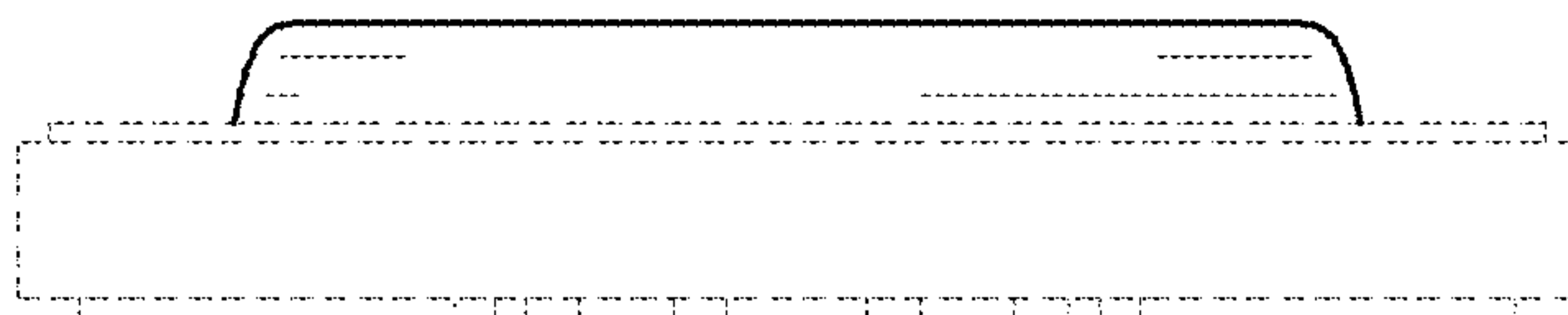


FIG. 32

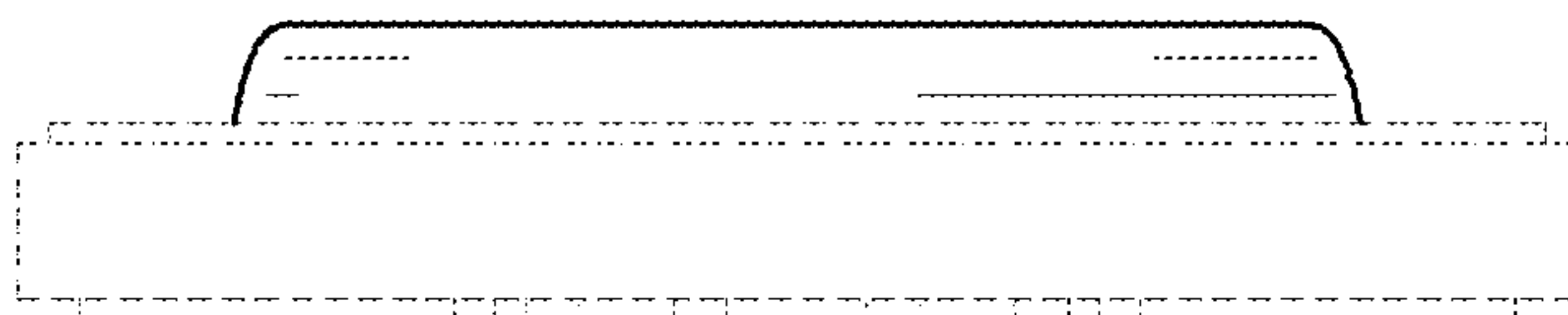


FIG. 33

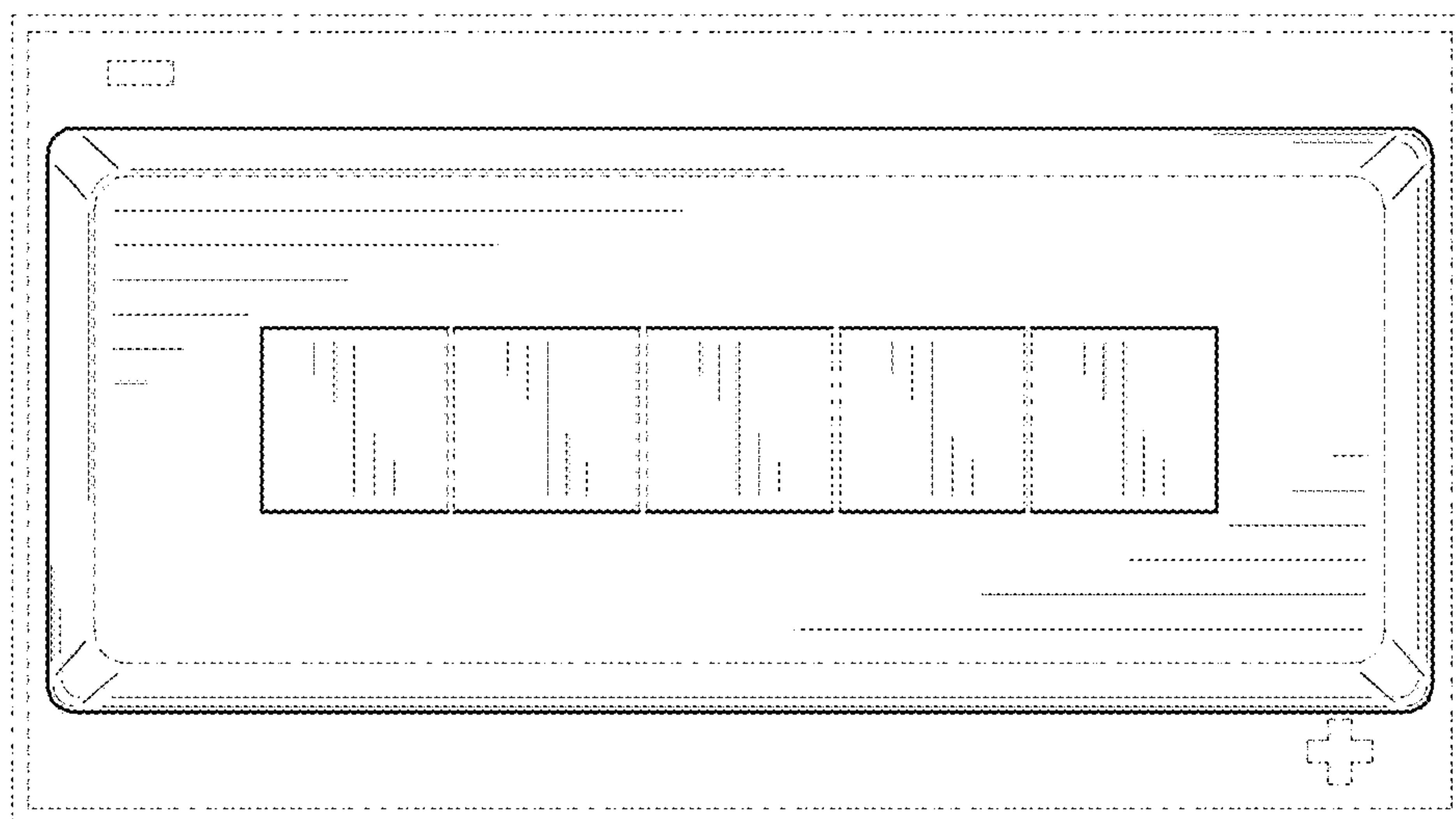


FIG. 34

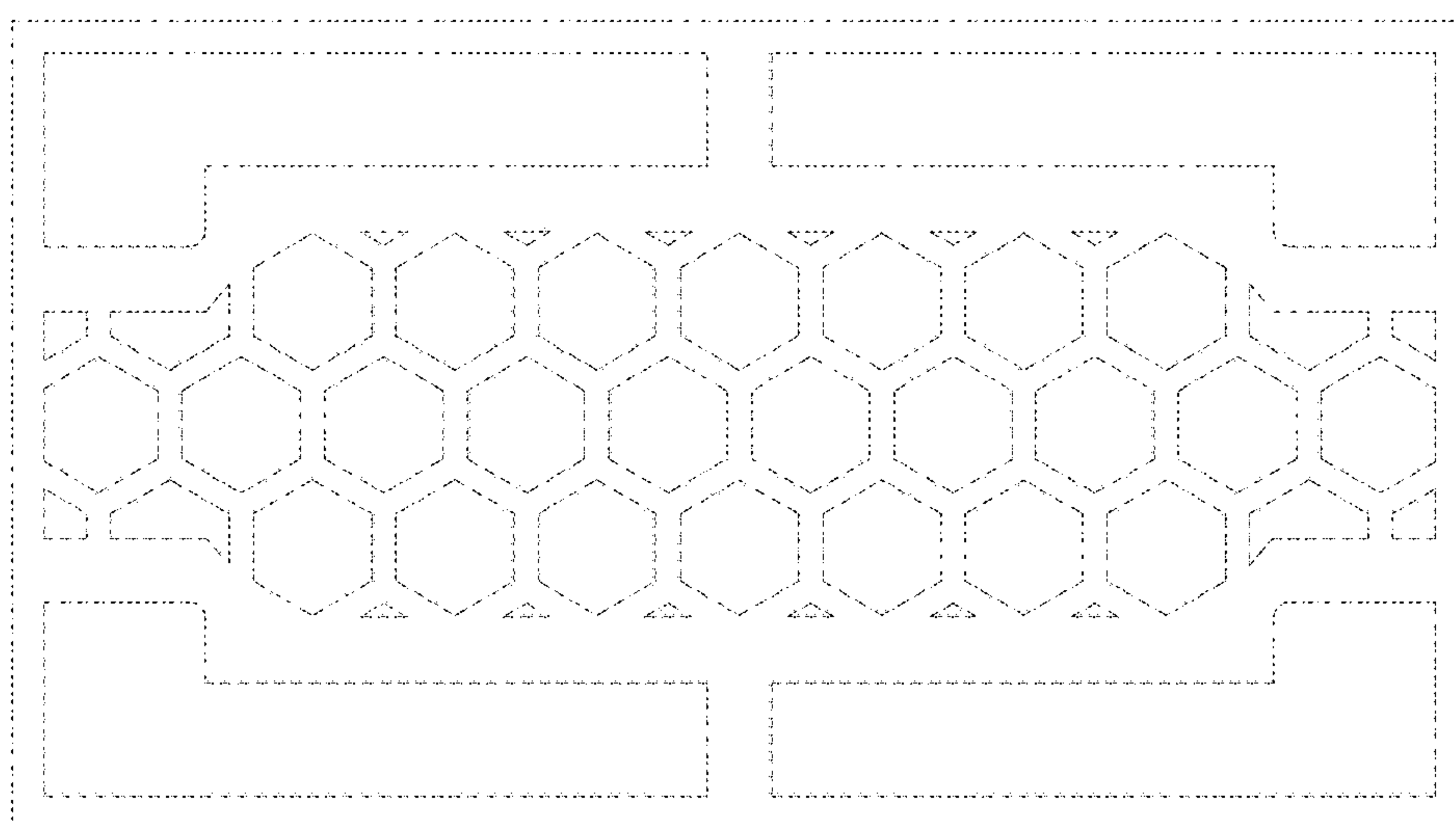


FIG. 35