



US00D847237S

(12) **United States Design Patent**
Sakka et al.

(10) **Patent No.:** **US D847,237 S**
(45) **Date of Patent:** **** Apr. 30, 2019**

(54) **SUBSTRATE PROCESSING UNIT**
(71) Applicant: **Hitachi High-Technologies Corporation**, Minato-ku, Tokyo (JP)

D805,044 S * 12/2017 Sakai D13/182
D808,350 S * 1/2018 Hart D13/182
D811,457 S * 2/2018 Sakai D15/199

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(73) Assignee: **Hitachi High-Technologies Corporation**, Tokyo (JP)

* cited by examiner
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(**) Term: **15 Years**

(21) Appl. No.: **29/606,730**

(57) **CLAIM**

(22) Filed: **Jun. 7, 2017**

The ornamental design for a substrate processing unit, as shown and described.

(30) **Foreign Application Priority Data**

DESCRIPTION

Dec. 9, 2016 (JP) 2016-026823

(51) **LOC (11) Cl.** **15-03**

(52) **U.S. Cl.**
USPC **D15/199**

(58) **Field of Classification Search**
USPC D15/138, 199; D13/182, 184, 199
CPC H05H 1/2406; H05H 2001/2412; H05H 2001/2418
See application file for complete search history.

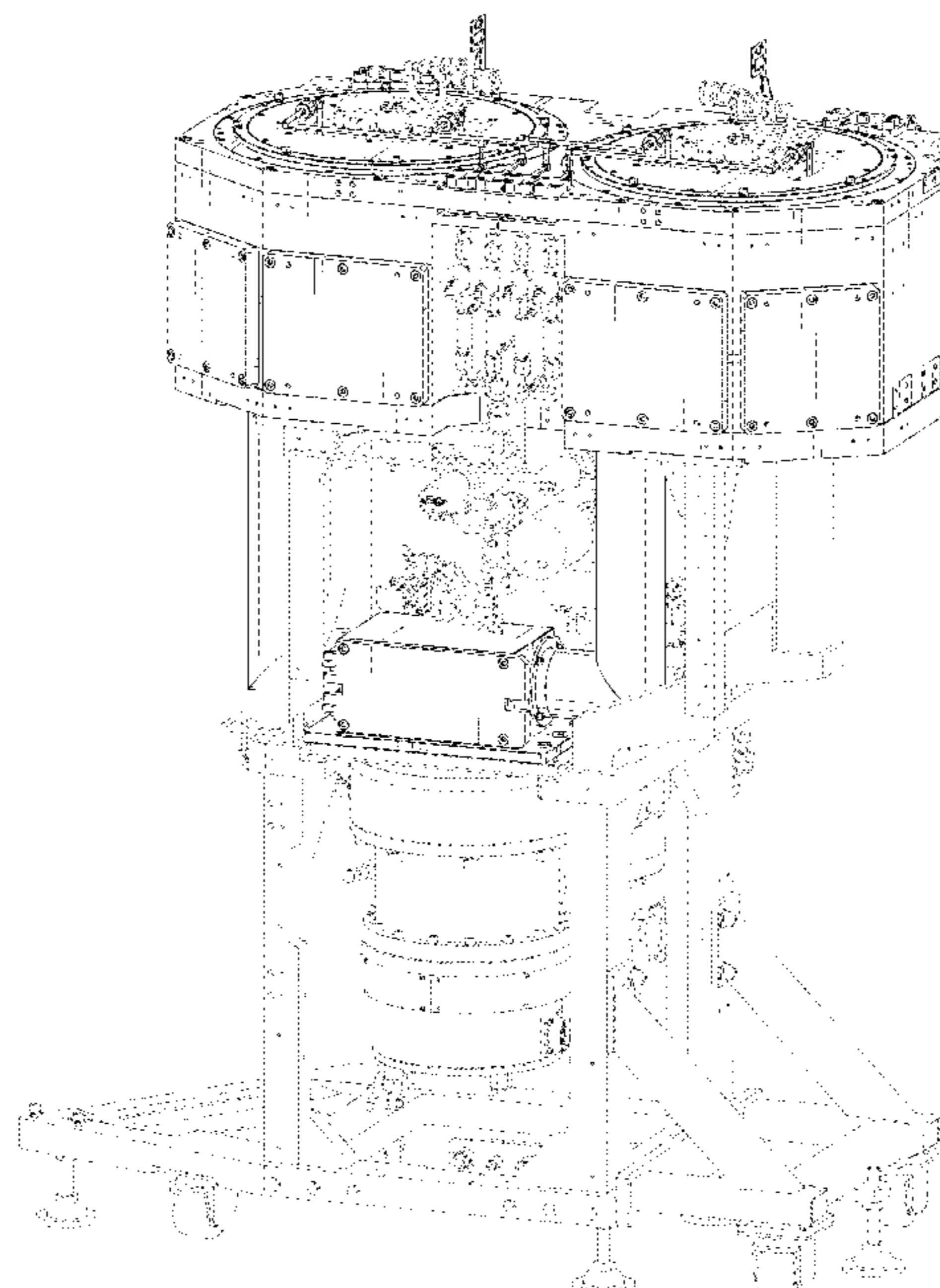
Cross-reference to related applications:
Application Ser. No. 29/606,718, filed herewith and entitled "Substrate Processing Unit"; and
Application Ser. No. 29/606,724, filed herewith and entitled "Substrate Processing Unit".
FIG. 1 is a front, top and right side perspective view of a substrate processing unit according to the design;
FIG. 2 is a front elevational view thereof;
FIG. 3 is a rear elevational view thereof;
FIG. 4 is a right side elevational view thereof;
FIG. 5 is a left side elevational view thereof;
FIG. 6 is a top plan view thereof;
FIG. 7 is a bottom plan view thereof; and,
FIG. 8 is a reference perspective view thereof showing an example of a potential using condition with a cover.
The broken lines are for illustrative purposes only and form no part of the claimed design.

(56) **References Cited**

U.S. PATENT DOCUMENTS

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1 Claim, 7 Drawing Sheets



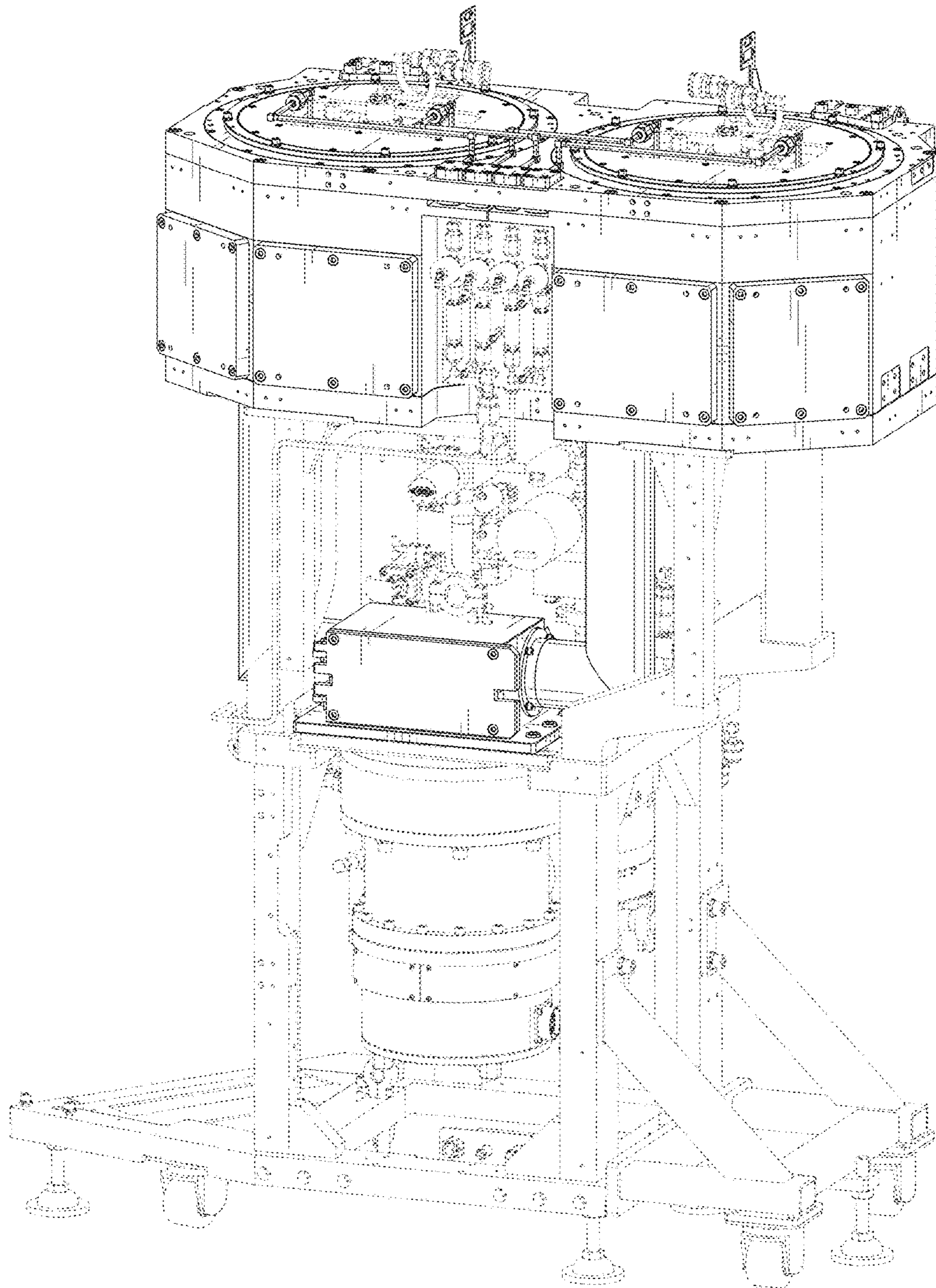


FIG. 1

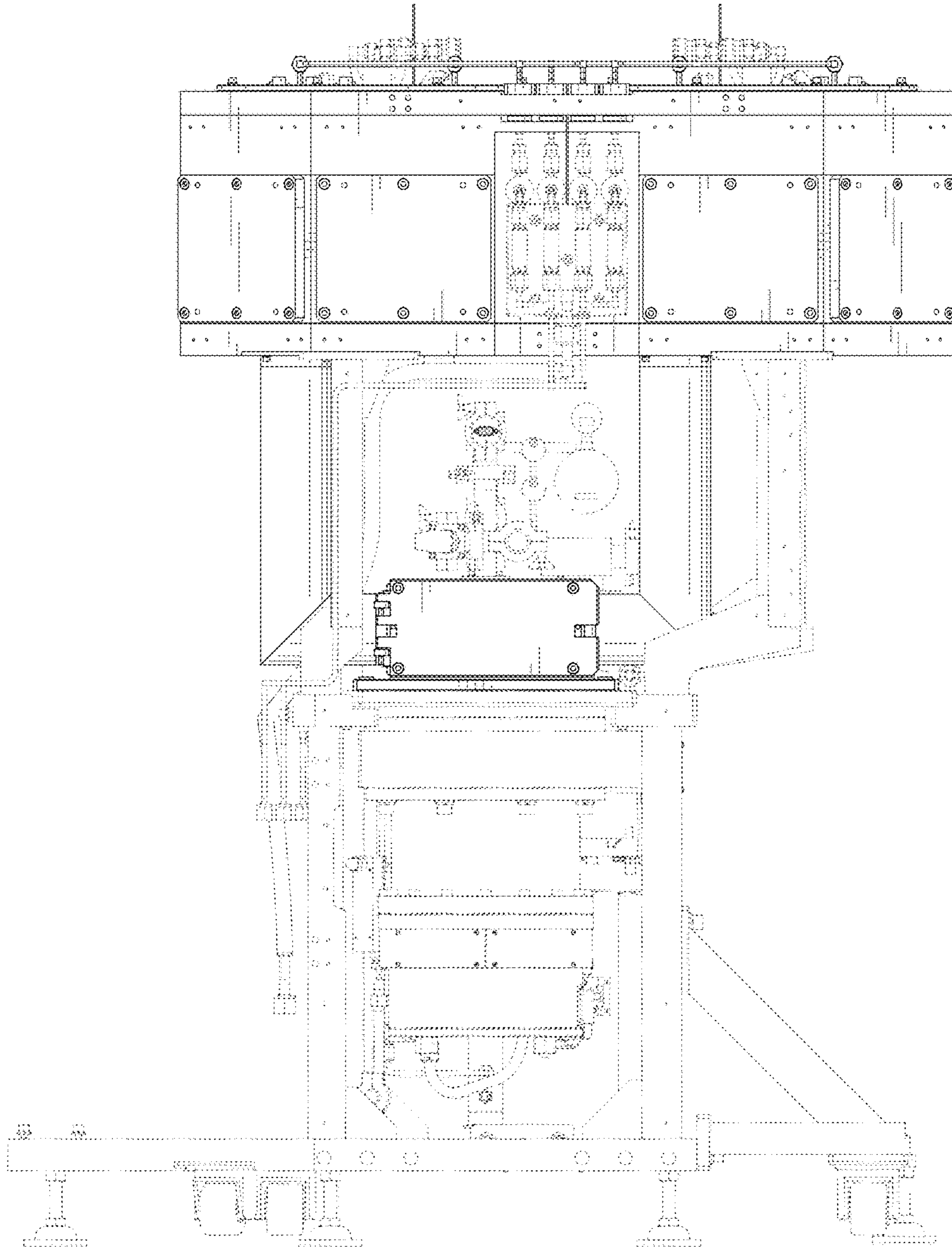


FIG. 2

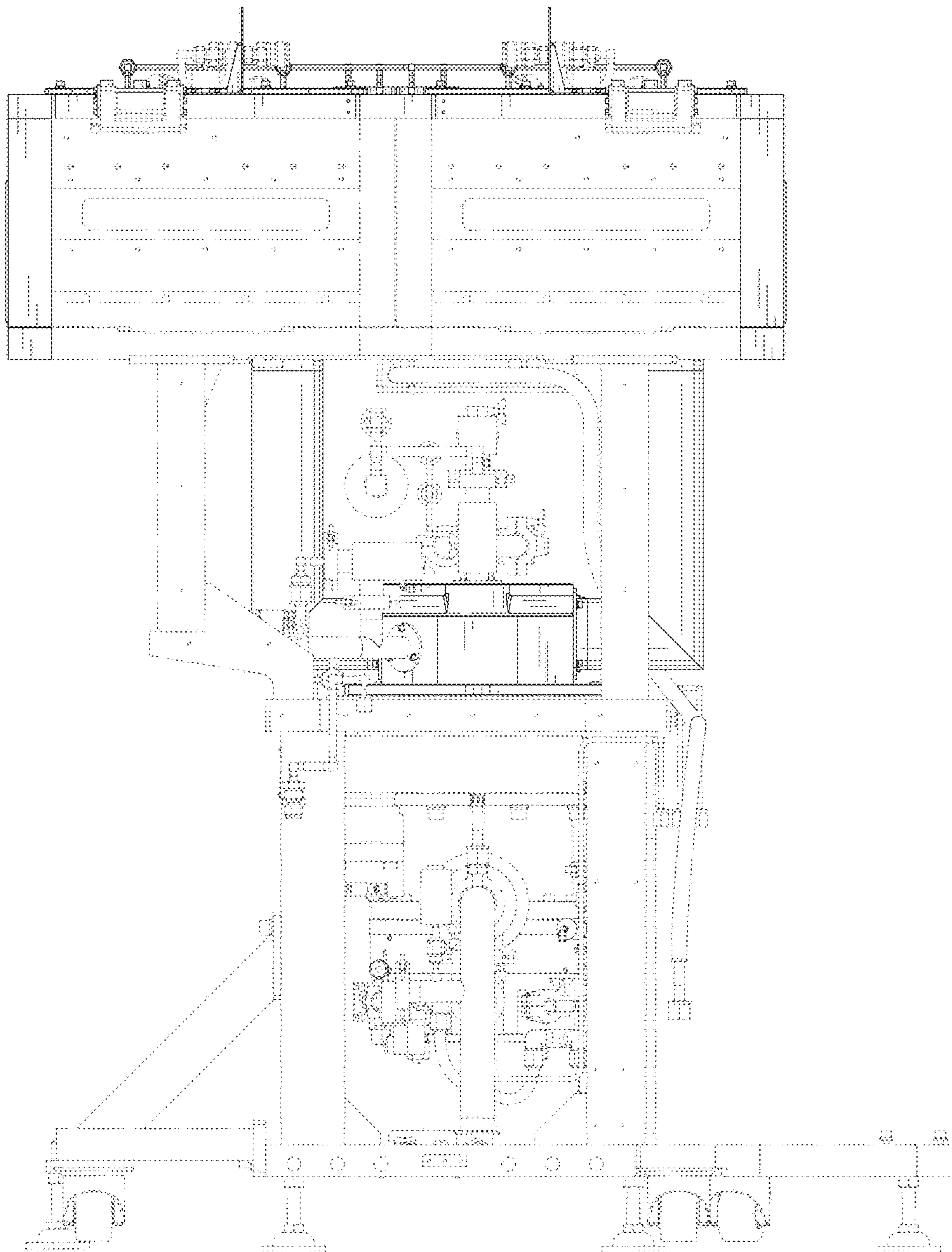


FIG. 3

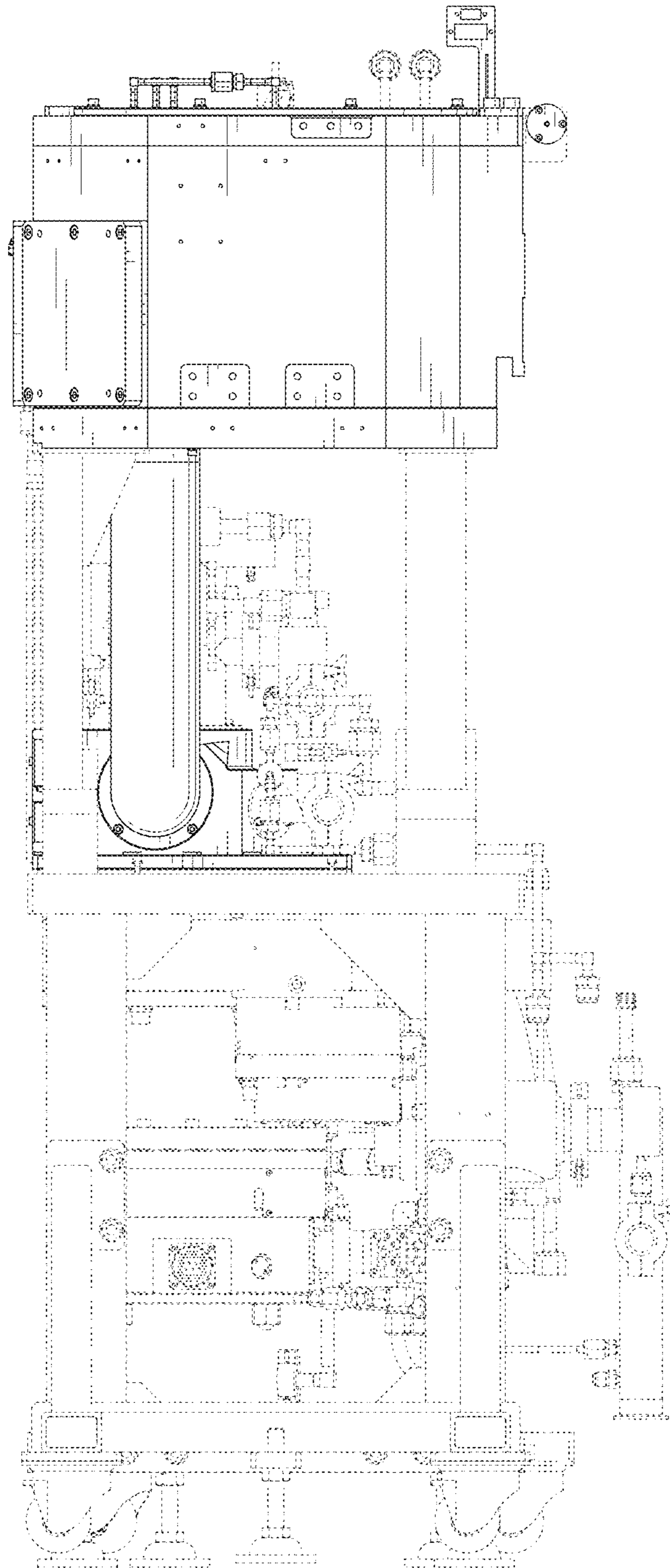


FIG. 4

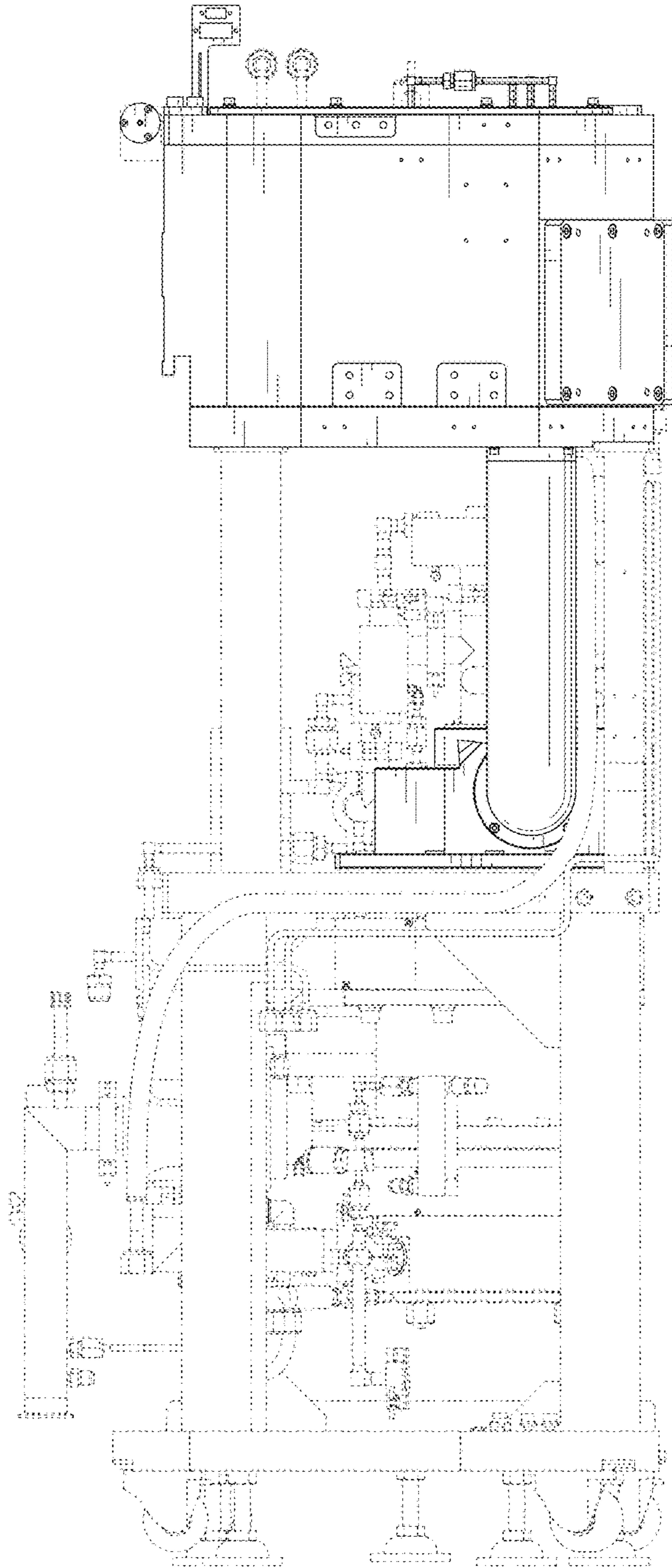


FIG. 5

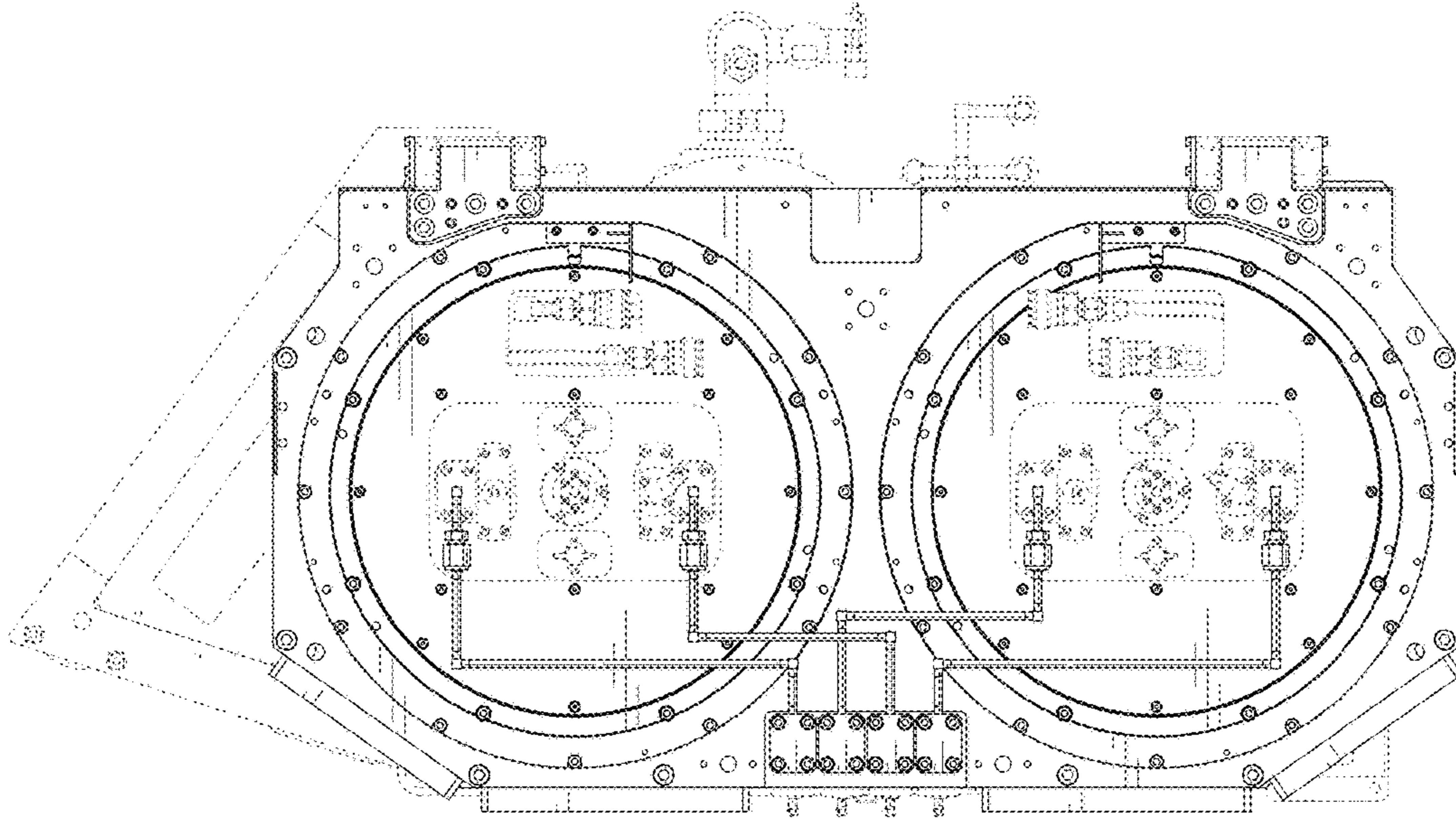


FIG. 6

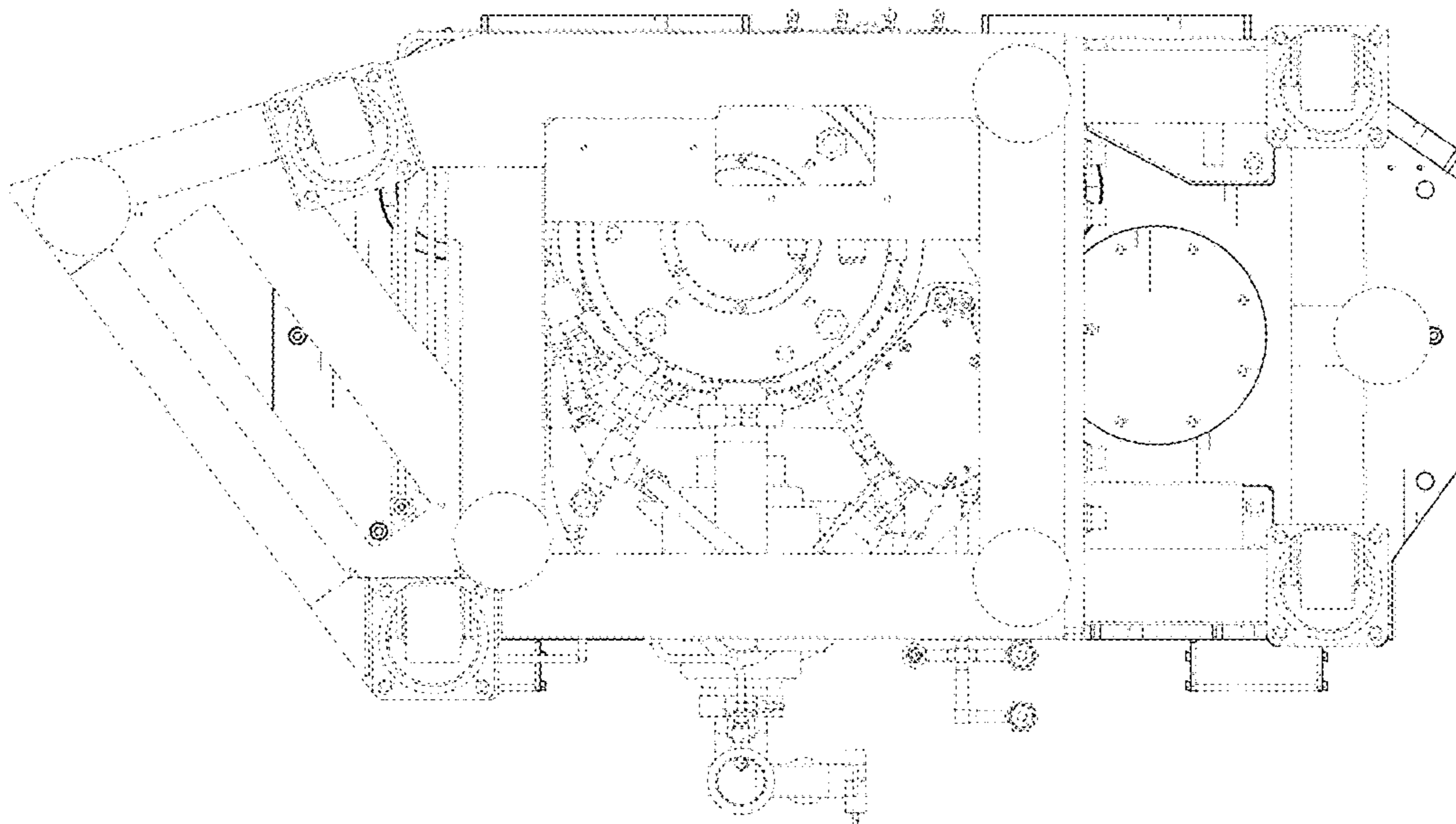


FIG. 7

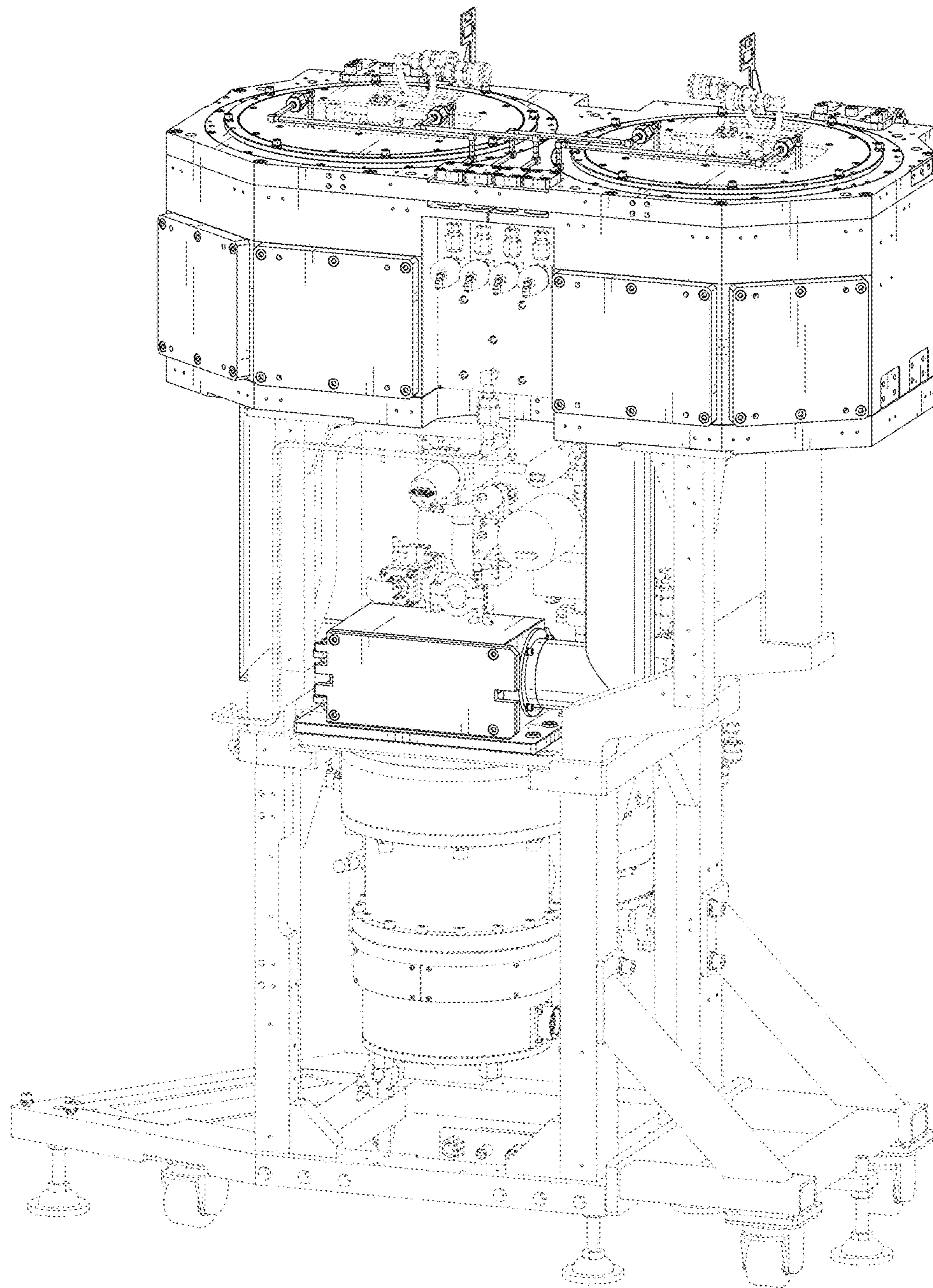


FIG. 8