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(12) **United States Design Patent**
Krivosnak et al.

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(45) **Date of Patent:** **** Mar. 26, 2019**

(54) **ELECTRONICS ENCLOSURE**

7/183; H05K 7/14; H05K 5/00; H05K
7/00; H05H 7/20; G11B 33/126; G11B
33/128; G11B 33/08; G11B 33/02

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See application file for complete search history.

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Primary Examiner — Freda S Nunn

(**) Term: **15 Years**

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(21) Appl. No.: **29/614,800**

(22) Filed: **Aug. 23, 2017**

(57) **CLAIM**

Related U.S. Application Data

The ornamental design for an electronics enclosure, as
shown and described.

(62) Division of application No. 29/573,548, filed on Aug.
8, 2016, now Pat. No. Des. 802,582.

(51) **LOC (11) Cl.** **14-02**

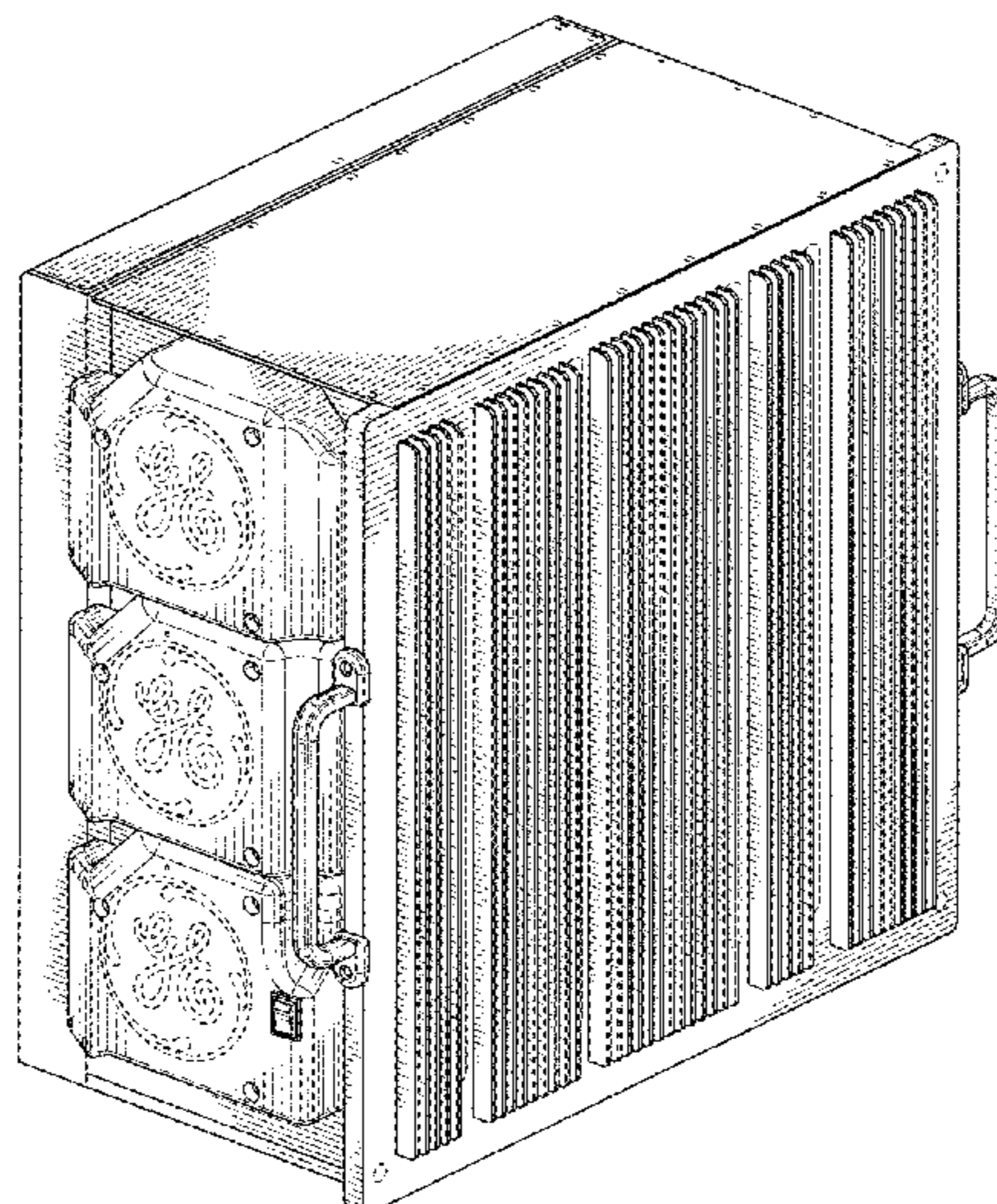
DESCRIPTION

(52) **U.S. Cl.**
USPC **D14/300**

(58) **Field of Classification Search**
USPC D14/240, 300, 301, 308, 313, 314,
D14/348-356, 364-368, 441-446, 188;
D13/123, 158, 162, 184, 199;
312/223.1-223.3, 351.11;
360/99.01-99.12; 369/34.01-36.01;
361/679.31-679.33, 679.37, 679.39,
361/679.46, 679.53, 679.4, 679.41, 727
CPC . G06F 1/20; G06F 1/181; G06F 1/187; G06F
1/183; G06F 1/184; G06F 1/16; H05K
7/16; H05K 7/1424; H05K 7/1409; H05K
7/20; H05K 7/20545; H05K 7/20727;
H05K 7/1425; H05K 7/1488; H05K

FIG. 1 is a perspective view of an electronics enclosure
depicting our new design;
FIG. 2 is a front view thereof;
FIG. 3 is a rear view thereof;
FIG. 4 is a side view thereof;
FIG. 5 is an opposite side view thereof;
FIG. 6 is a top plan view thereof;
FIG. 7 is a bottom plan view thereof; and,
FIG. 8 is an additional perspective view thereof.
The broken line portion of the figure drawings is included to
show unclaimed subject matter only and forms no part of the
claimed design.

1 Claim, 7 Drawing Sheets



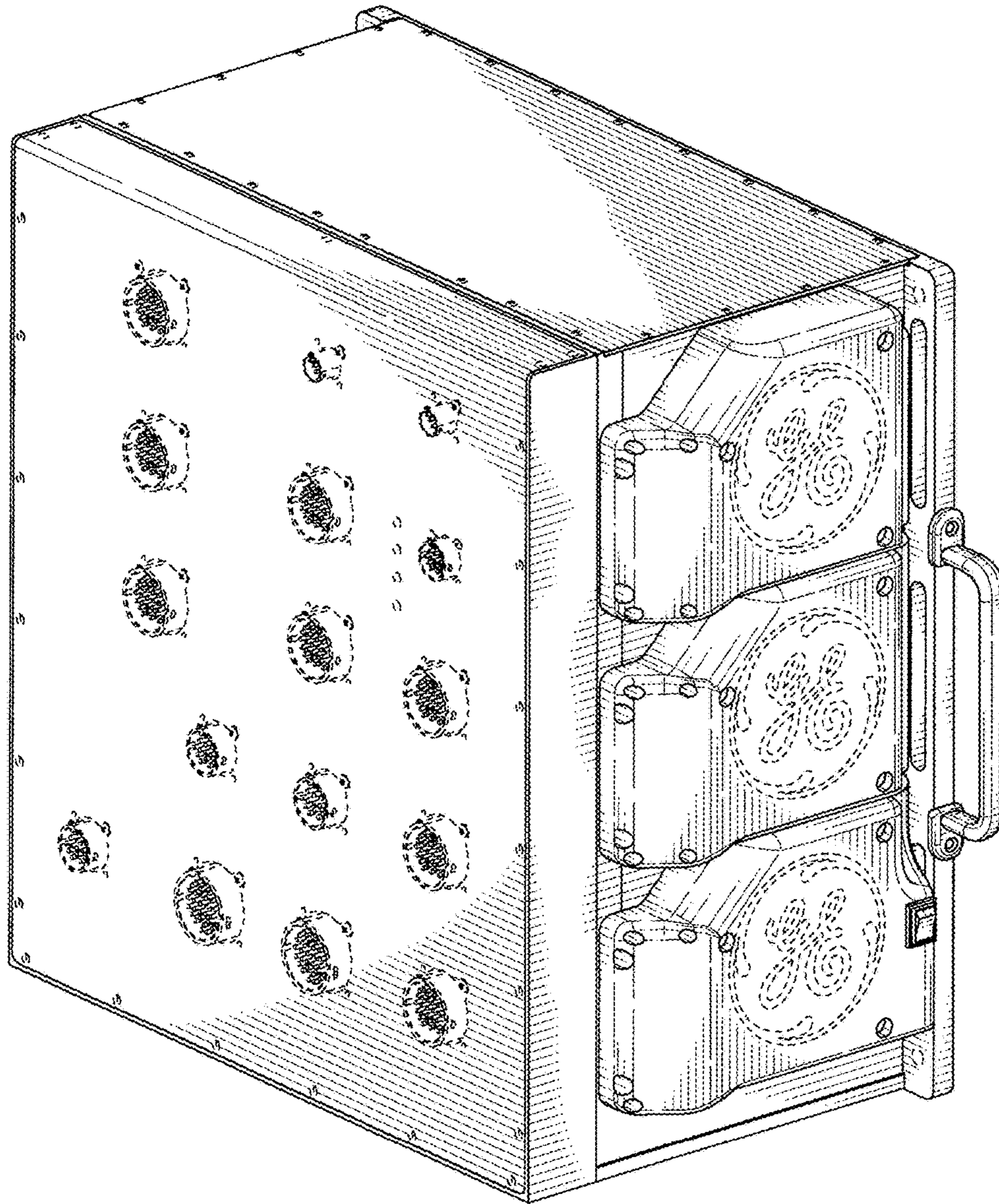


FIG. 1

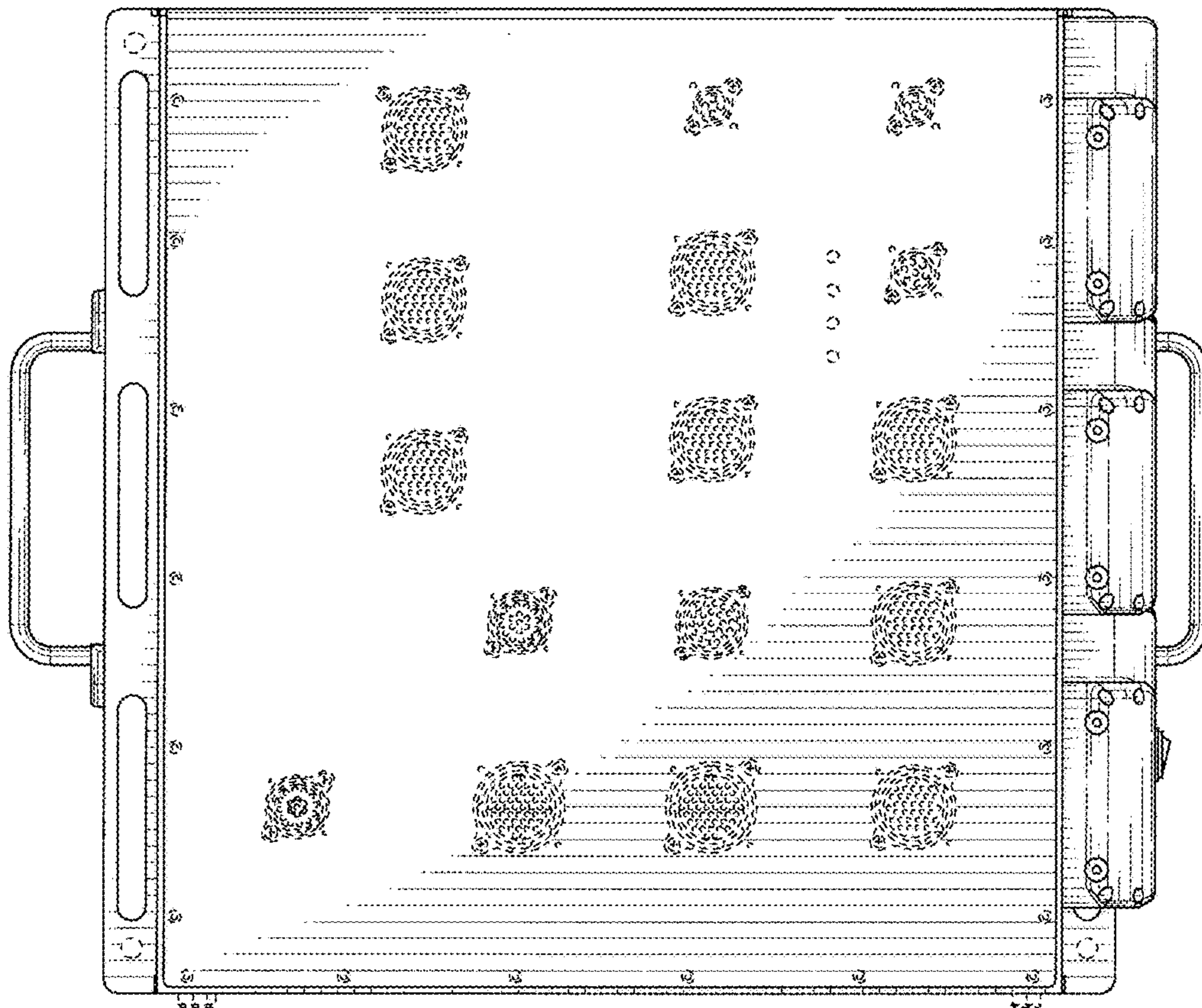


FIG. 2

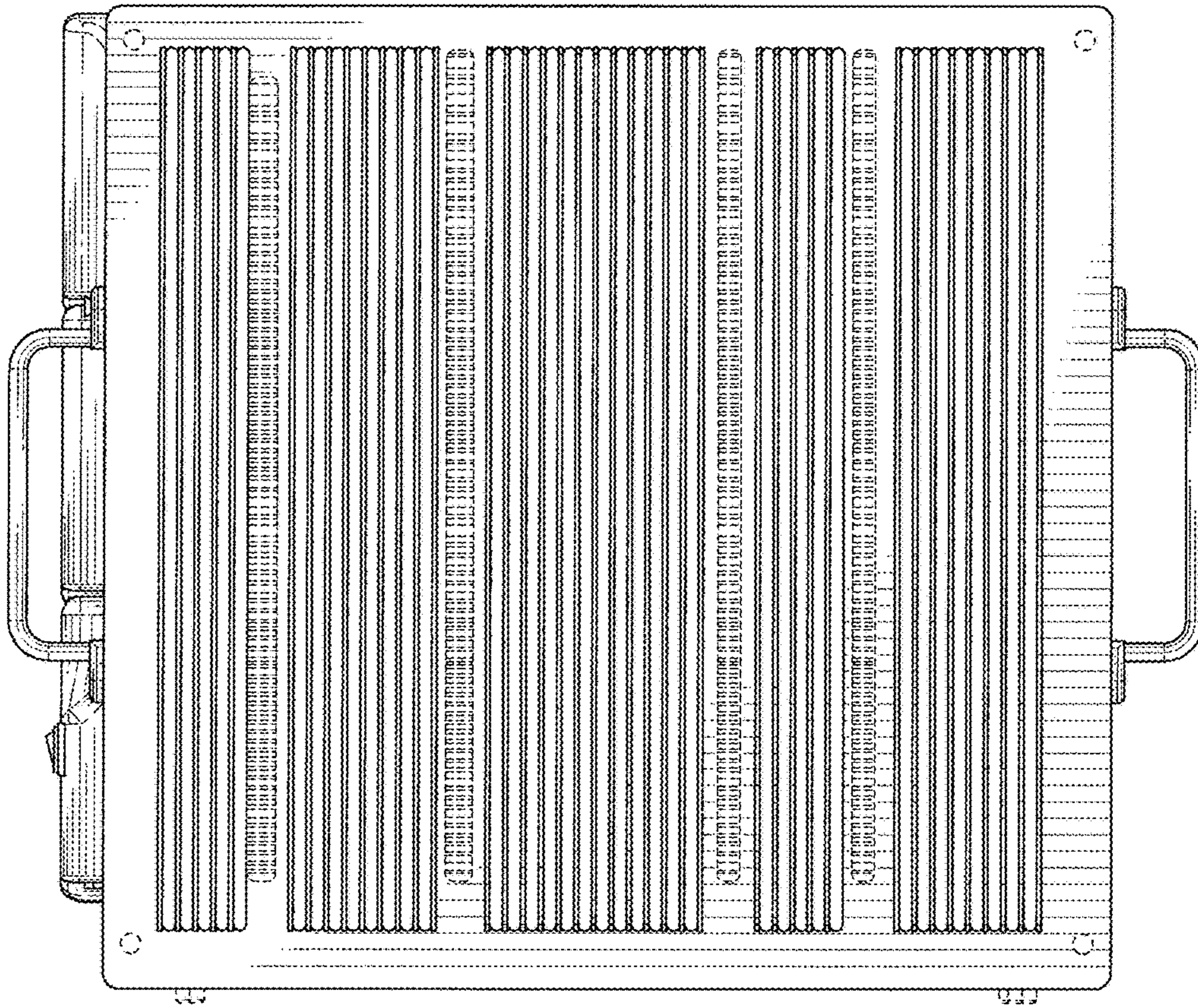


FIG. 3

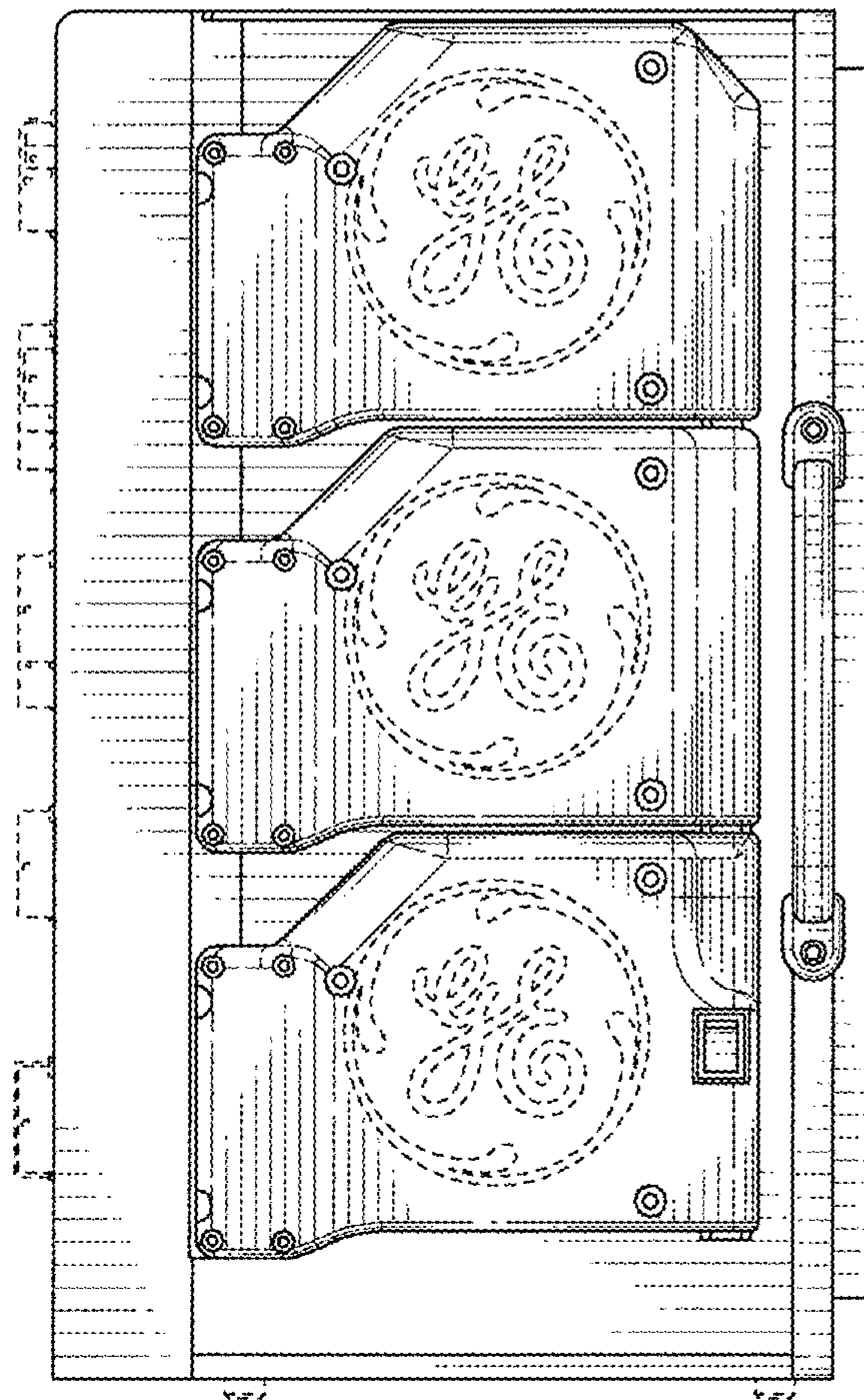


FIG. 4

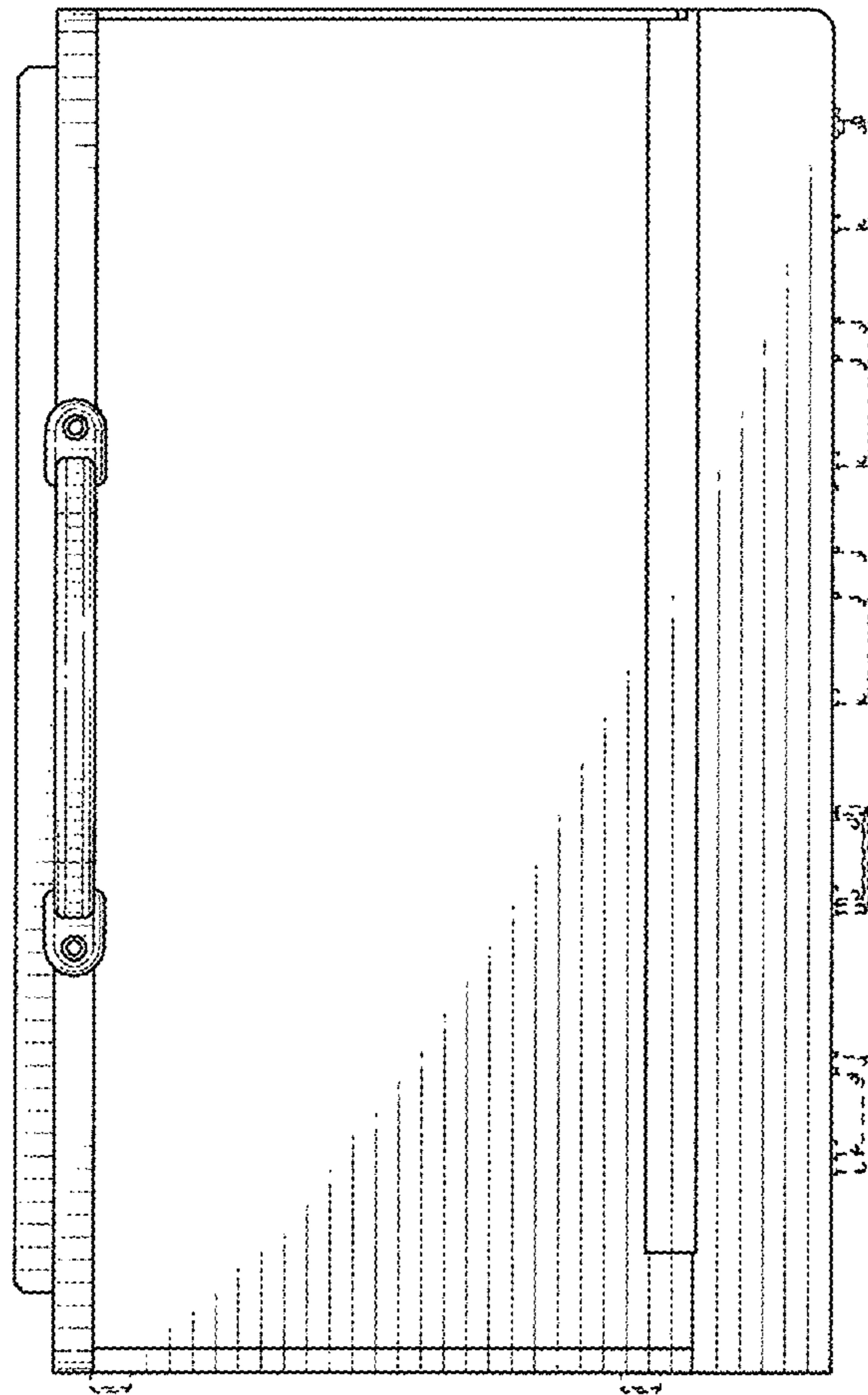


FIG. 5

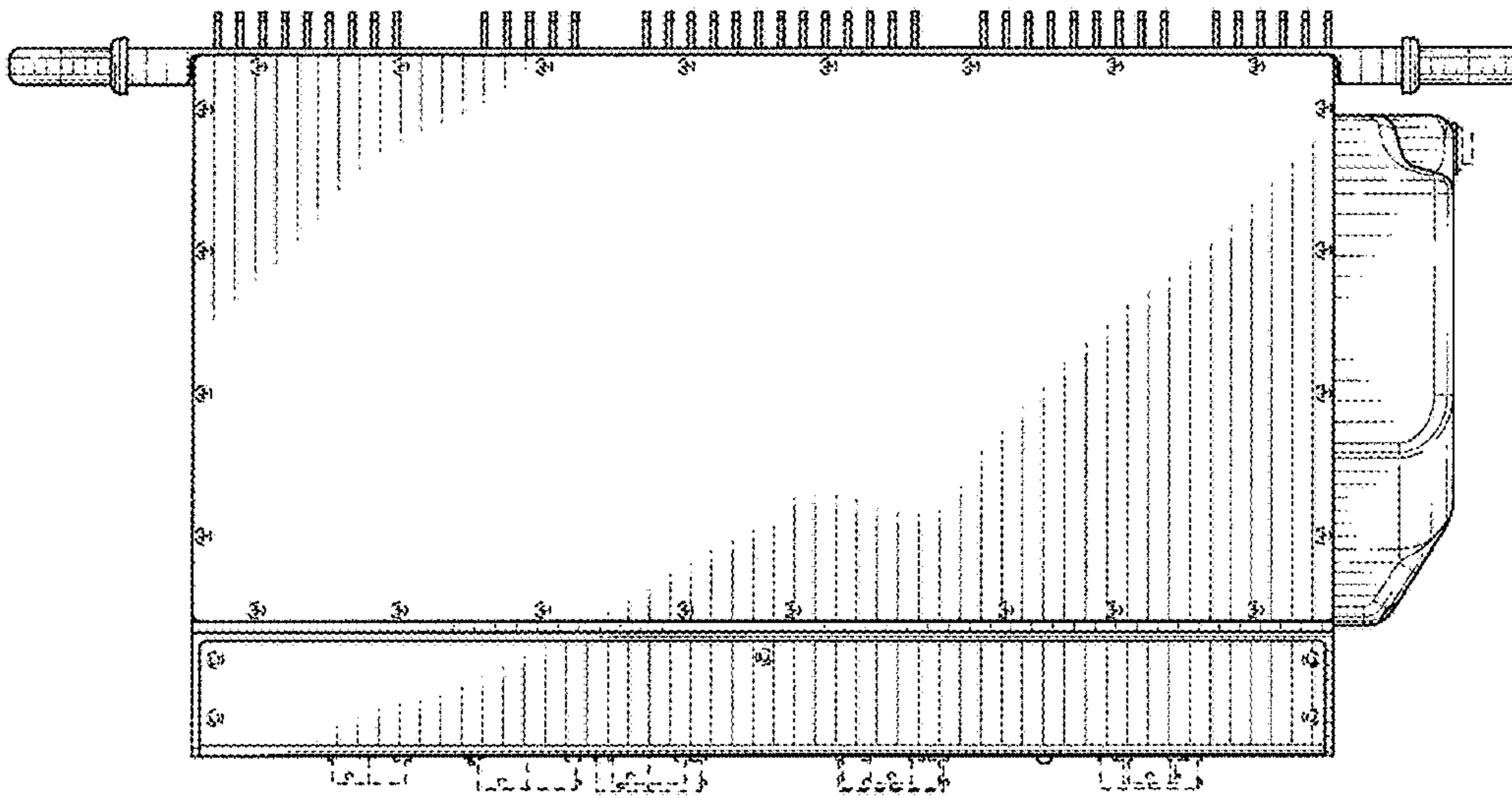


FIG. 6

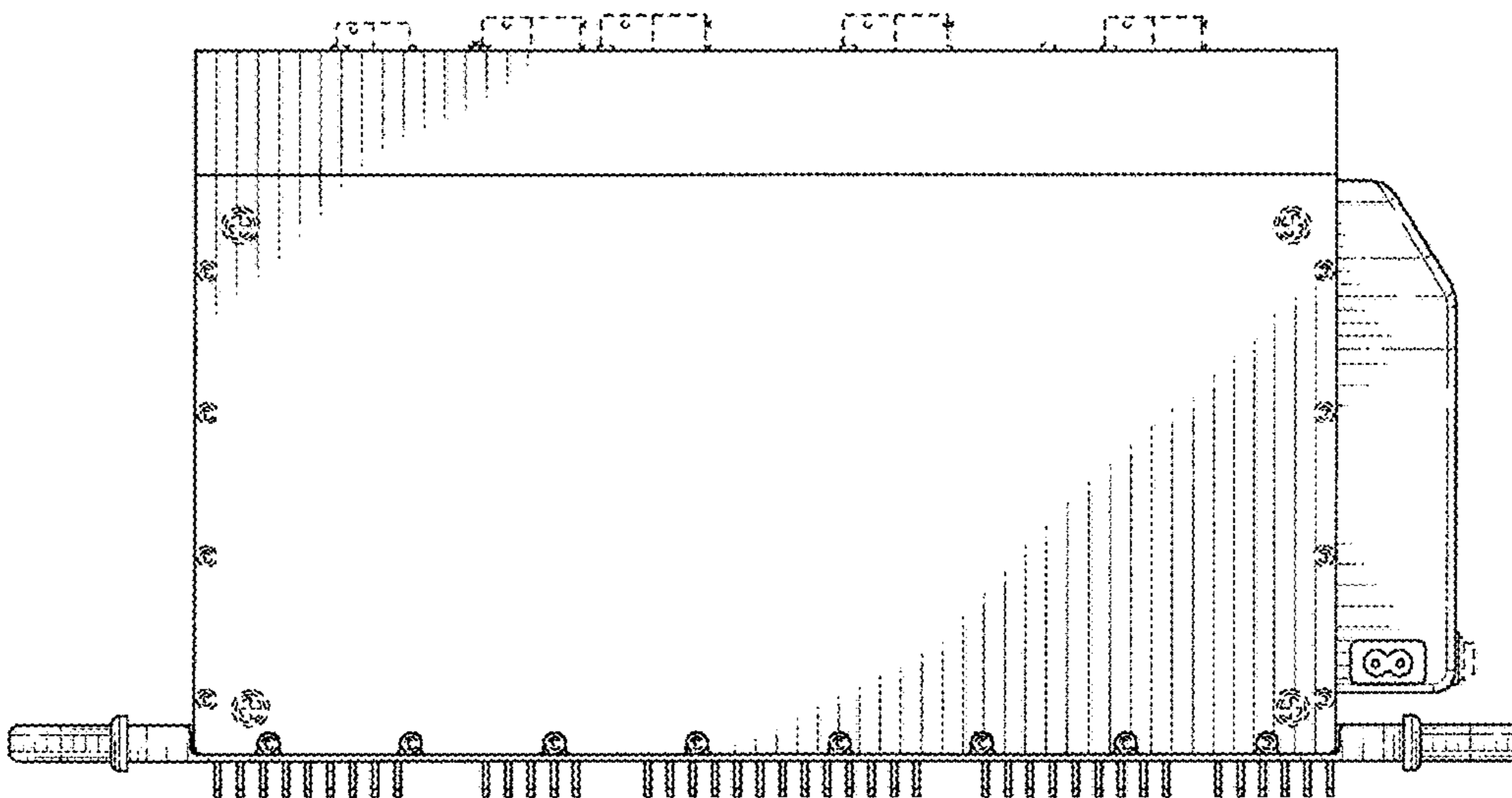


FIG. 7

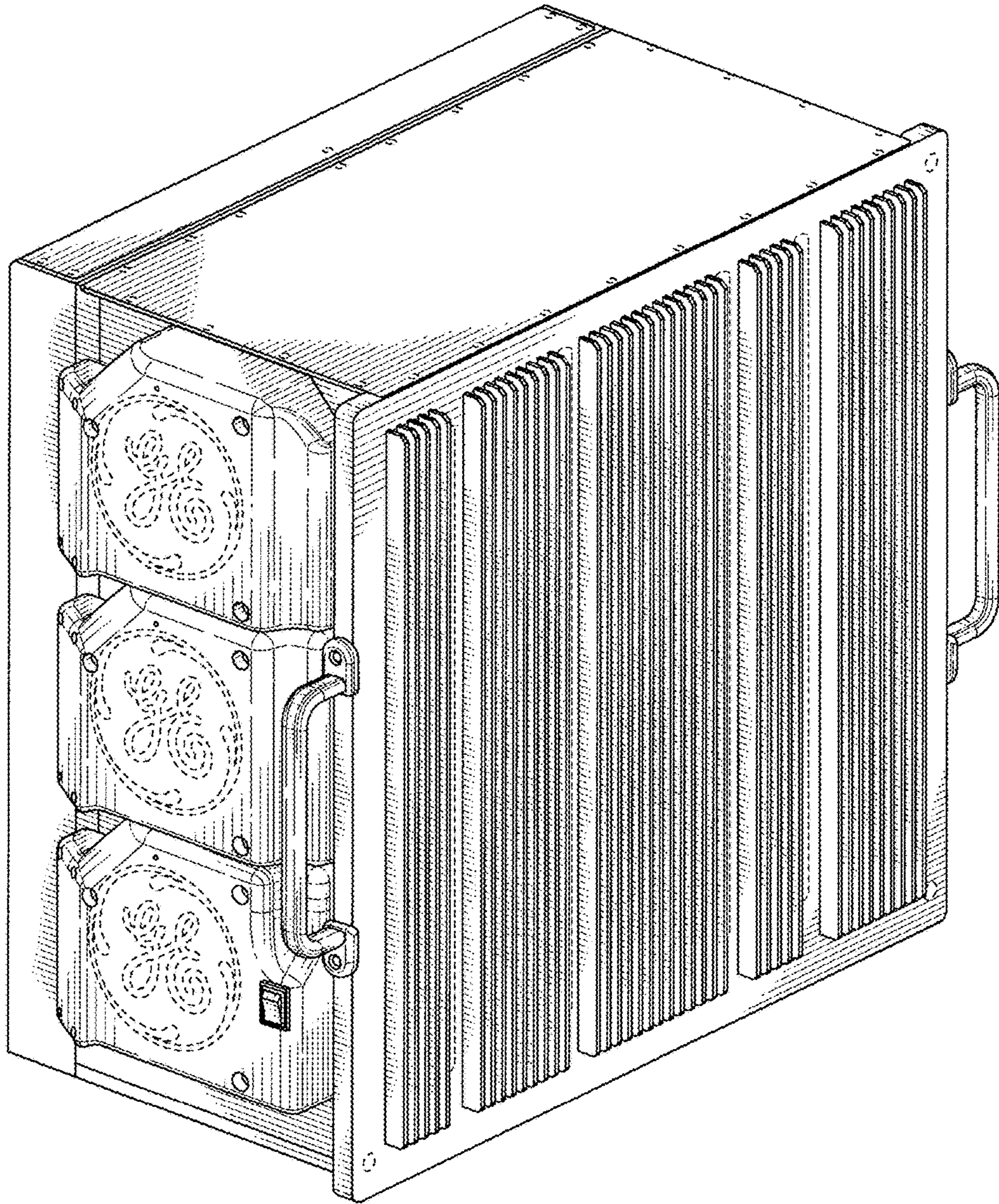


FIG. 8