



US00D842305S

(12) **United States Design Patent** (10) **Patent No.:** **US D842,305 S**
Gao (45) **Date of Patent:** **** Mar. 5, 2019**

(54) **STAND FOR ELECTRONIC DEVICES**

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(**) Term: **15 Years**

(21) Appl. No.: **29/634,199**

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(52) **U.S. Cl.**
USPC **D14/447**

(58) **Field of Classification Search**
USPC D14/447, 432, 434, 439, 440, 451, 452, D14/457, 239; D8/363, 373, 380; D6/406.3, 406.4, 406.5, 406.6; D12/415
CPC A47B 21/04; A47B 2097/006; A47B 2097/005; A47B 2023/049; A45C 2011/002; A45C 2011/003; F16M 2200/00; F16M 13/00
See application file for complete search history.

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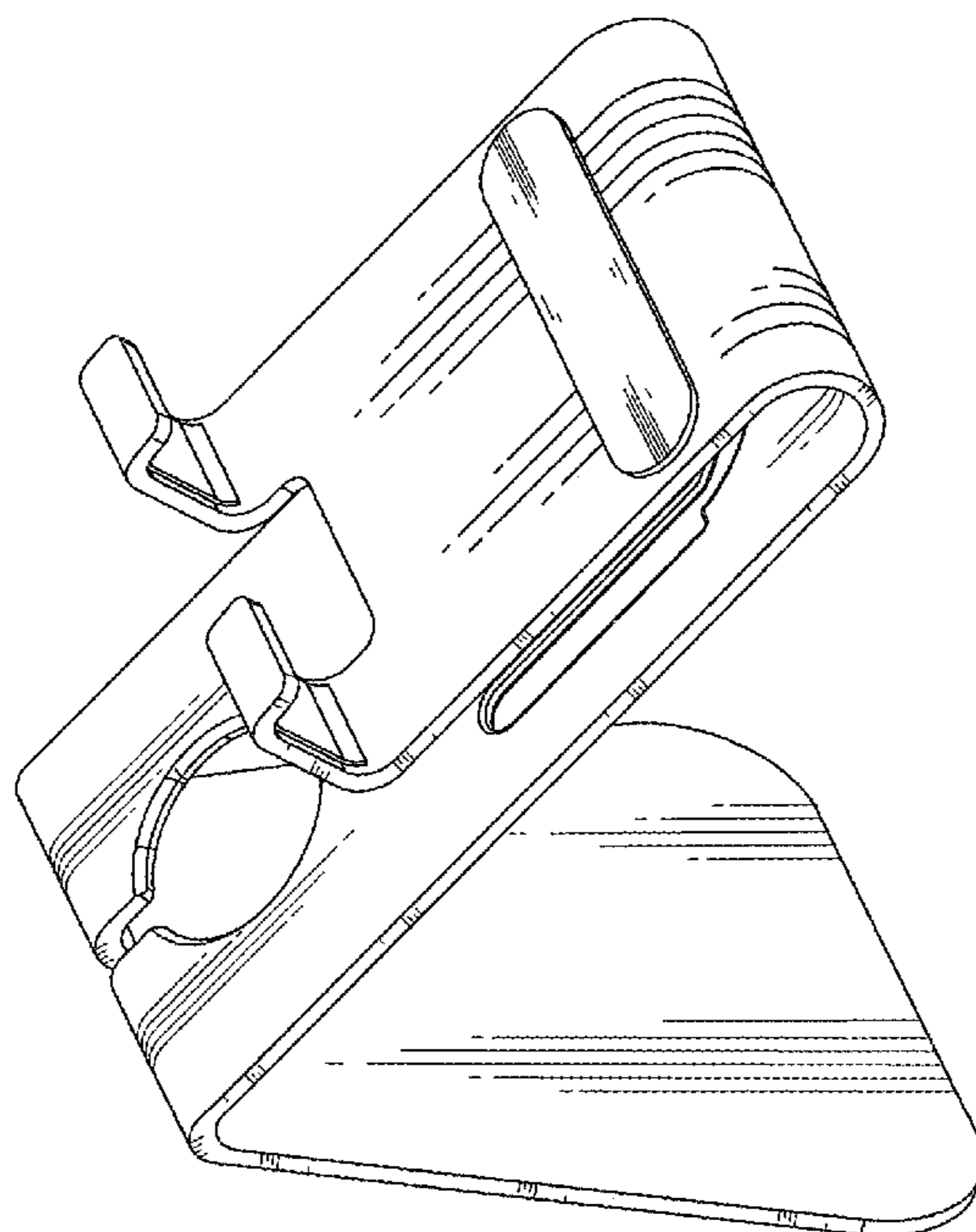
(57) **CLAIM**

The ornamental design for a stand for electronic devices, as shown and described.

DESCRIPTION

FIG. 1 is a front, right side, top perspective view of the stand for electronic devices showing my new design; FIG. 2 is a rear, right side, bottom perspective view thereof; FIG. 3 is a front elevational view thereof; FIG. 4 is a rear elevational view thereof; FIG. 5 is a left side elevational view thereof; FIG. 6 is a right side elevational view thereof; FIG. 7 is a top plan view thereof; and, FIG. 8 is a bottom plan view thereof.

1 Claim, 8 Drawing Sheets



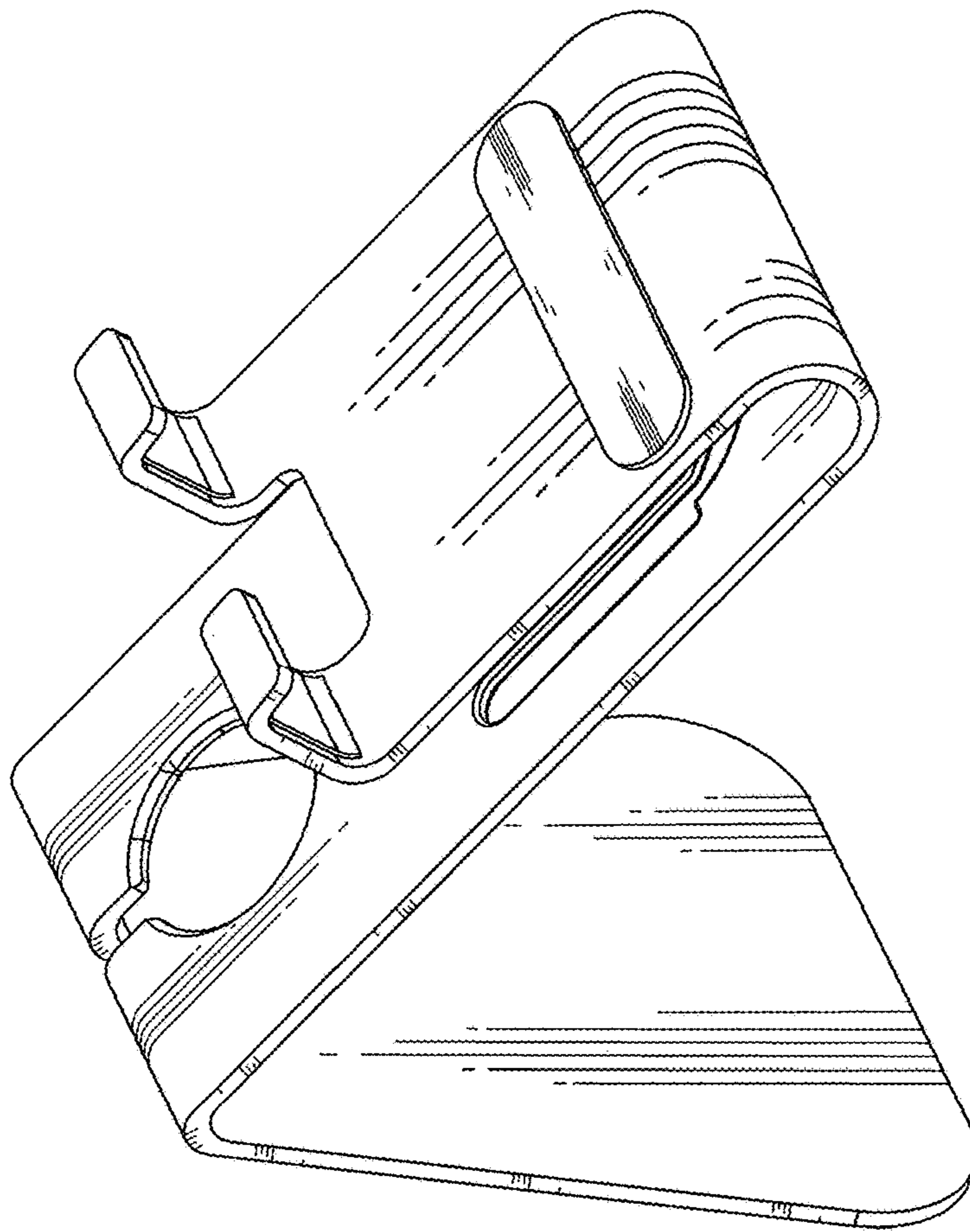


FIG. 1

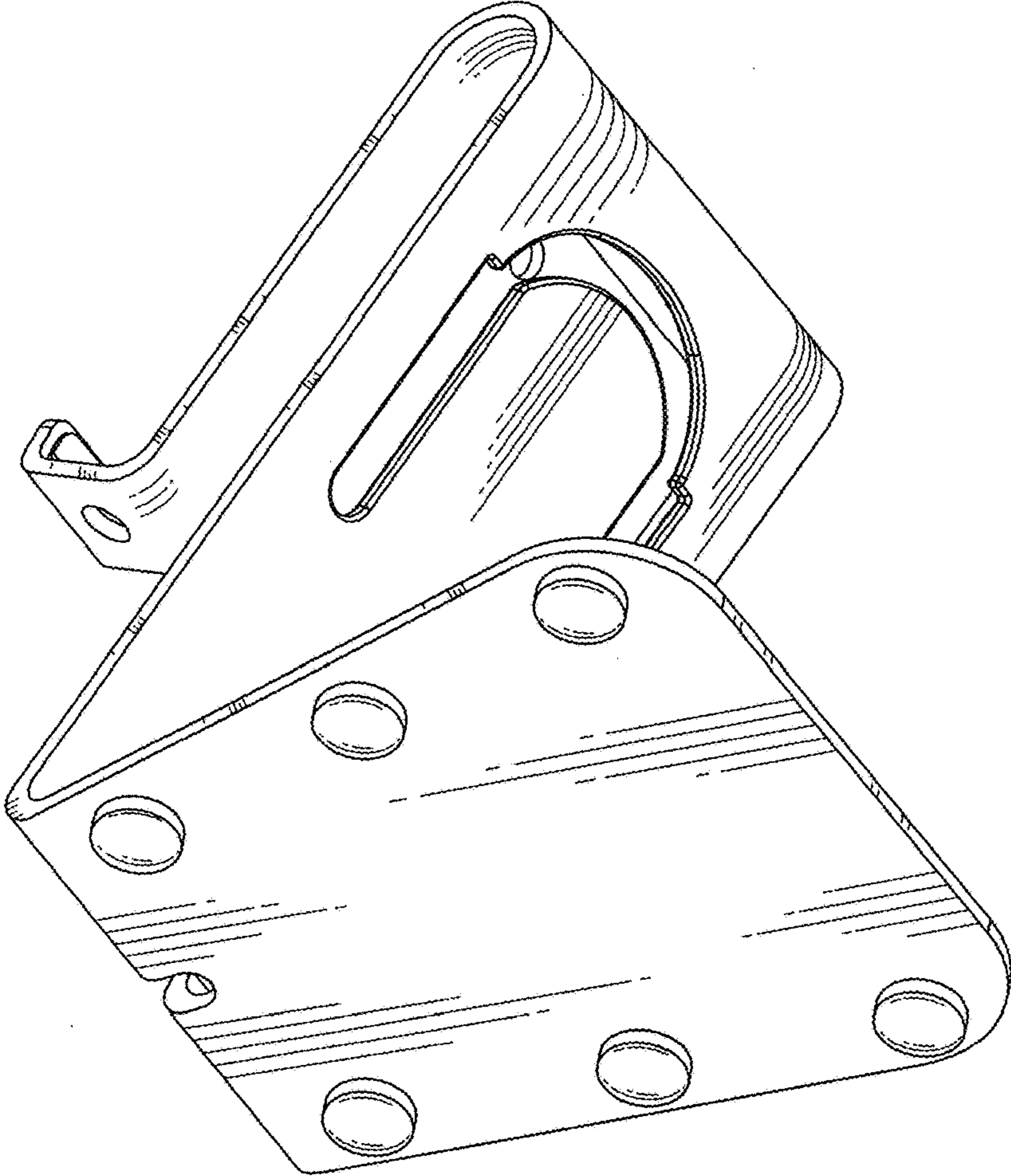


FIG. 2

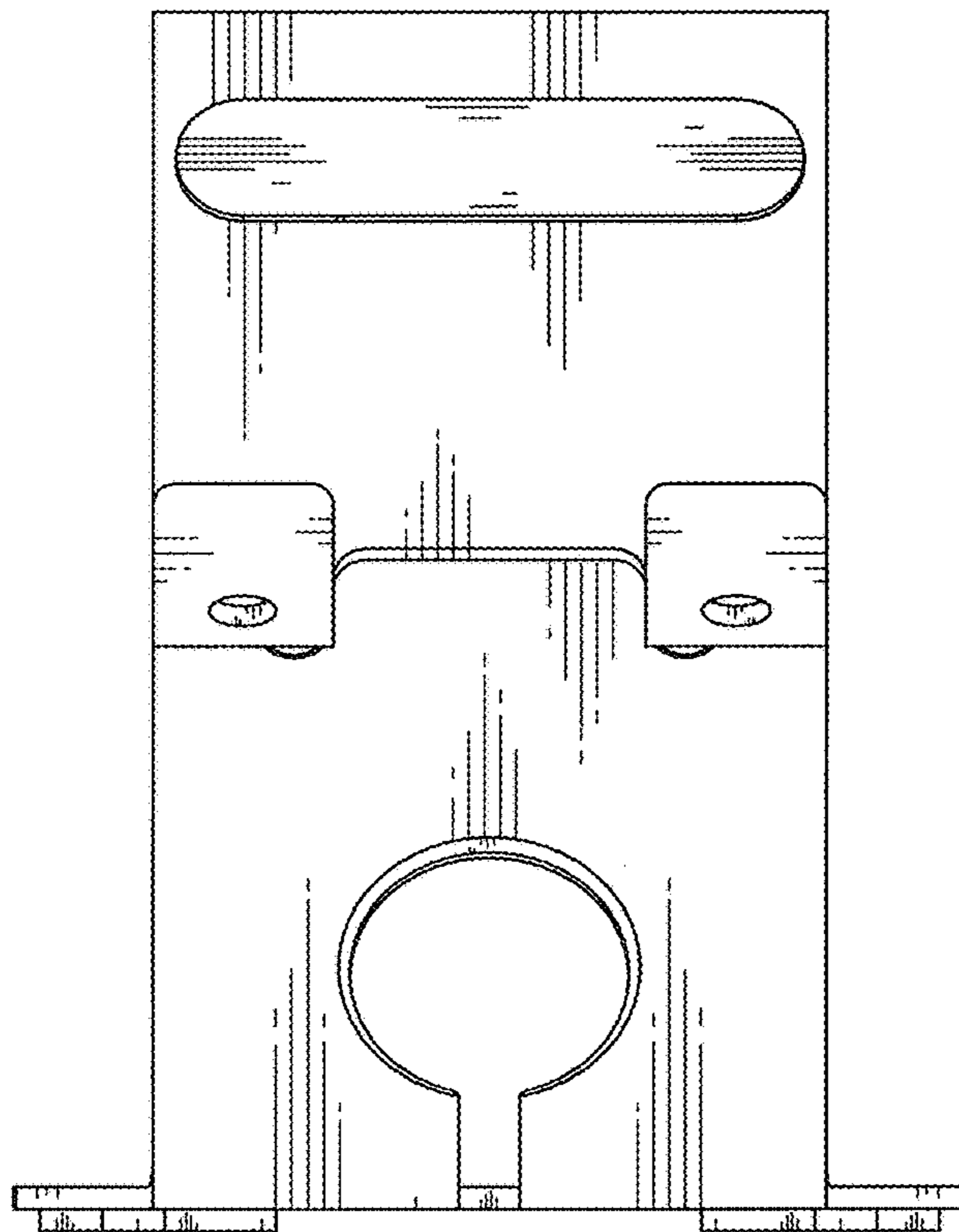


FIG. 3

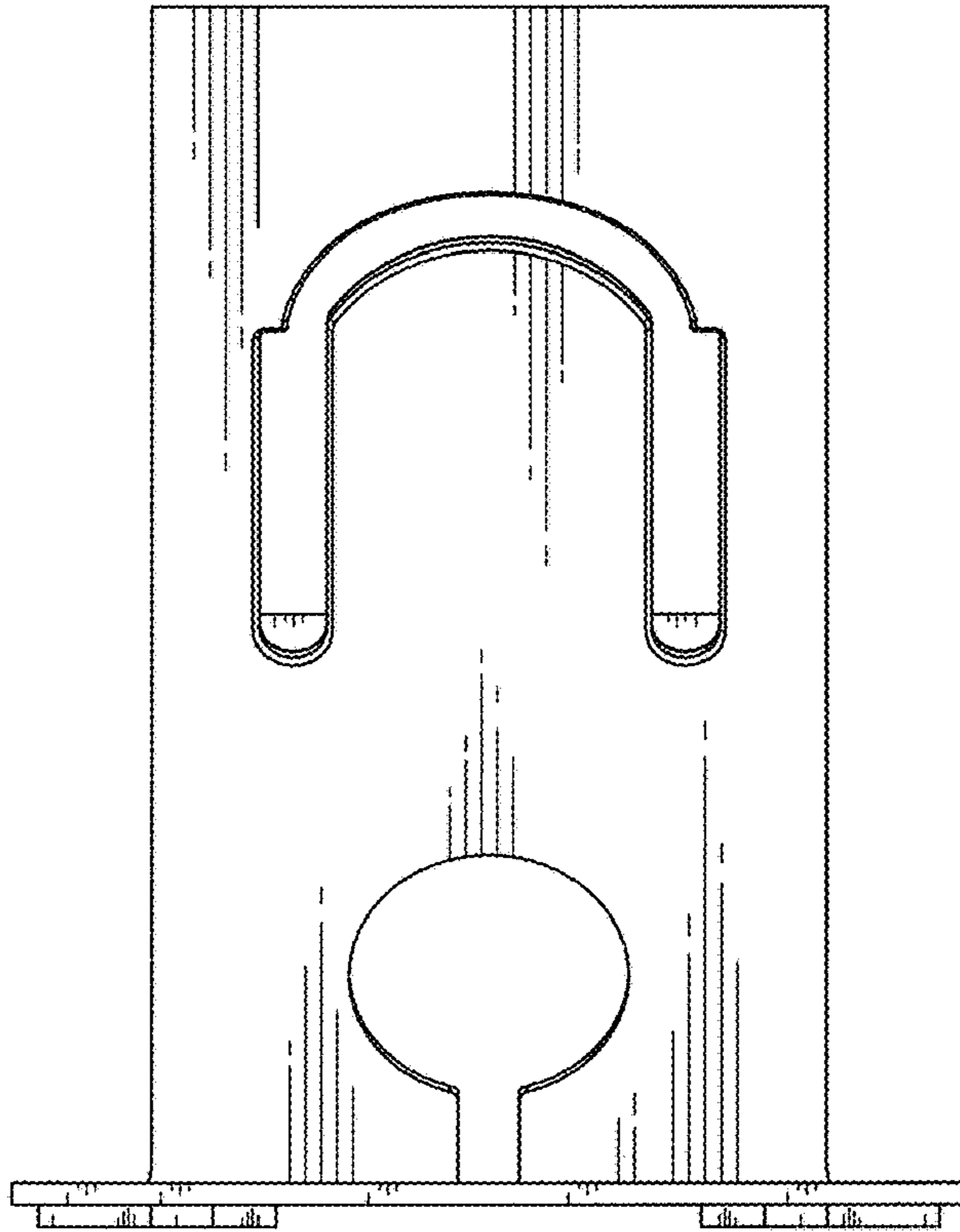


FIG. 4

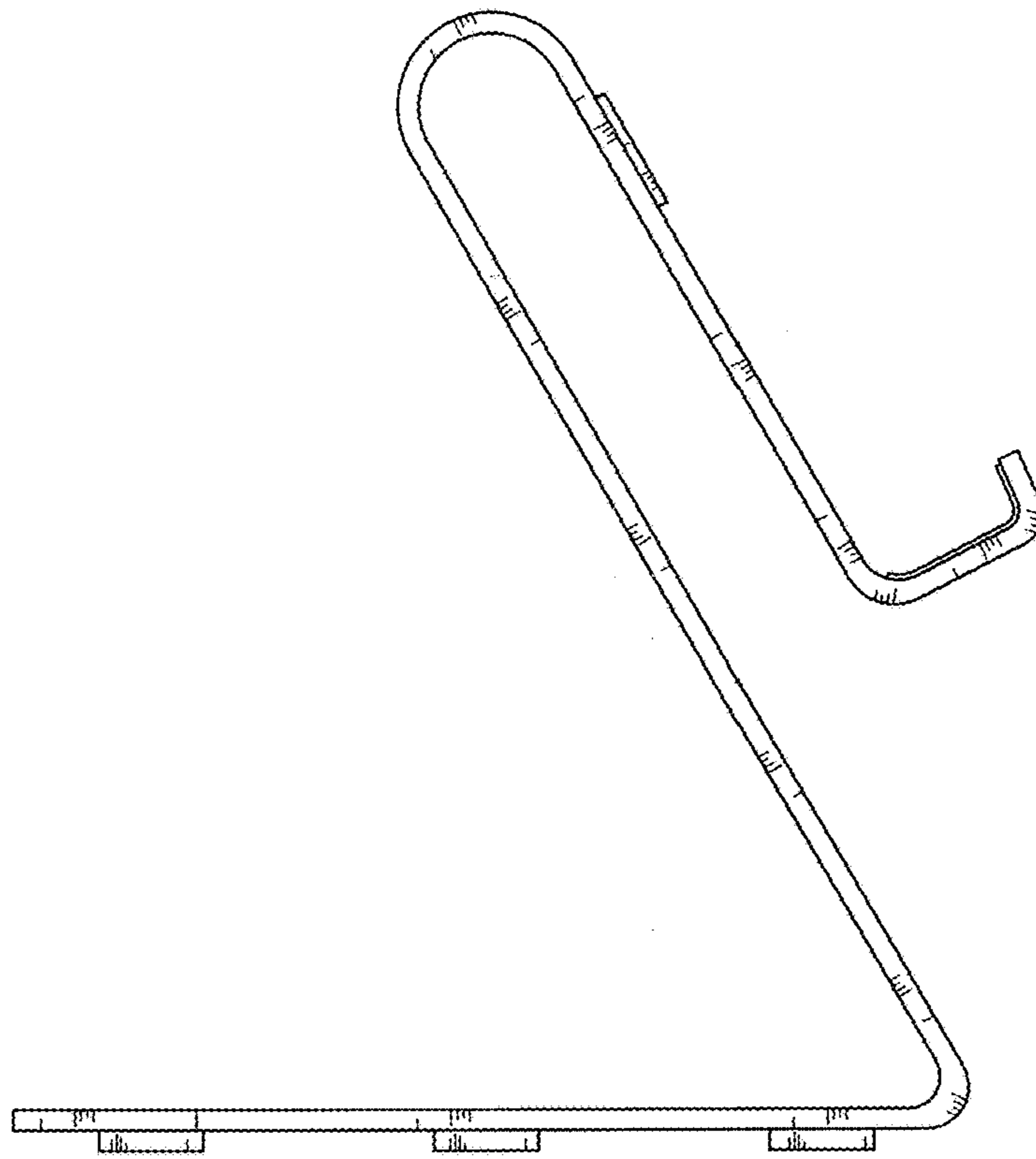


FIG. 5

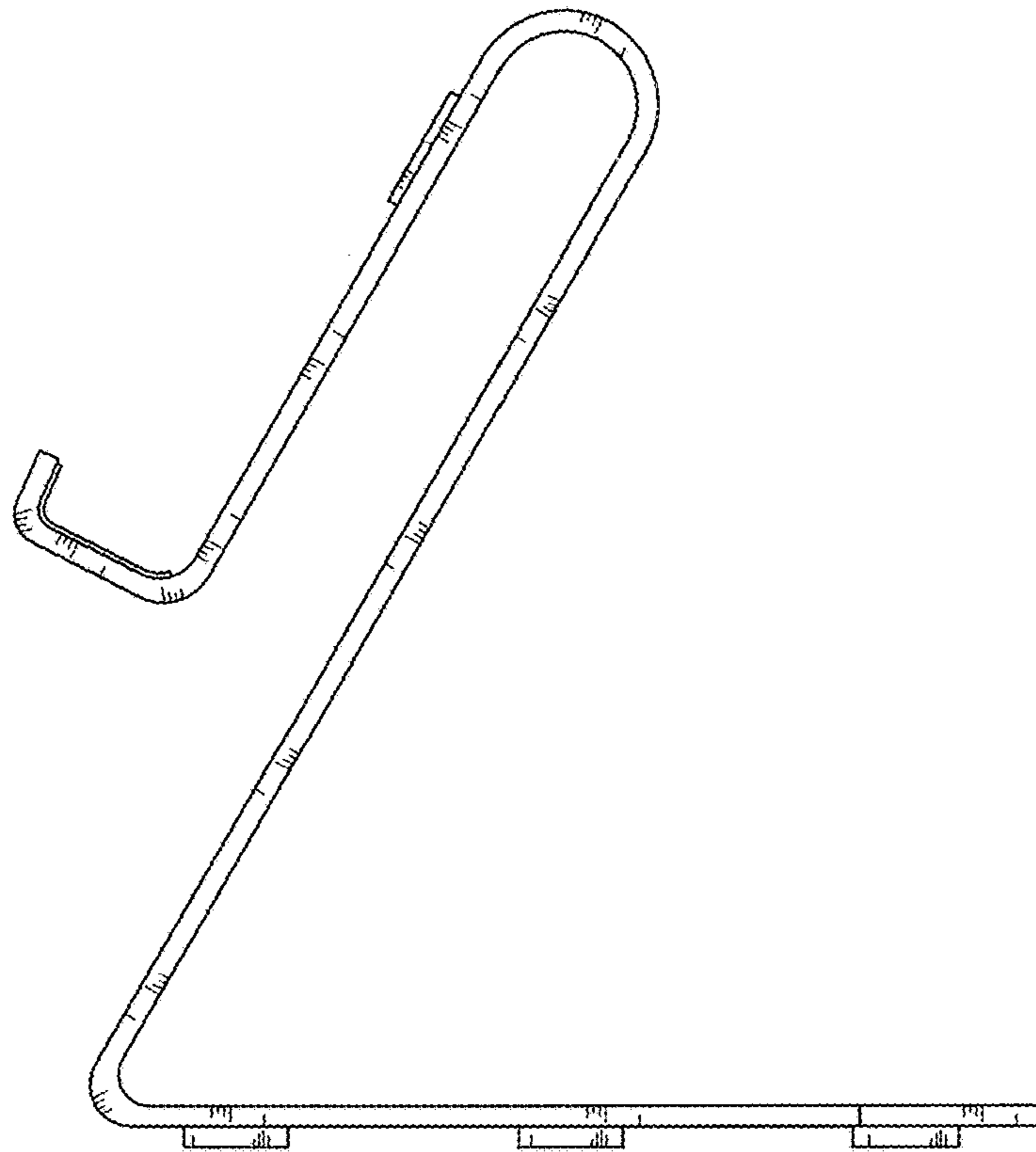


FIG. 6

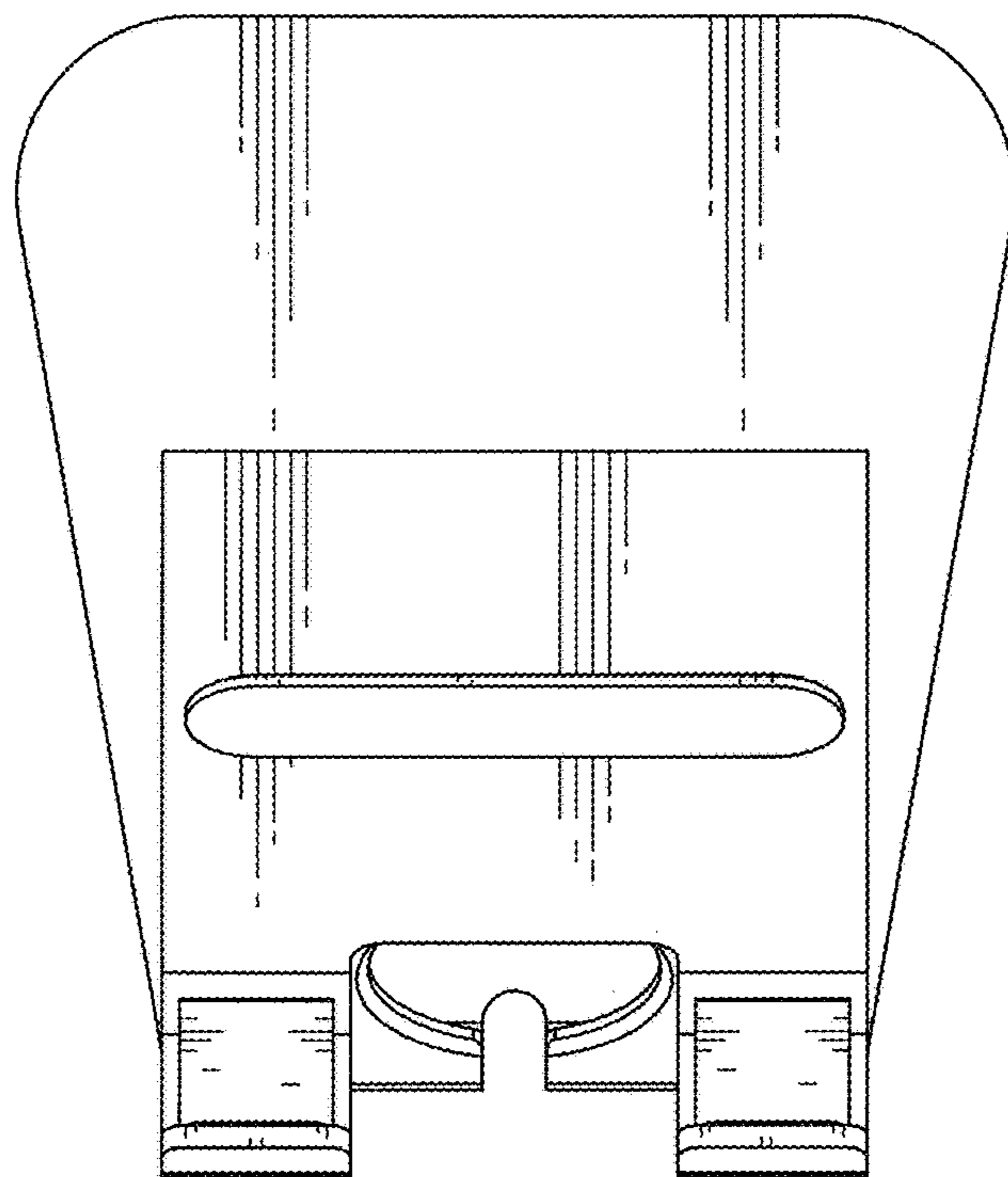


FIG. 7

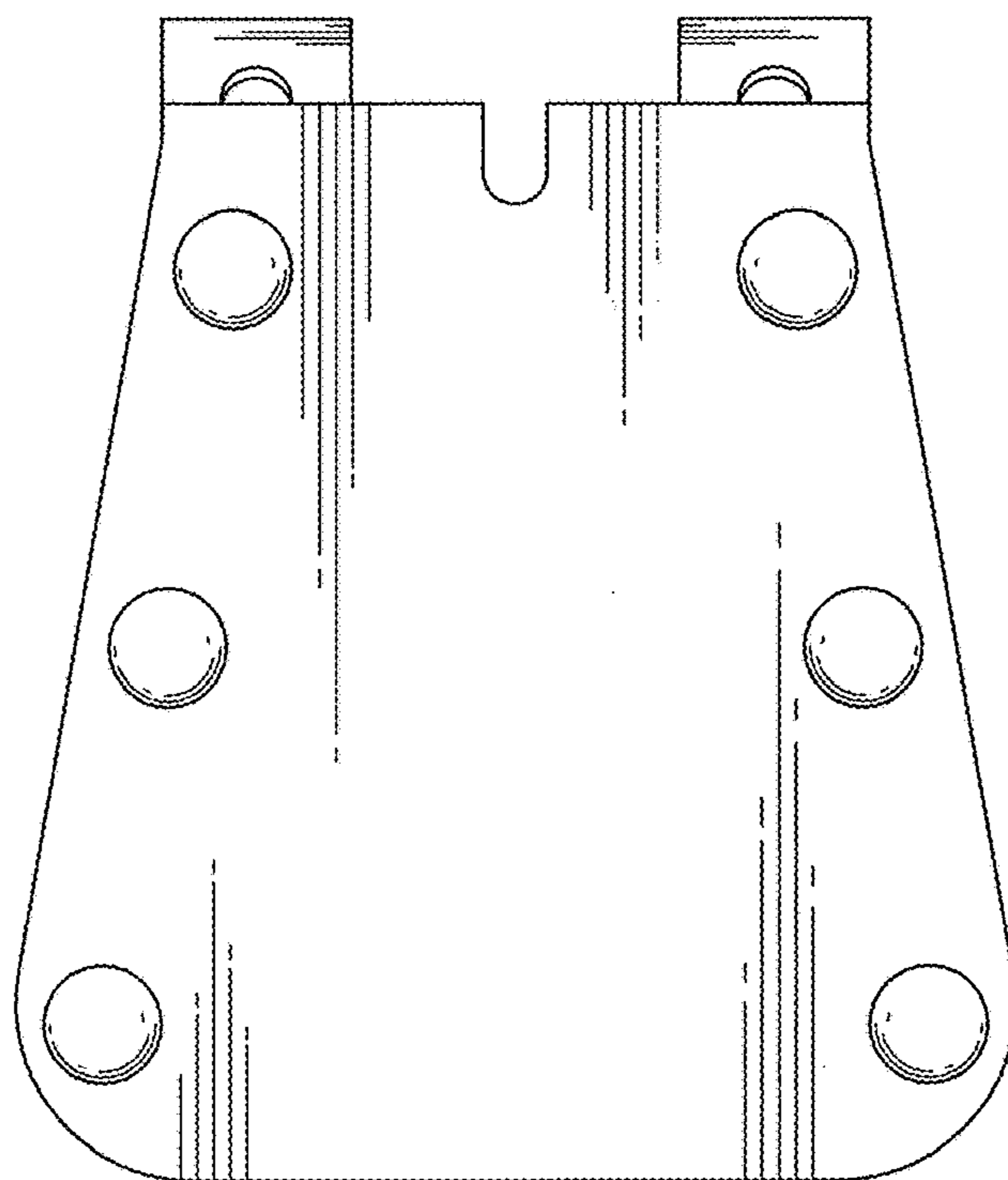


FIG. 8