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(12) **United States Design Patent** (10) **Patent No.:** **US D841,632 S**
Eun et al. (45) **Date of Patent:** **** Feb. 26, 2019**

(54) **GATEWAY TERMINAL**

(56) **References Cited**

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(73) Assignee: **LG ELECTRONICS INC.**, Seoul (KR)
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Int. Reg. Pub. Date: **May 19, 2017**

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(74) *Attorney, Agent, or Firm* — Birch, Stewart, Kolasch & Birch, LLP

(57) **CLAIM**

The ornamental design for a gateway terminal, as shown and described.

(30) **Foreign Application Priority Data**

May 25, 2016 (KR) 30-2016-0025190

(51) **LOC (11) Cl.** **14-03**

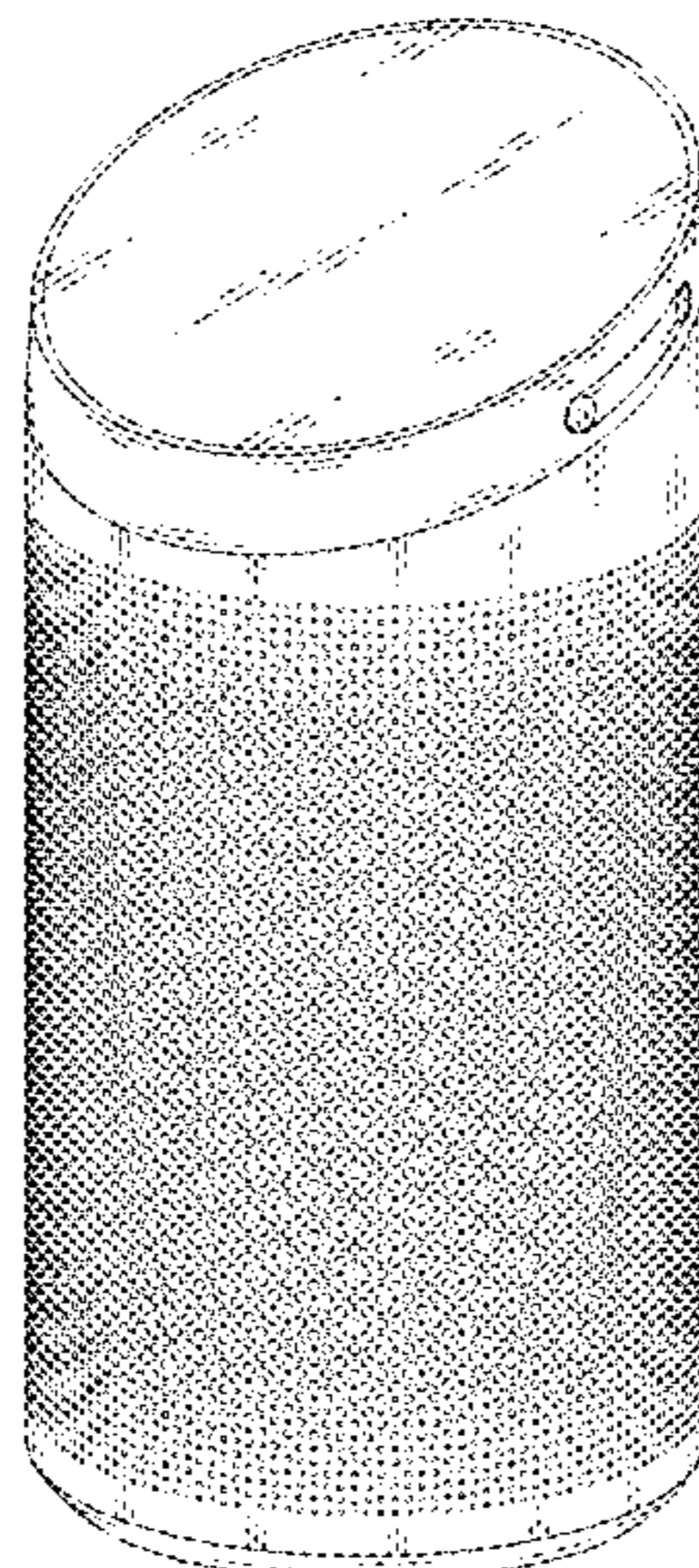
(52) **U.S. Cl.**
USPC **D14/242**
CPC **A63F 13/00** (2013.01)

(58) **Field of Classification Search**
USPC ... D14/137, 138, 160, 168, 20.1–203.8, 217, D14/401, 435, 474, 483, 496, 356, 507; 345/156, 169, 173–179, 905; 715/727–729, 864; 710/1, 5, 8; 713/1, 713/600; 455/1.1, 1.7, 73, 344–347, 93, 455/95, 3.01–3.06, 550.1, 573.1; 370/342–344; 369/1, 2, 6–12; 463/43–47
CPC ... A63F 13/00; A63F 11/00; A63F 2300/8047
See application file for complete search history.

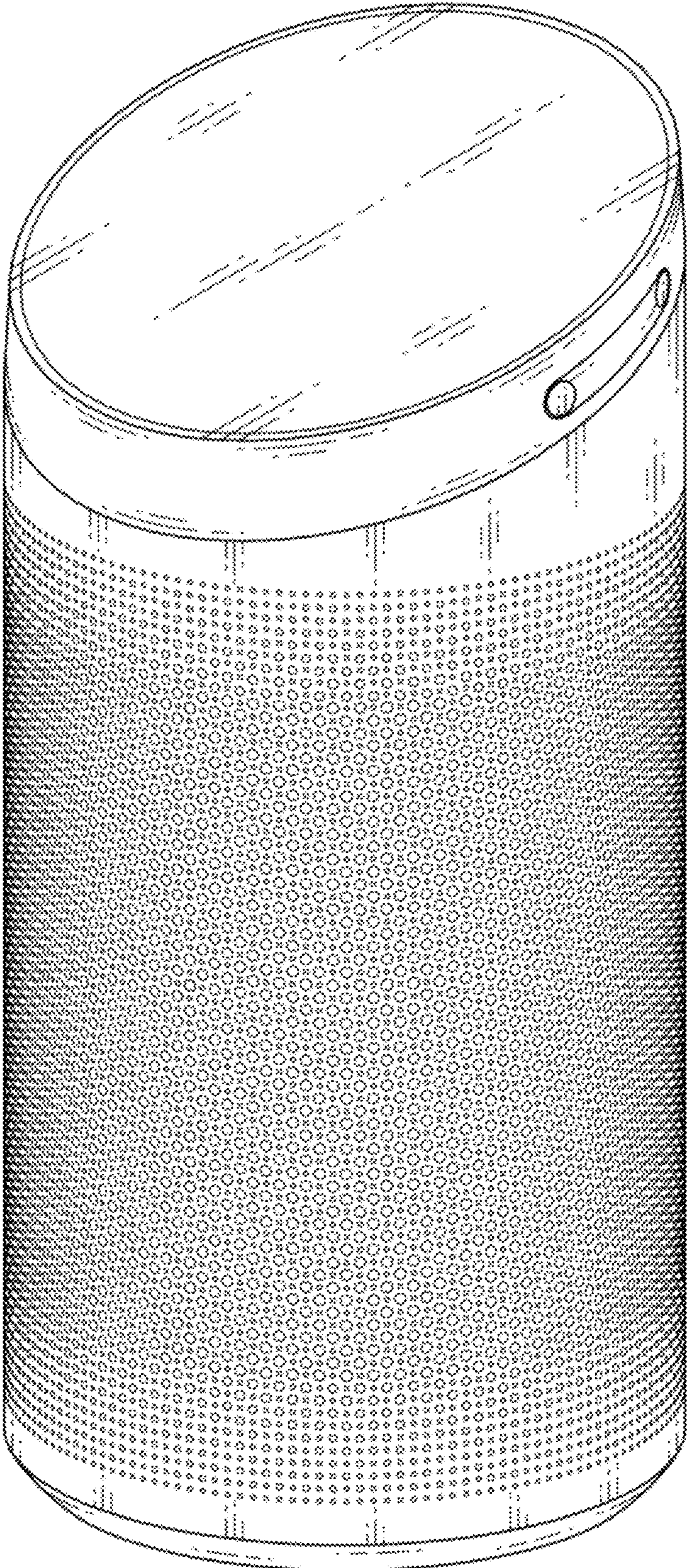
DESCRIPTION

1. Gateway terminal
1.1 is a perspective view of a gateway terminal, showing our new design;
1.2 is a front elevation view thereof;
1.3 is a rear elevation view thereof;
1.4 is a left-side elevation view thereof;
1.5 is a right-side elevation view thereof;
1.6 is a top plan view thereof;
1.7 is a bottom plan view thereof; and
1.8 is a perspective view thereof, in a use condition.
The broken lines shown in Figure 1.8 are for the purpose of illustrating portions of the gateway terminal that form no part of the claimed design.

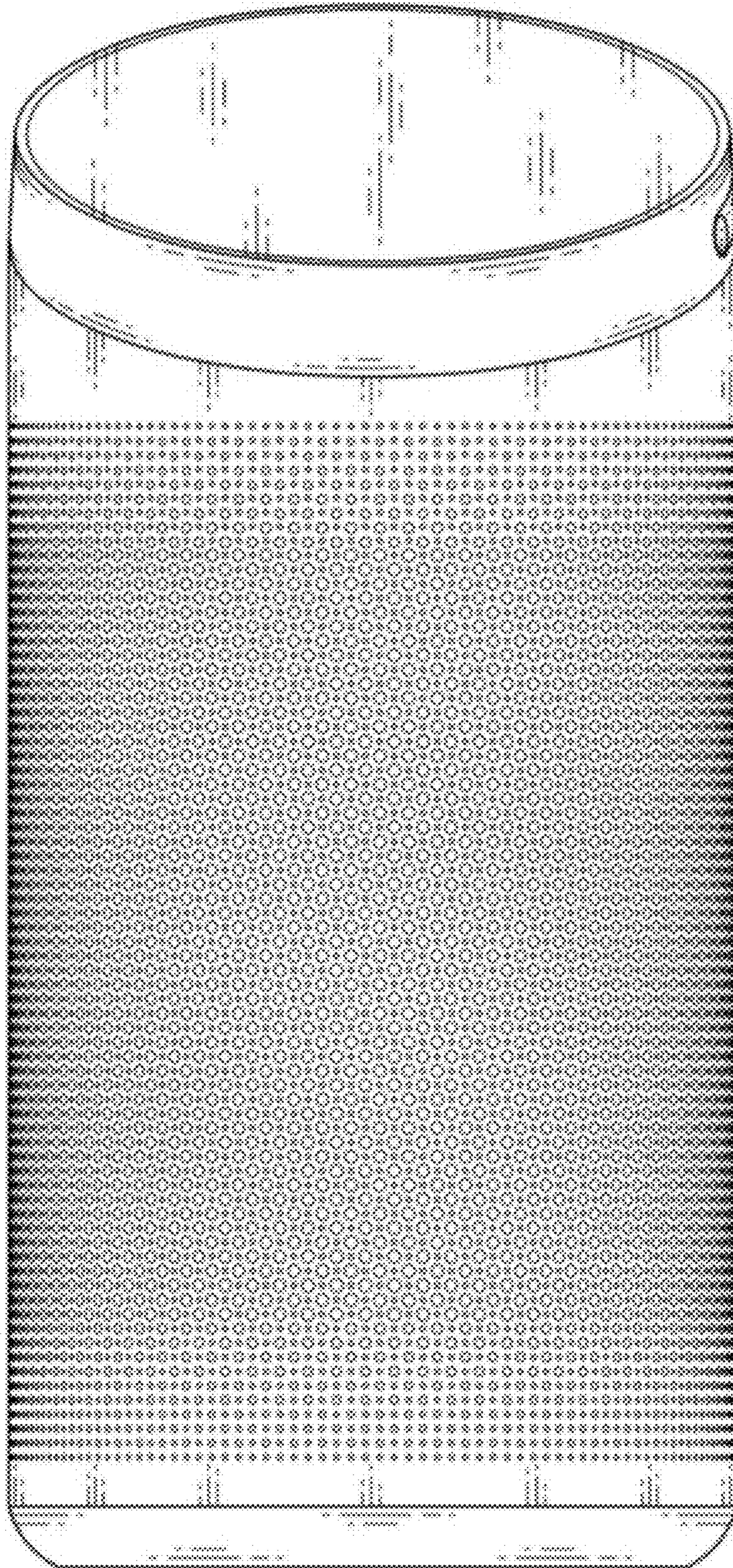
1 Claim, 8 Drawing Sheets



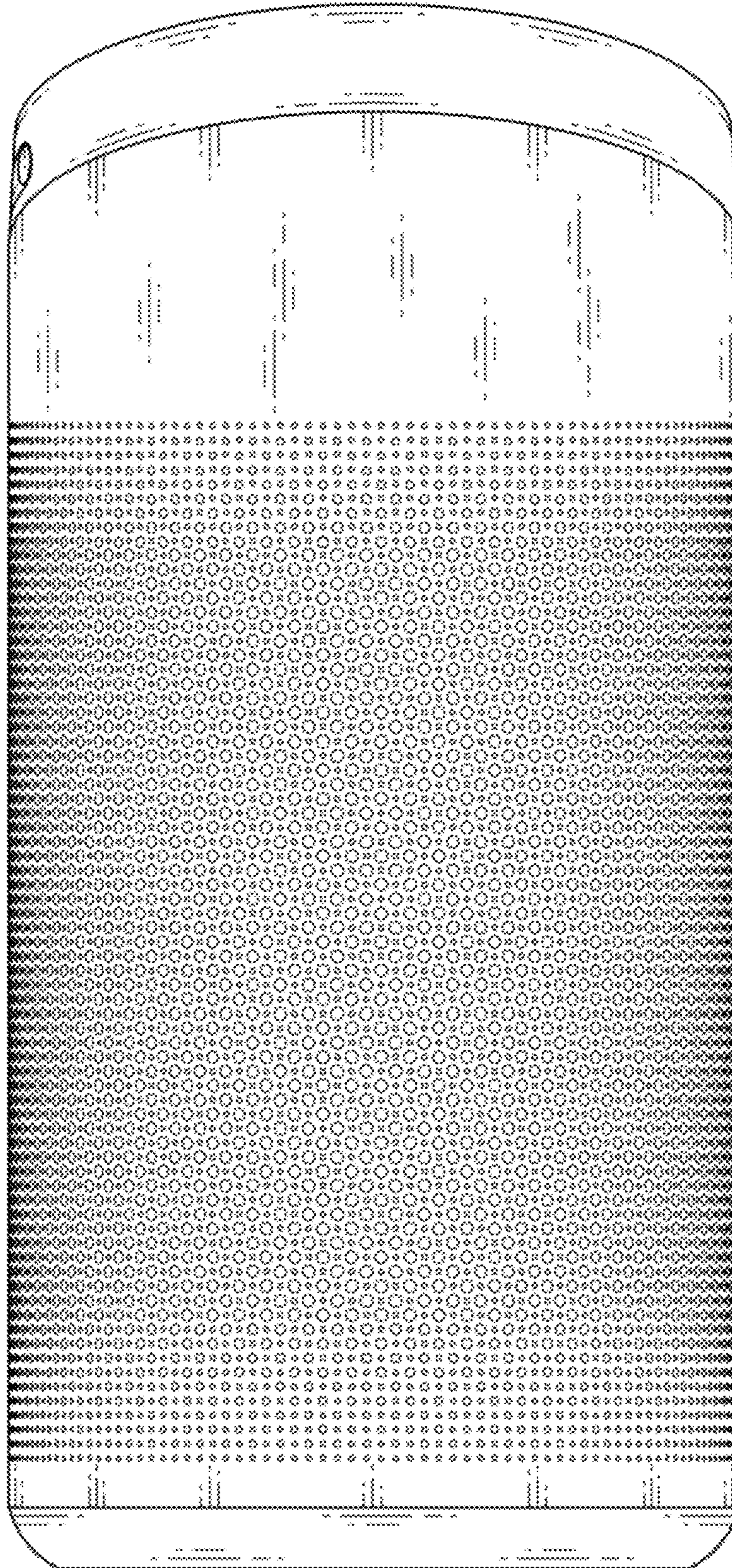
1.1



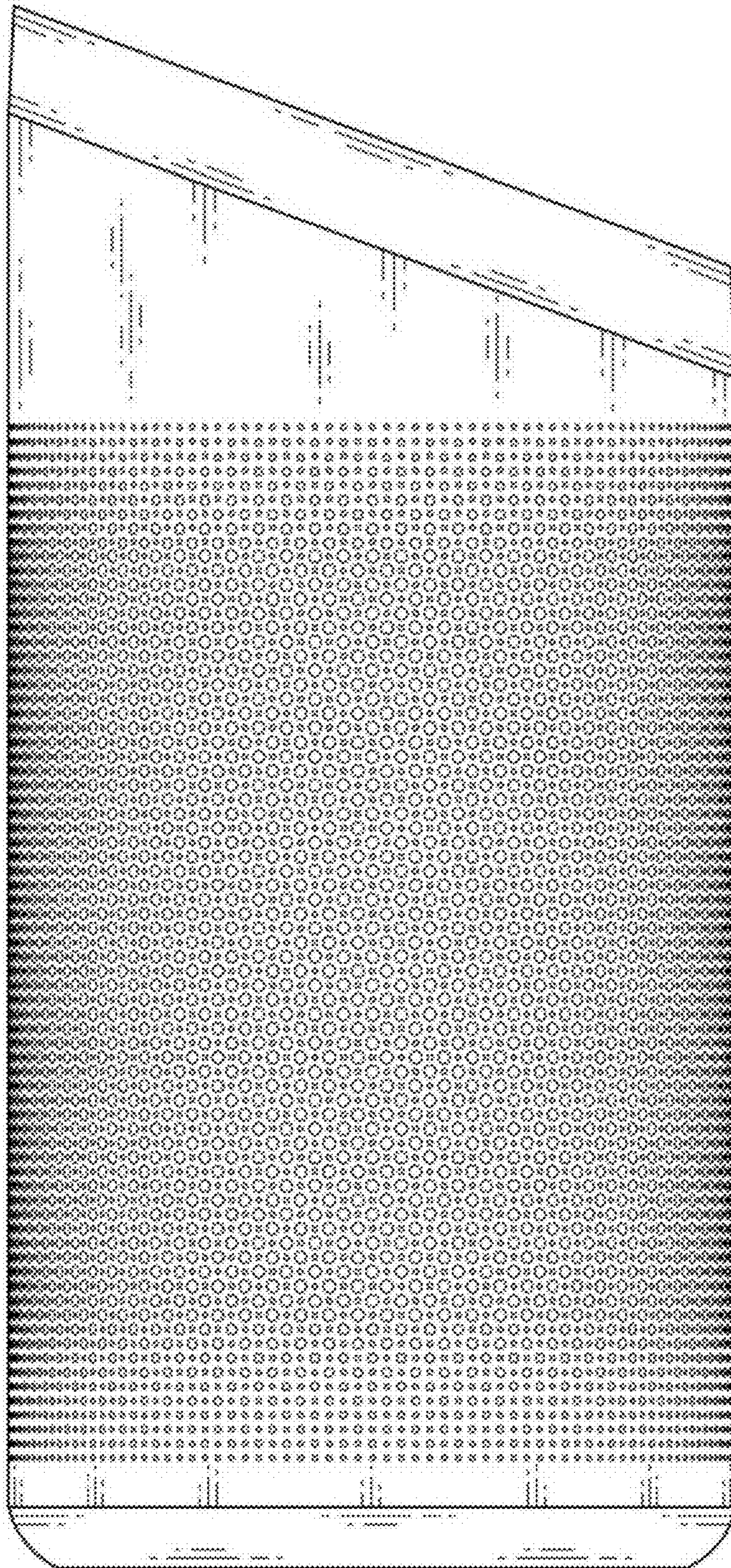
1.2



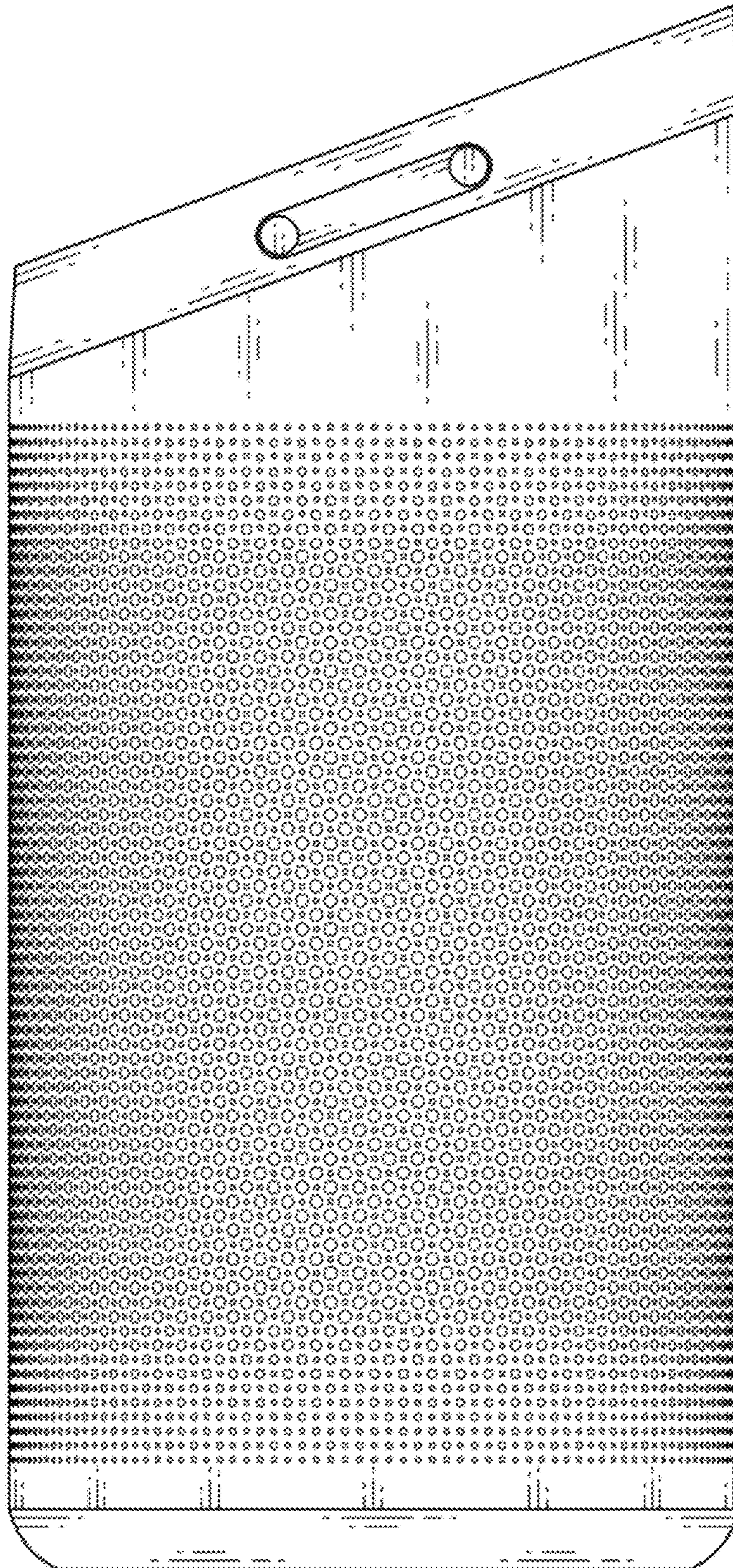
1.3



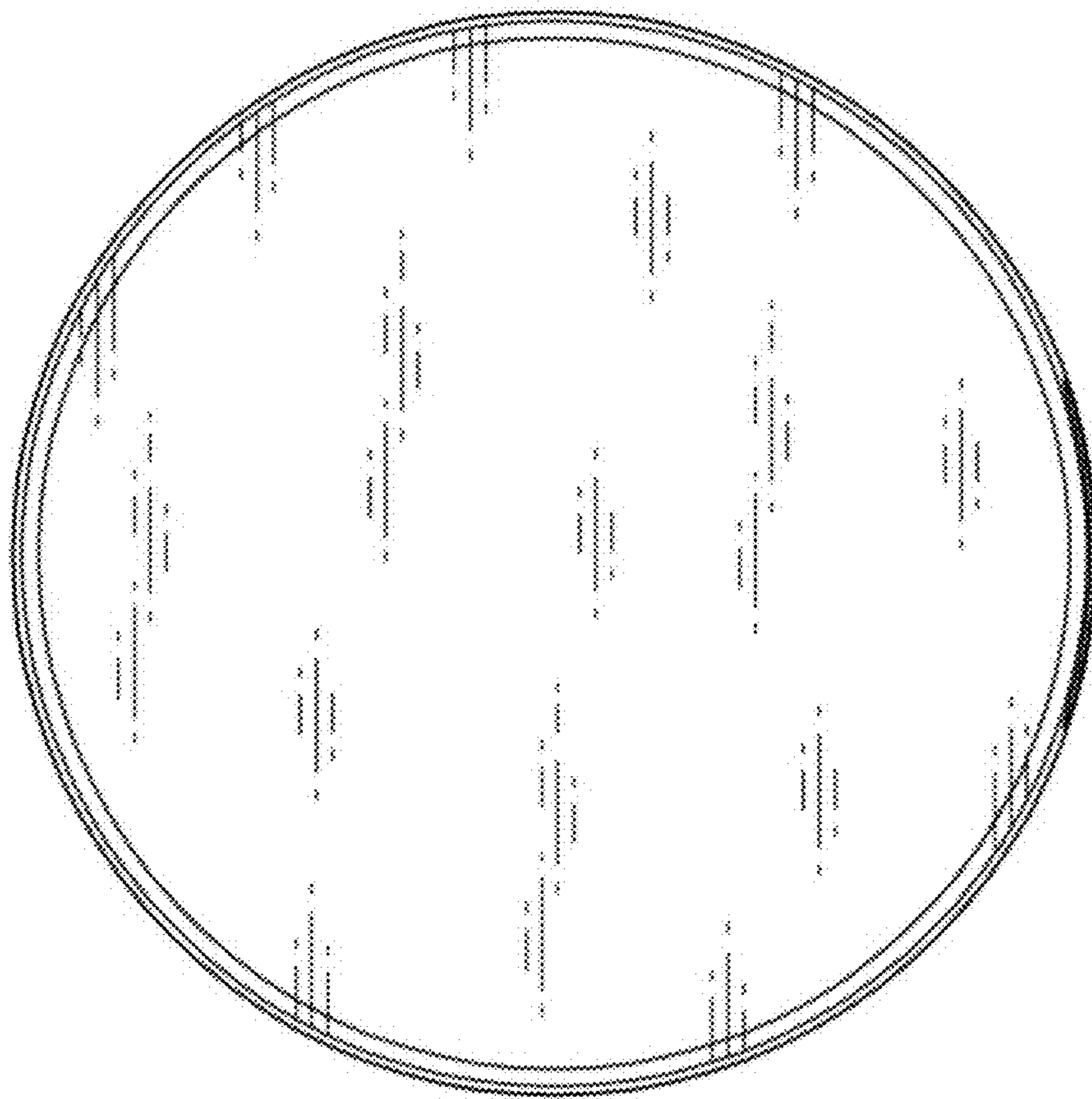
1.4



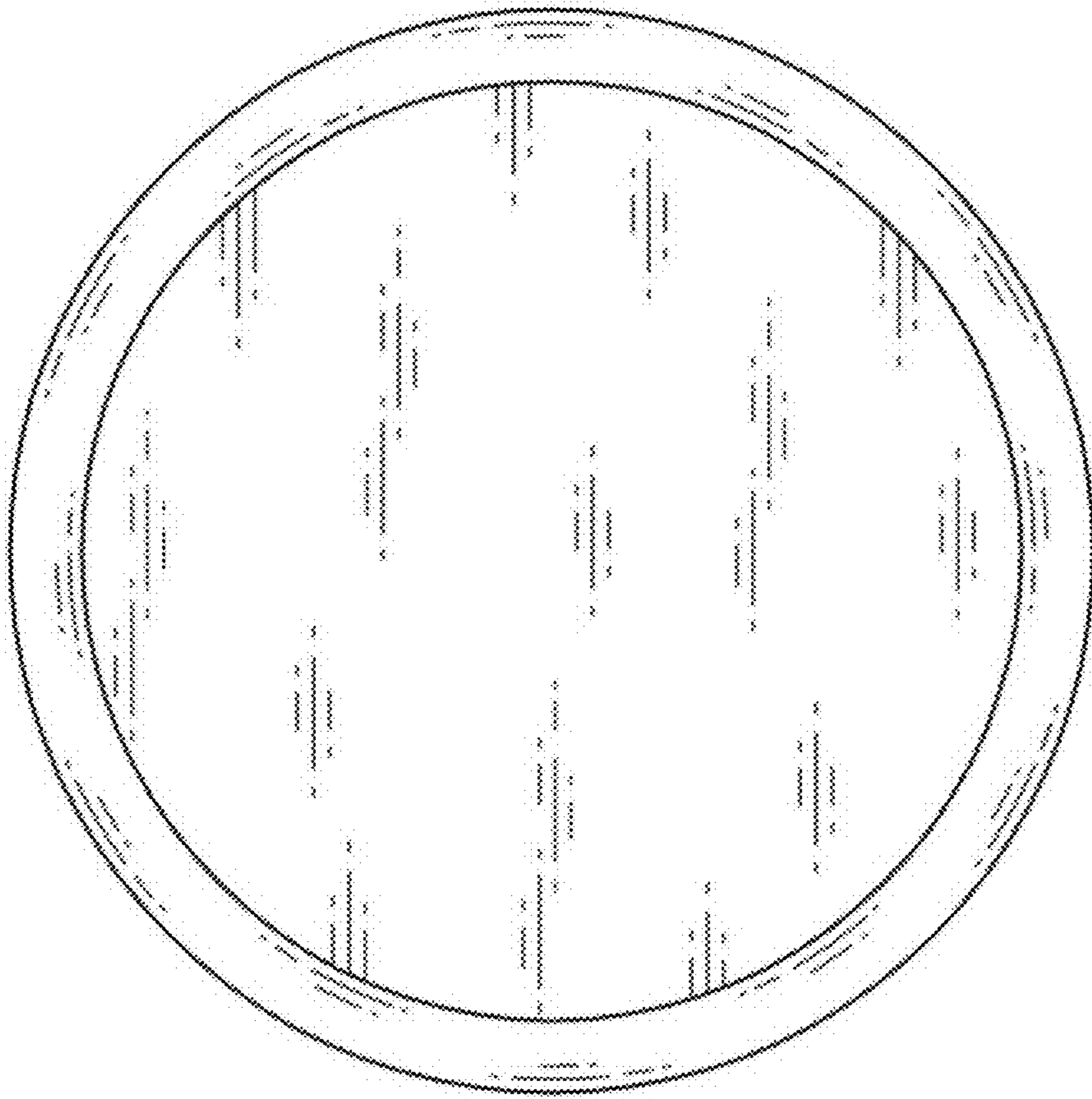
1.5



1.6



1.7



1.8

