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(12) **United States Design Patent**
Satzger et al.

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(54) **COMPUTING DEVICE**

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(73) Assignee: **Intel Corporation**, Santa Clara, CA (US)

(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **14 Years**

(21) Appl. No.: **29/513,137**

(22) Filed: **Dec. 27, 2014**

(51) **LOC (11) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/341**

(58) **Field of Classification Search**
USPC ..... D14/341–347, 137, 138 R, 138 AA, D14/138 C, 138 G, 496, 203.1, 203.3, D14/203.4, 203.7, 129, 130, 147, 218, D14/248, 389, 388, 426, 420; D10/65, D10/104.1; D18/6, 7; D21/324, 329, D21/330; 455/556.1, 556.2, 566, 575.1, 455/90.3; 379/433.04, 433.01, 433.06, 379/916; 345/173, 901, 905; 361/679.26, 679.3, 679.55, 679.56
CPC H04M 1/00; H04M 11/066; H04M 1/2478; H04M 1/72519; H04M 2201/38; H04M 2250/22; G06F 15/00
See application file for complete search history.

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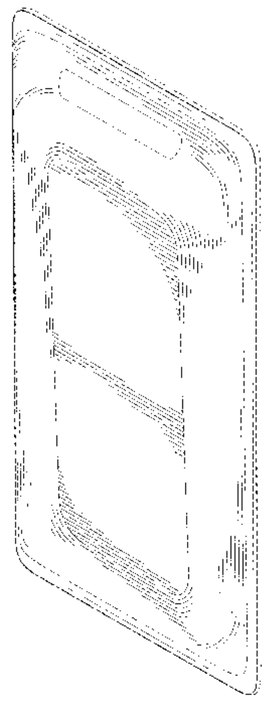
(57) **CLAIM**

The ornamental design for a computing device, as shown and described.

DESCRIPTION

FIG. **1** is a perspective view illustrating a computing device according to one embodiment;
FIG. **2** is a back view thereof;
FIG. **3** is a front view thereof;
FIG. **4** is a side view thereof;
FIG. **5** is another side view thereof;
FIG. **6** is a top view thereof; and,
FIG. **7** is a bottom view thereof.
The evenly-spaced broken lines in FIGS. **1-7** illustrate portions of the computing device that form no part of the claimed design.

1 Claim, 5 Drawing Sheets



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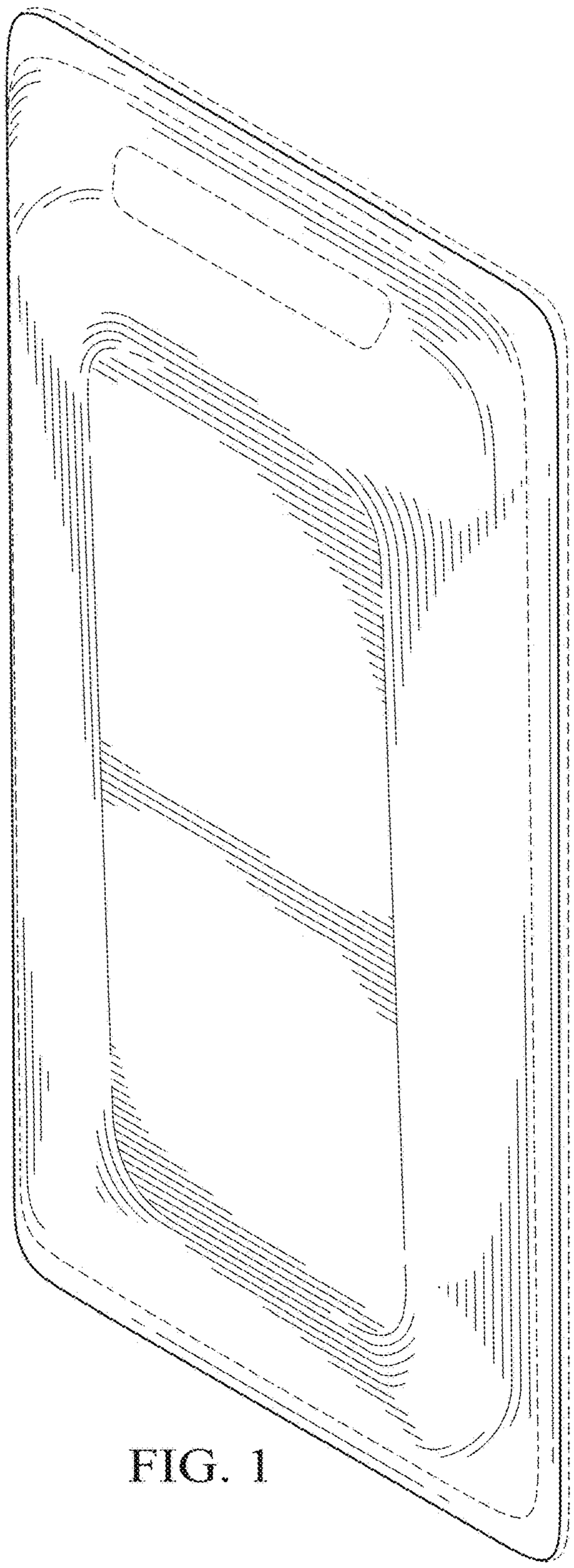

FIG. 1

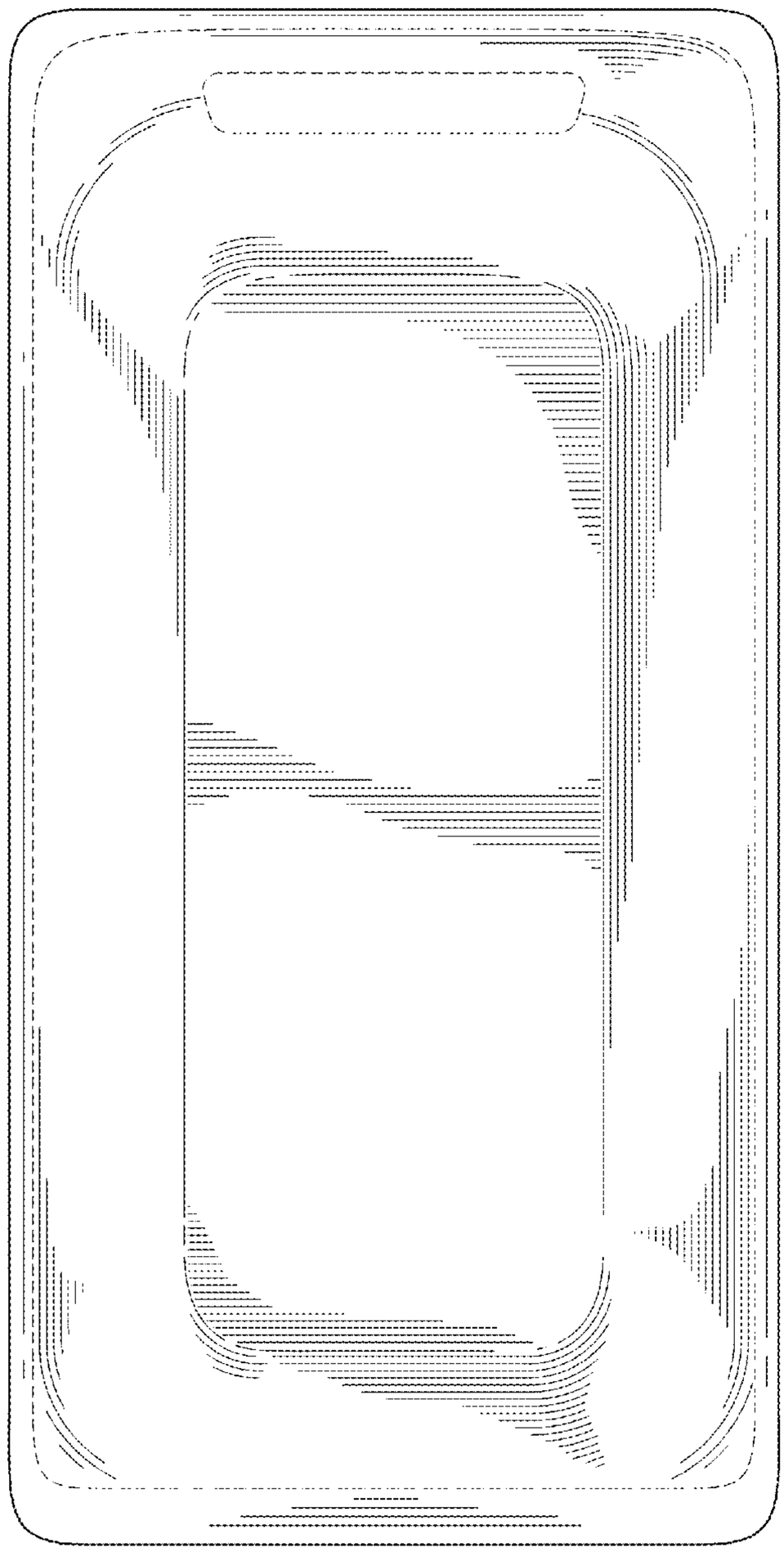

FIG. 2

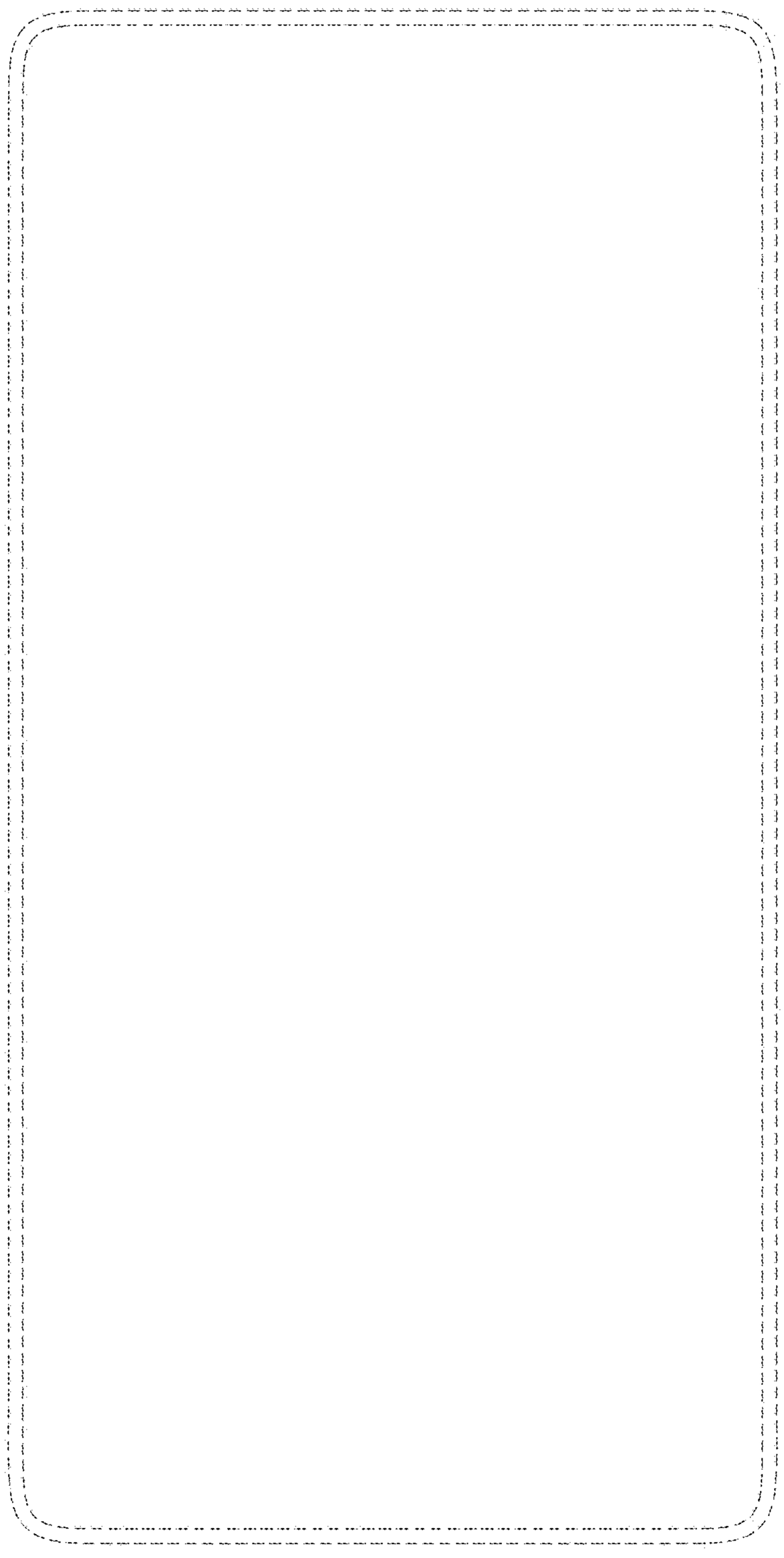

FIG. 3

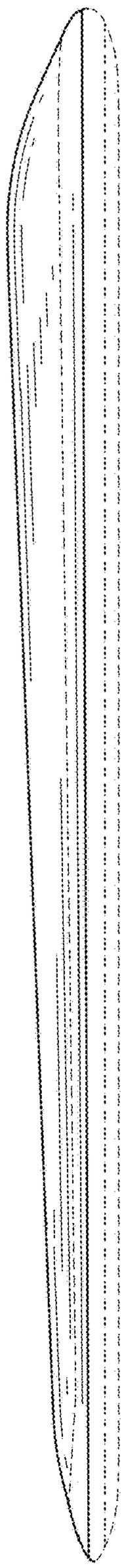

FIG. 4

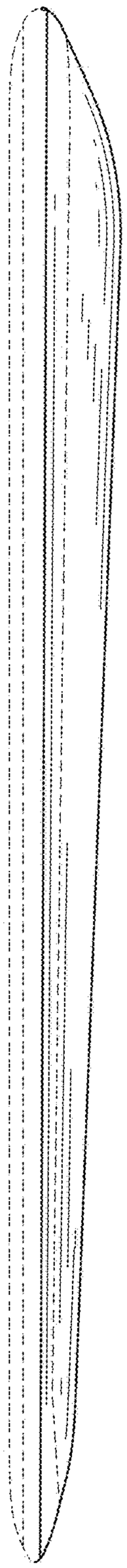

FIG. 5

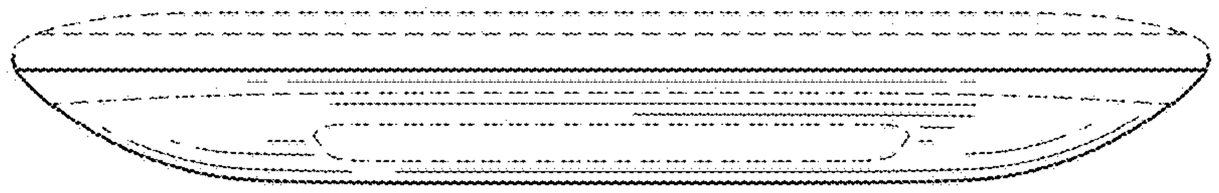

FIG. 6

FIG. 7