



US00D839197S

(12) **United States Design Patent** (10) **Patent No.:** **US D839,197 S**
Endo et al. (45) **Date of Patent:** **** Jan. 29, 2019**

(54) **ELECTRICAL CONNECTOR HOUSING**

(71) Applicant: **Dai-ichi Seiko Co., Ltd.**, Kyoto-shi, Kyoto (JP)
(72) Inventors: **Takayoshi Endo**, Shizuoka (JP); **Shunya Oohashi**, Shizuoka (JP)
(73) Assignee: **DAI-ICHI SEIKO CO., LTD.**, Kyoto (JP)

(**) Term: **15 Years**
(21) Appl. No.: **29/614,627**
(22) Filed: **Aug. 21, 2017**

(30) **Foreign Application Priority Data**

Jun. 13, 2017 (JP) 2017-012615
(51) **LOC (11) Cl.** **13-03**
(52) **U.S. Cl.**
USPC **D13/133; D13/147; D13/154**
(58) **Field of Classification Search**
USPC D13/110, 112, 118, 120, 122, 123, 133, D13/146, 147, 154, 173, 184, 199; D8/349, 382, 383, 277, 394, 395
(Continued)

(56) **References Cited**

U.S. PATENT DOCUMENTS

3,213,500 A * 10/1965 Thompson F16B 21/086 174/164
3,311,866 A * 3/1967 Williamson H01R 13/02 439/732

(Continued)

FOREIGN PATENT DOCUMENTS

JP 2000-067959 A 3/2000
JP 2002-151191 A 5/2002

OTHER PUBLICATIONS

Gikfun 2 Pin and 3 Pin Screw Terminal Block Connector, dated Sep. 15, 2015, [online], [site visited Apr. 24, 2018]. Available from Internet, <URL:https://www.amazon.com/dp/B015E3O4GS/ref=sspa_dk_detail_4?psc=1&pd_rd_i=B015E3O4GS&pd_rd_wg=fQFye&pd_rd_r=CFSHA3ZB8T7VWEQWVFYS&pd_rd_w=i3UWA> (Year: 2015).*

(Continued)

Primary Examiner — Angela J Lee
Assistant Examiner — Shawn T Gingrich
(74) *Attorney, Agent, or Firm* — K&L Gates LLP; Louis C. Cullman; Georgia N. Kefallinos

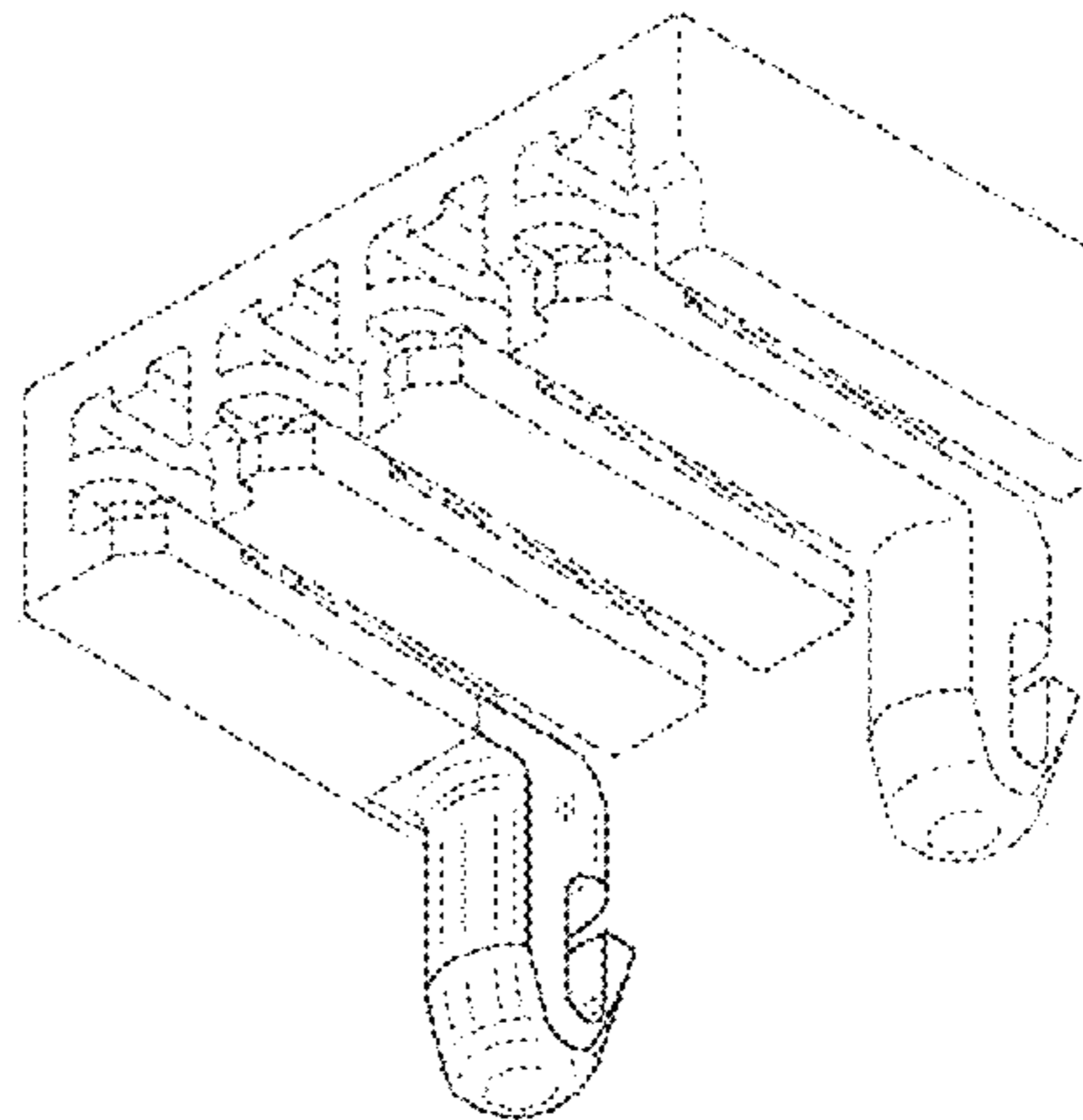
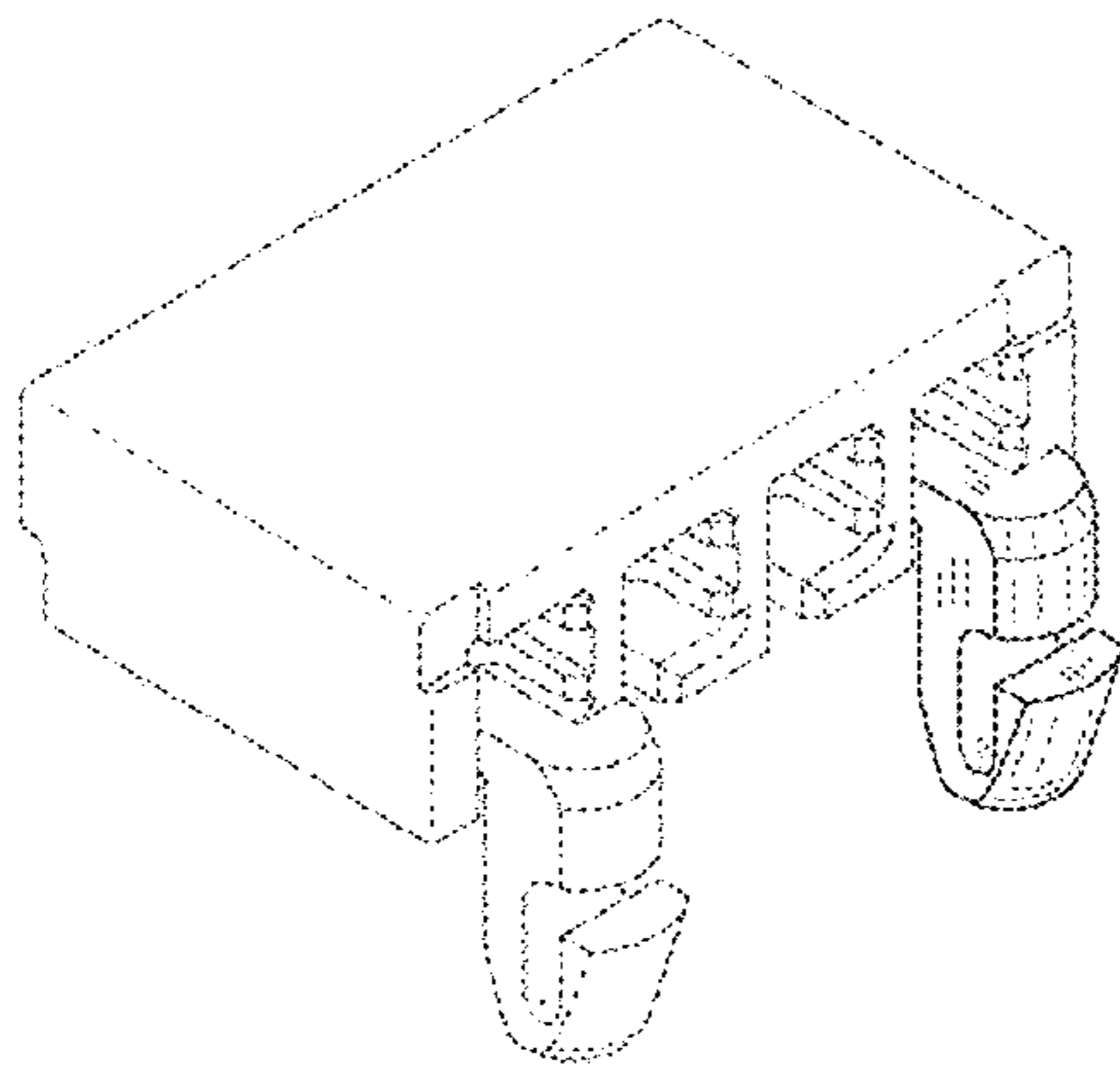
(57) **CLAIM**

The ornamental design for an electrical connector housing, as shown and described.

DESCRIPTION

FIG. 1 is a front view of an electrical connector housing of my design.
FIG. 2 is a back view of the electrical connector housing of FIG. 1.
FIG. 3 is a first side view of the electrical connector housing of FIG. 1.
FIG. 4 is a second side view of the electrical connector housing of FIG. 1.
FIG. 5 is a top view of the electrical connector housing of FIG. 1.
FIG. 6 is a bottom view of the electrical connector housing of FIG. 1.
FIG. 7 is a top perspective view of the electrical connector housing of FIG. 1; and,
FIG. 8 is a bottom perspective view of the electrical connector housing of FIG. 1.
The dash-dash broken lines are included for the purpose of illustrating environmental structure and form no part of the claimed design. The dash-dot broken lines are included to illustrate boundaries of the claim that form no part of the claimed design.

1 Claim, 8 Drawing Sheets



(58) **Field of Classification Search**

CPC ... H01R 4/24; H01R 4/48; H01R 9/22; H01R 9/24; H01R 13/02; H01R 13/04; H01R 13/15; H01R 13/40; H01R 13/432; H01R 13/44; H01R 13/514; H01R 13/60; H01R 13/62; H01R 13/627; H01R 13/639; H01R 13/64; H01R 24/00; H01R 31/00; H01R 31/08; H01R 12/22; H01R 13/46; A41F 1/00; A47G 1/06; F16B 5/06; F16B 5/12; F16B 17/00; F16B 21/08; F16B 45/00

See application file for complete search history.

5,706,559 A * 1/1998 Oliver F16B 21/088
 24/115 M
 D451,789 S * 12/2001 Hsieh D13/179
 D540,156 S * 4/2007 Bryant D8/367
 7,627,973 B2 * 12/2009 Lin G06F 1/1601
 24/297
 D648,210 S * 11/2011 Reynoso D8/382
 D664,027 S * 7/2012 Vosika D8/382
 D768,472 S * 10/2016 Yoshihashi D8/382
 D796,452 S * 9/2017 Masaki D13/154
 2013/0122755 A1 * 5/2013 Smith H01R 13/514
 439/701
 2013/0199001 A1 * 8/2013 Jagoda F16B 5/0657
 24/453
 2013/0244506 A1 * 9/2013 Endo H01R 4/48
 439/825

(56) **References Cited**

U.S. PATENT DOCUMENTS

D273,091 S * 3/1984 Kurosaki D8/382
 D317,859 S * 7/1991 Kobayashi D13/154
 D330,850 S * 11/1992 Roick D8/367
 5,184,964 A * 2/1993 Douty H01R 13/6315
 439/247

OTHER PUBLICATIONS

Design HMI Terminals, dated Apr. 14, 2017, [online], [site visited Apr. 24, 2018]. Available from Internet, <URL:<http://www.designhmi.com/2017/04/14/press-fit-terminals/>> (Year: 2018).*

* cited by examiner

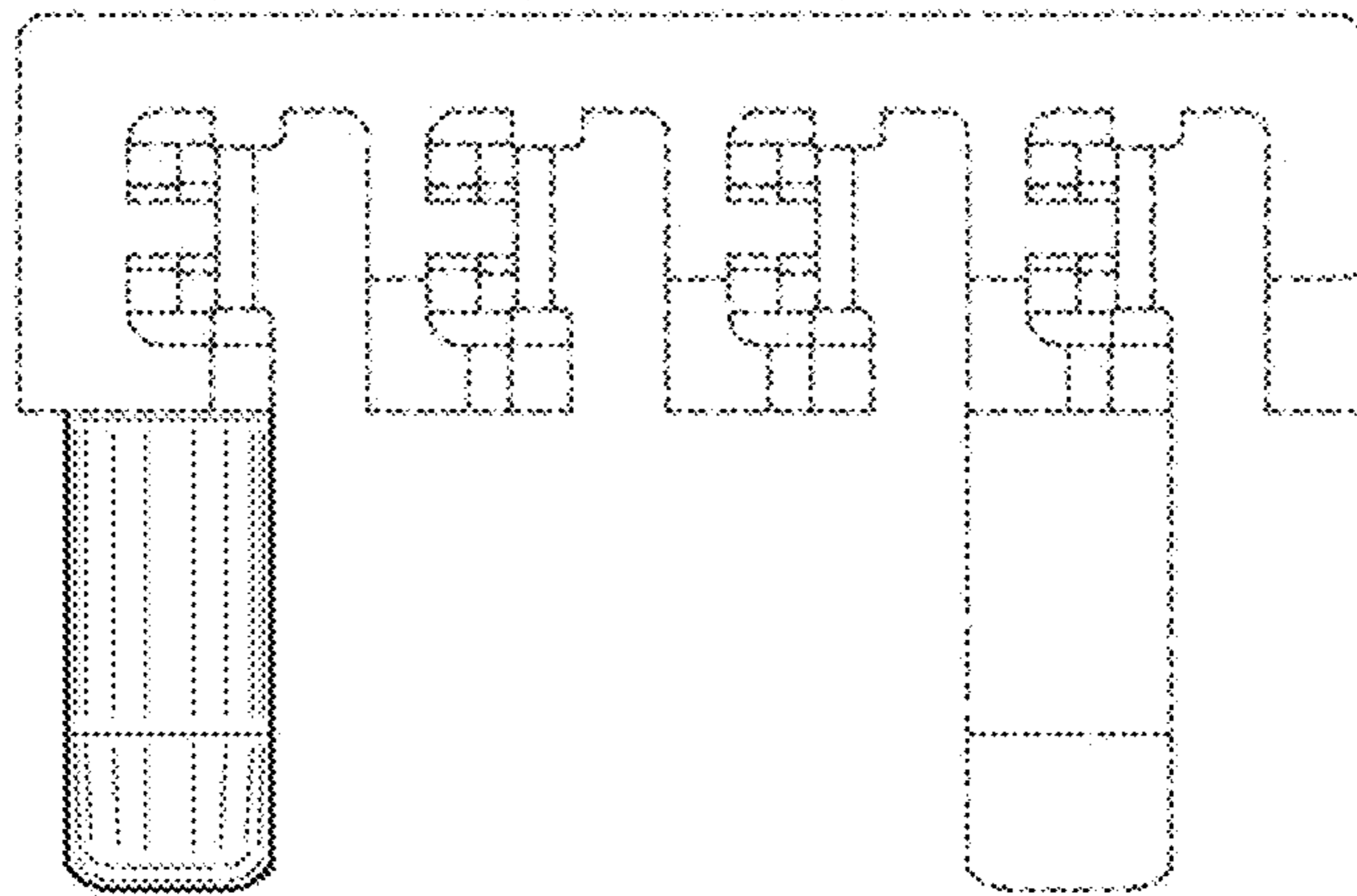


FIG. 1

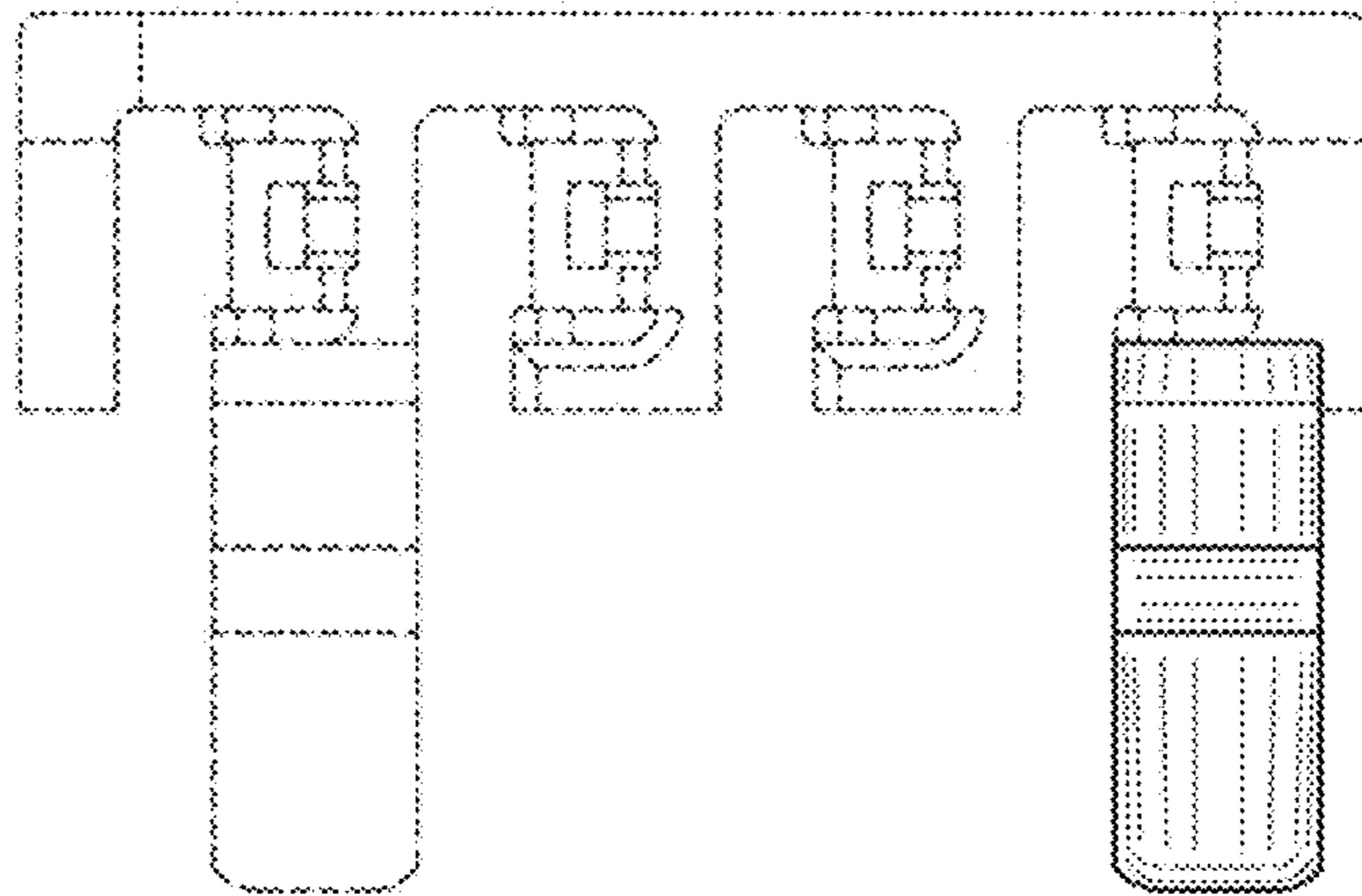


FIG. 2

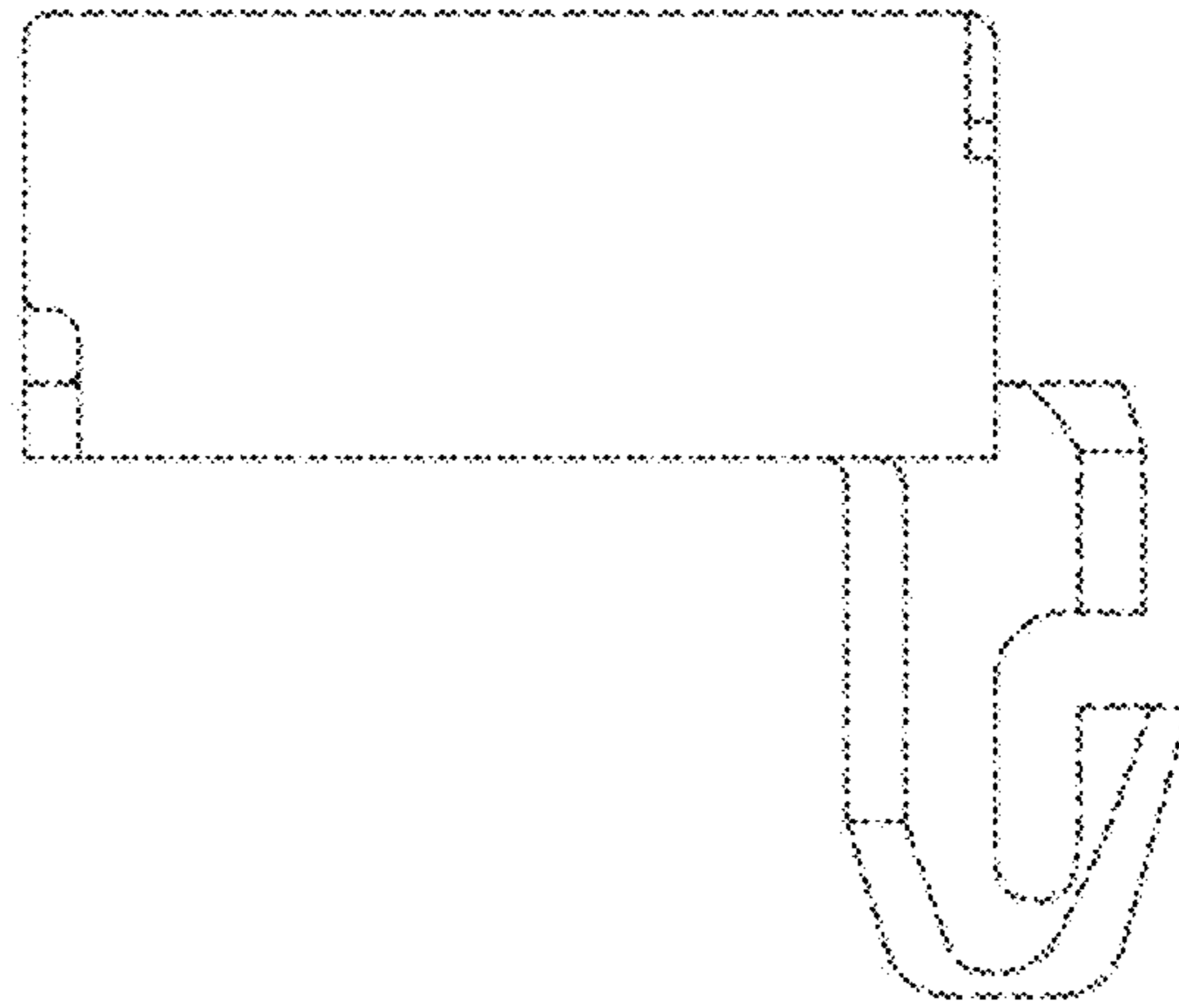


FIG. 3

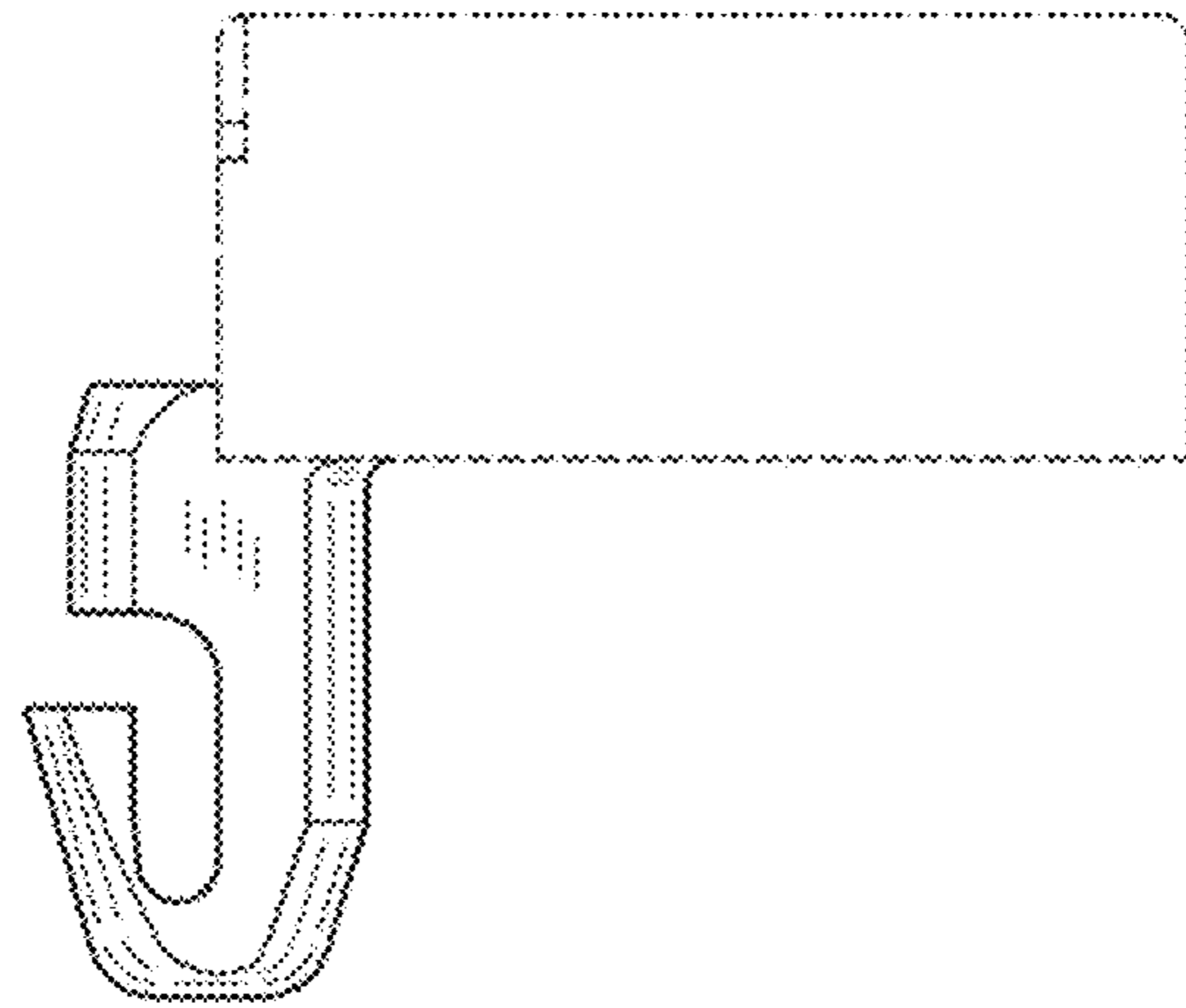


FIG. 4

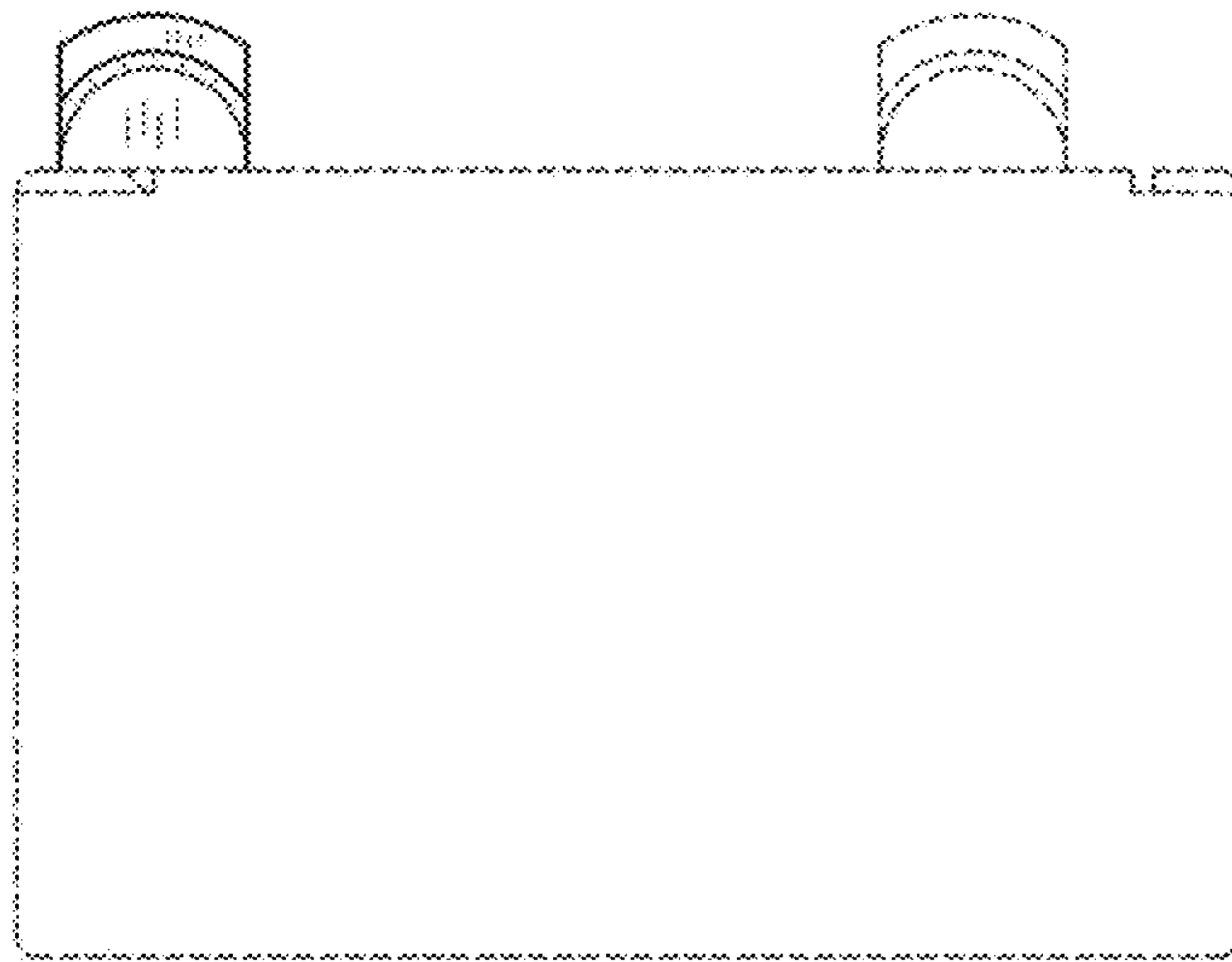


FIG. 5

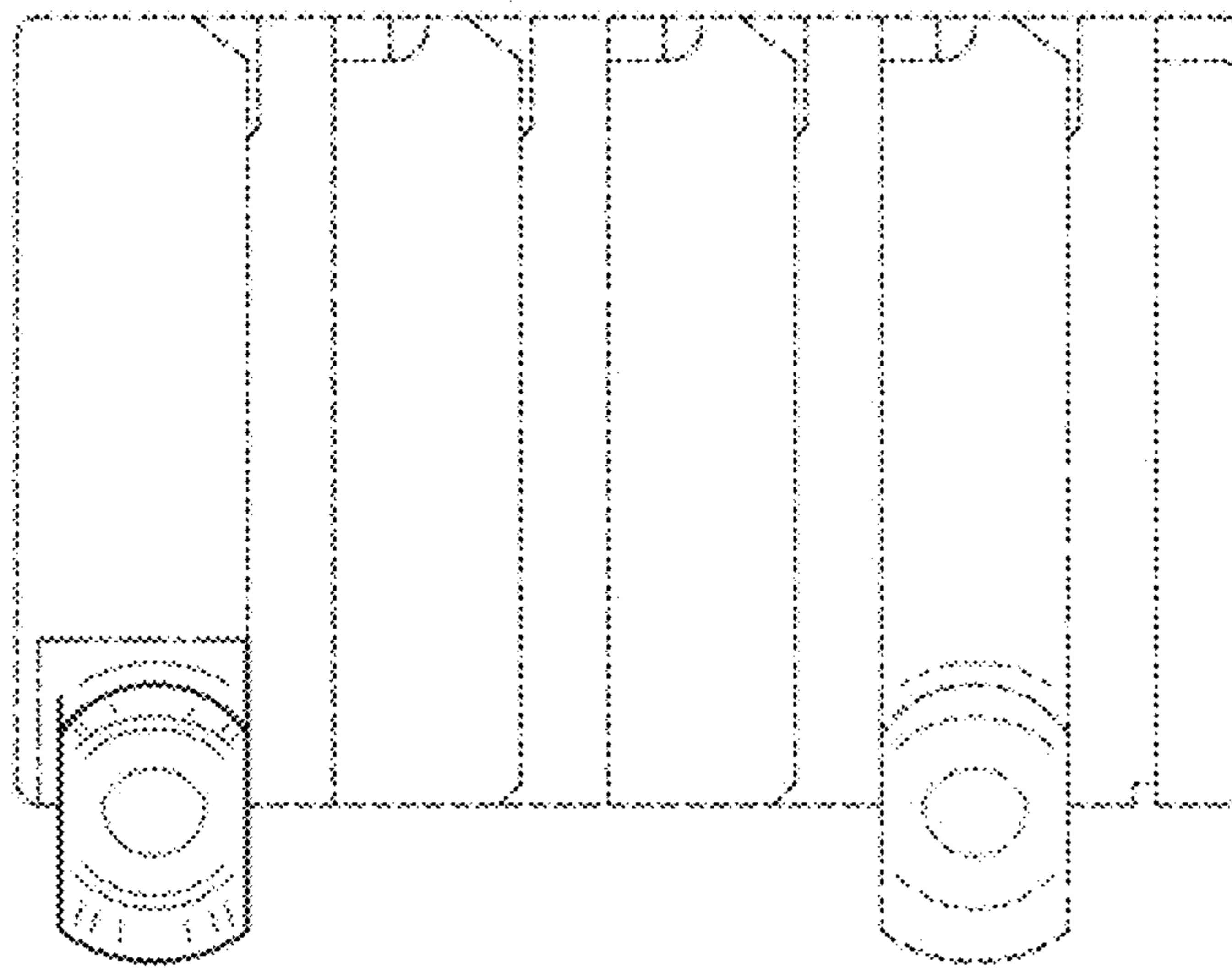


FIG. 6

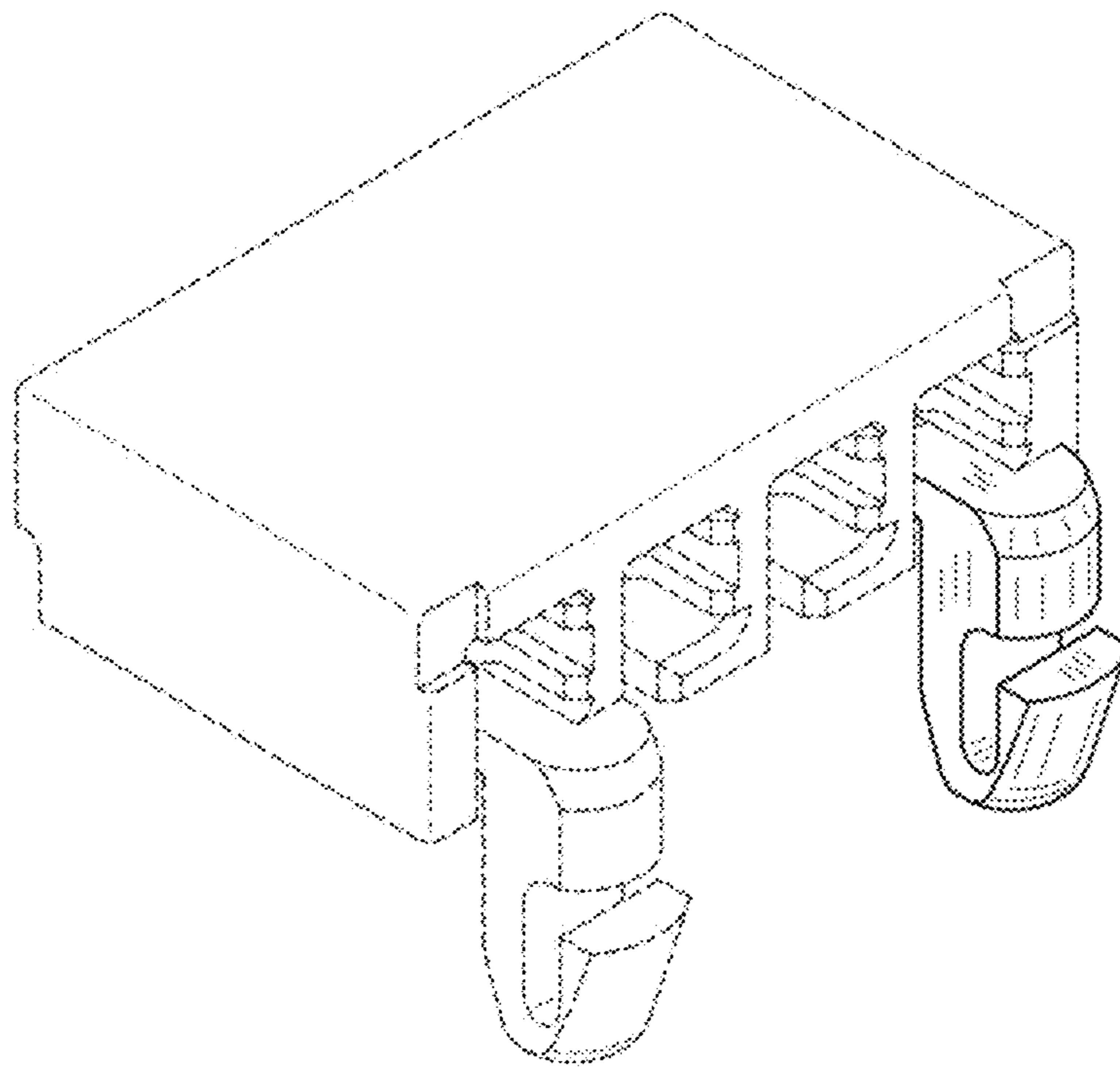


FIG. 7

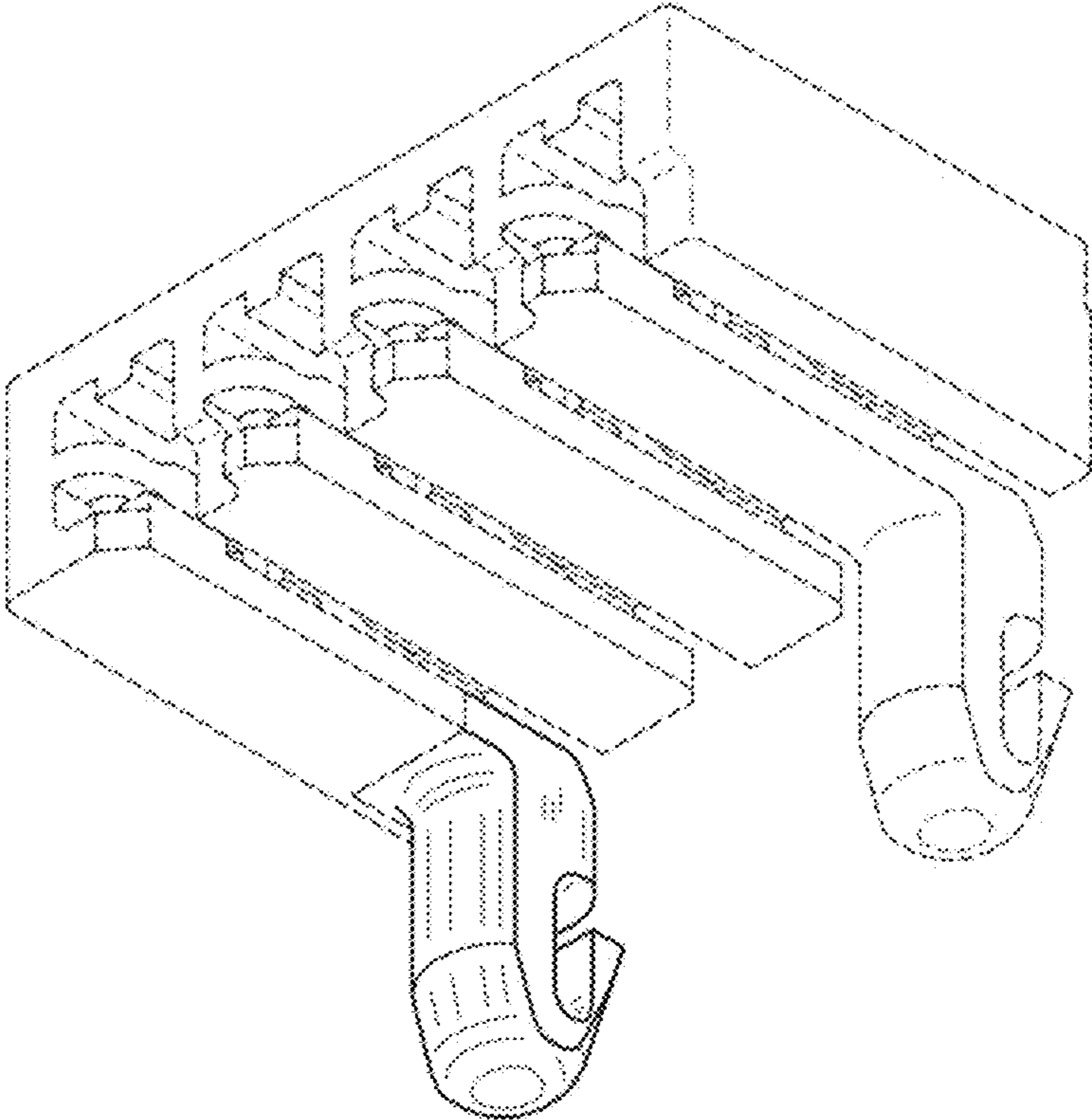


FIG. 8