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(12) **United States Design Patent** (10) **Patent No.:** **US D837,152 S**  
**Wegerer et al.** (45) **Date of Patent:** **\*\* Jan. 1, 2019**

(54) **COMBINED INPUT AND OUTPUT MODULE**

FOREIGN PATENT DOCUMENTS

(71) Applicant: **PILZ GMBH & CO. KG**, Ostfildern (DE)

CN 304175604 \* 6/2017

(72) Inventors: **Lydia Wegerer**, Ostfildern (DE);  
**Markus Cech**, Ostfildern (DE);  
**Andreas Bell**, Esslingen (DE)

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(73) Assignee: **PILZ GMBH & CO. KG**, Ostfildern (DE)

(Continued)

(\*\*) Term: **15 Years**

*Primary Examiner* — Cynthia Ramirez

*Assistant Examiner* — Laura H Yu

(21) Appl. No.: **29/573,283**

(74) *Attorney, Agent, or Firm* — Harness, Dickey & Pierce, P.L.C.

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(30) **Foreign Application Priority Data**

Feb. 25, 2016 (EM) ..... 3001742-0001-0008

(51) **LOC (11) Cl.** ..... **13-02**

(52) **U.S. Cl.**  
USPC ..... **D13/110**

(58) **Field of Classification Search**  
USPC ..... D13/110, 103, 108, 123, 133, 199  
CPC ..... H01R 31/065; H01R 35/04  
See application file for complete search history.

(57) **CLAIM**

The ornamental design for a combined input and output module, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of the combined input and output module embodying our new design;  
FIG. 2 is a top view of the combined input and output module shown in FIG. 1;  
FIG. 3 is a front view of the combined input and output module shown in FIG. 1;  
FIG. 4 is a rear view of the combined input and output module shown in FIG. 1;  
FIG. 5 is a left side view of the combined input and output module shown in FIG. 1;  
FIG. 6 is a right side view of the combined input and output module shown in FIG. 1; and,  
FIG. 7 is a bottom view of the combined input and output module shown in FIG. 1.

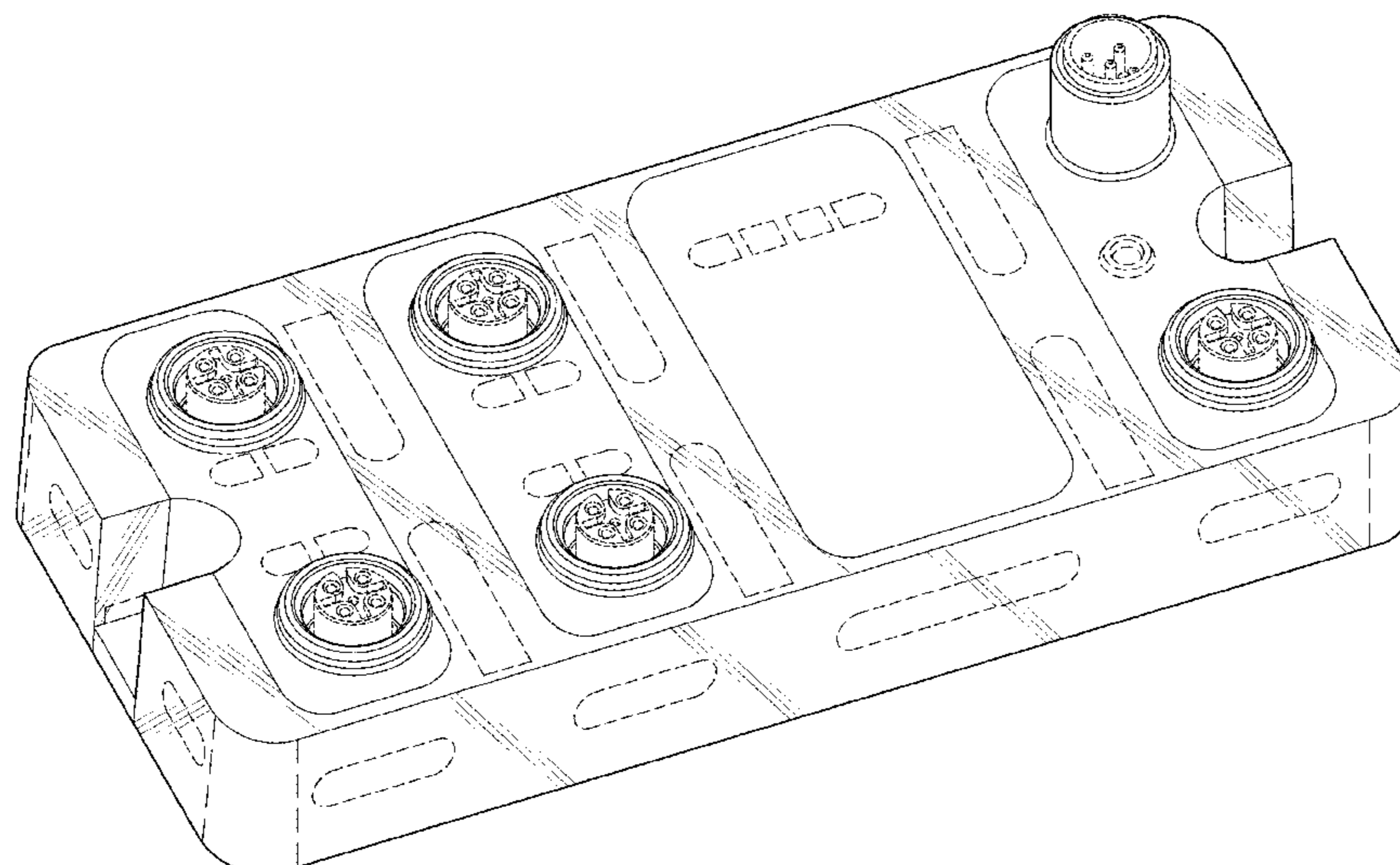
The broken lines in the figures are for the purpose of illustrating portions of the combined input and output module that form no part of the claimed design.

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**1 Claim, 5 Drawing Sheets**



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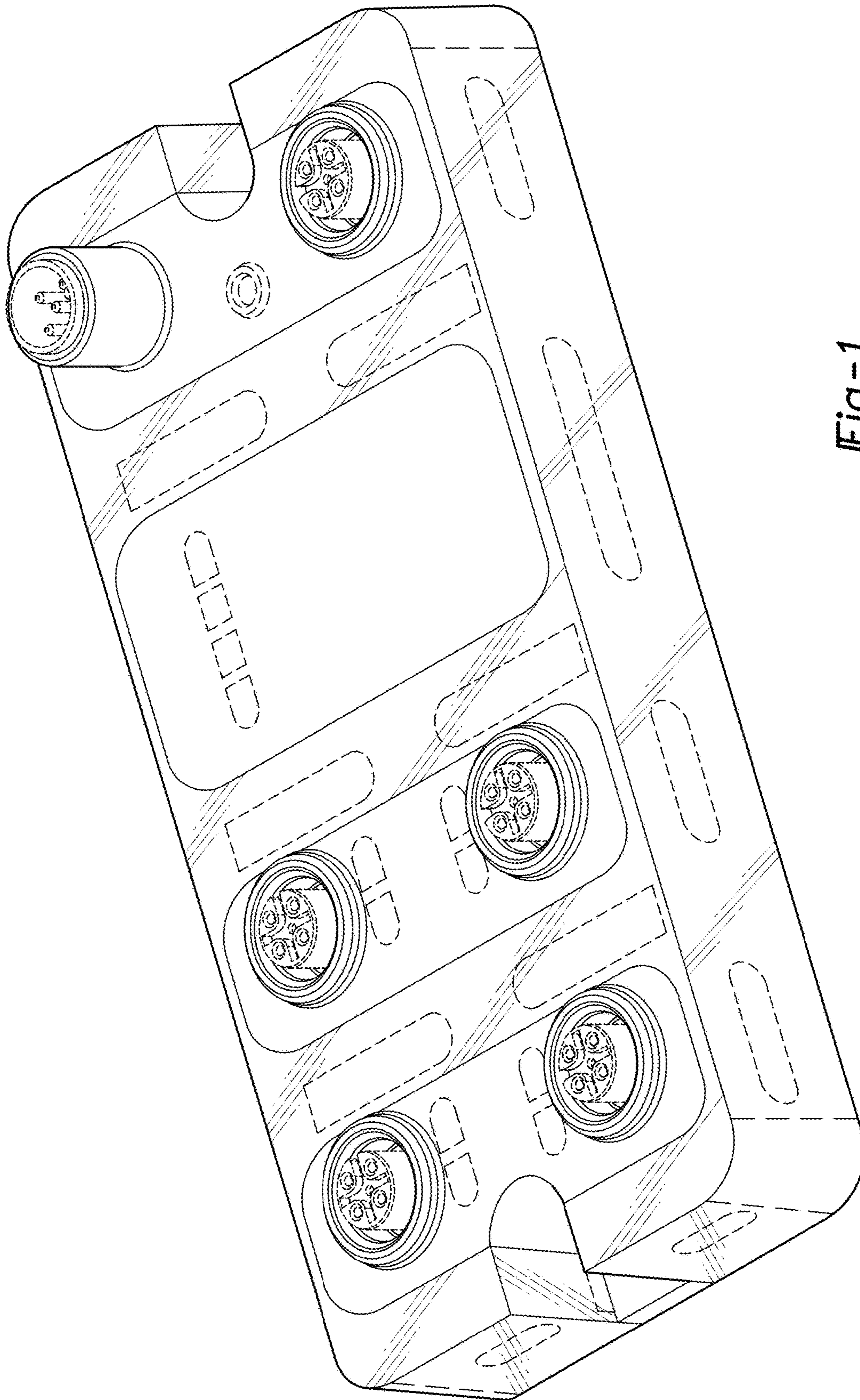


Fig-1

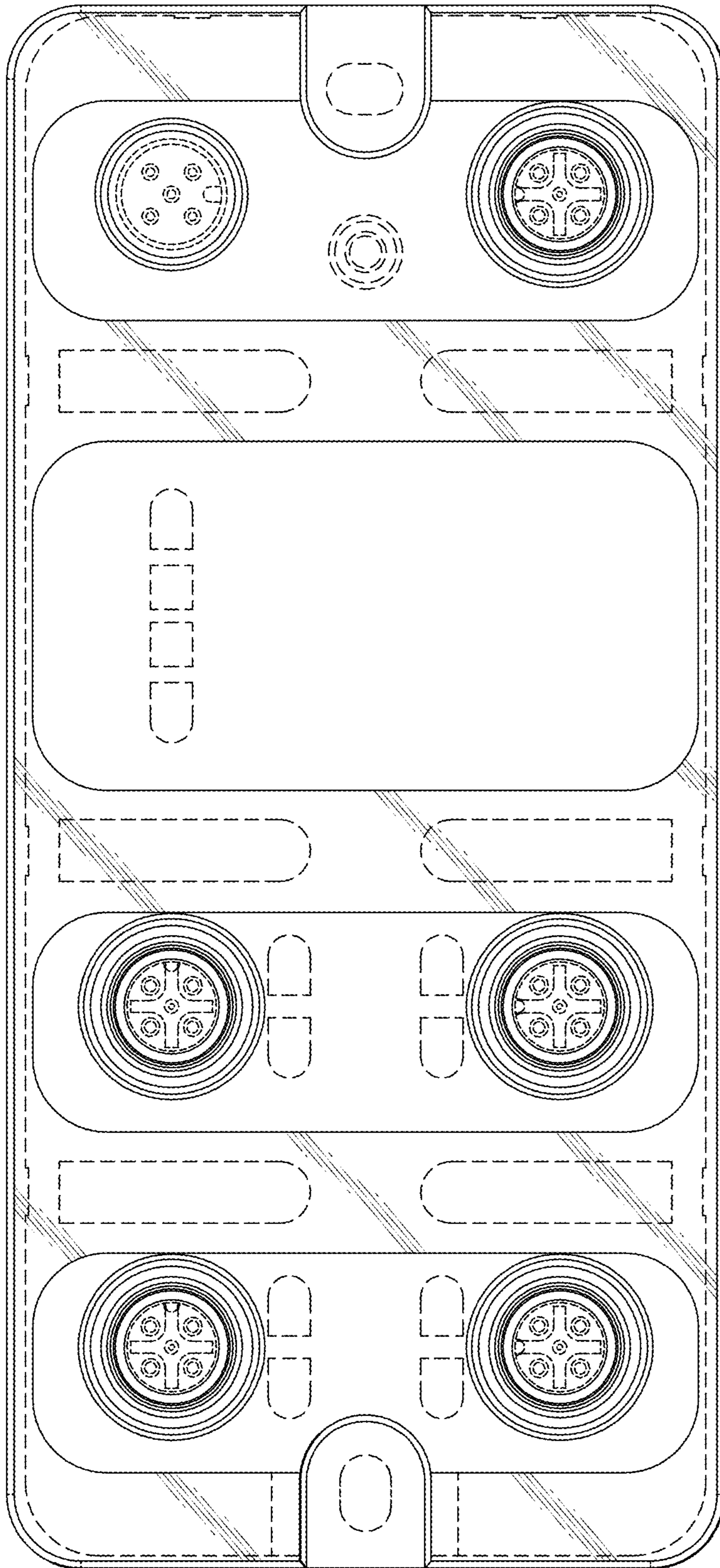


Fig-2

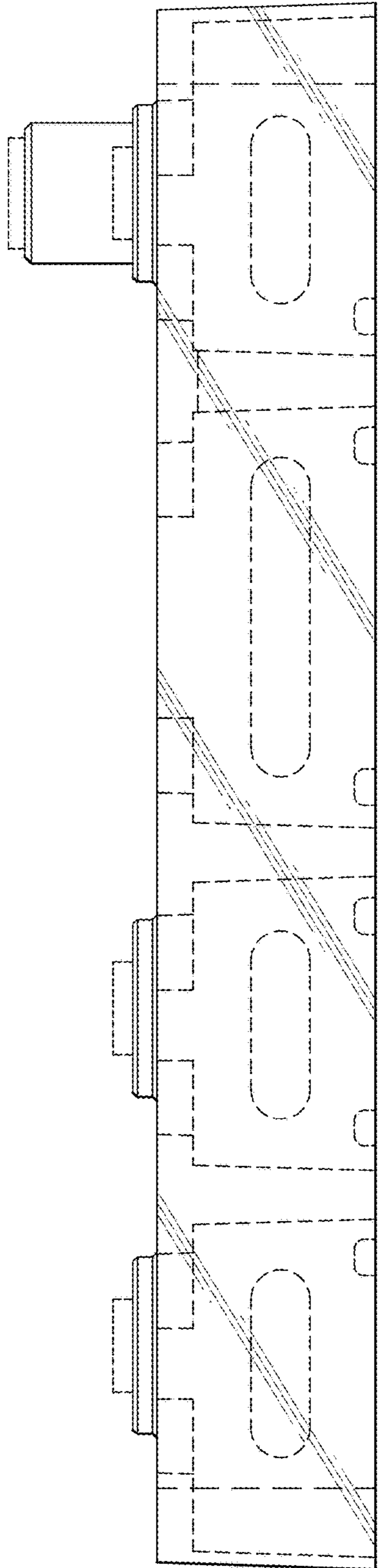


Fig-3

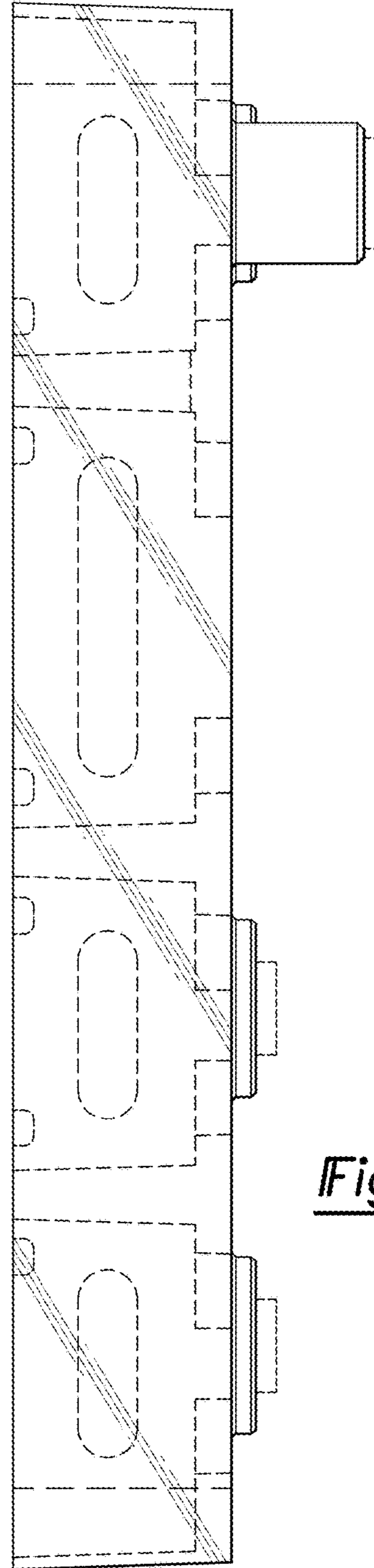


Fig-4

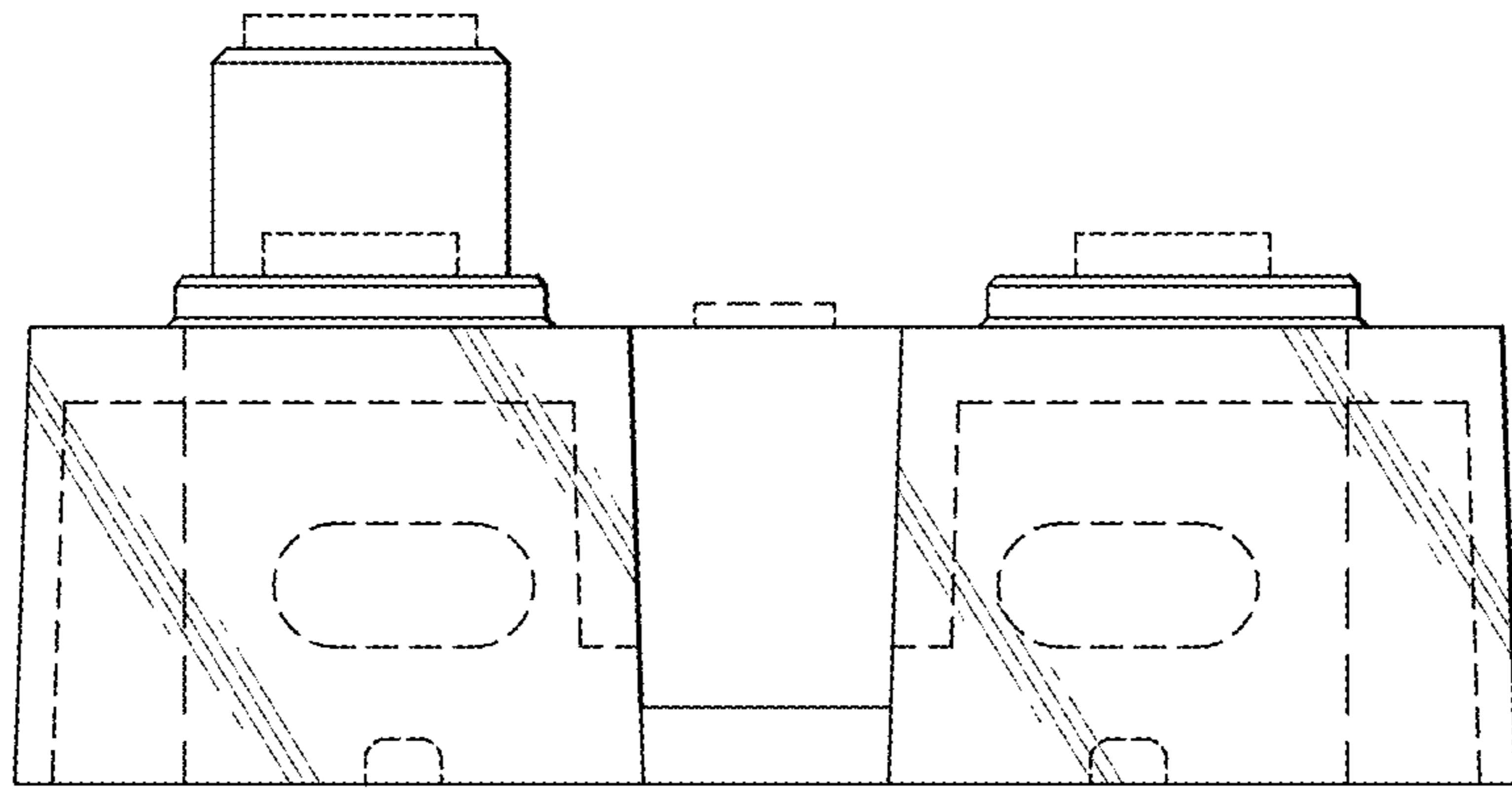


Fig-5

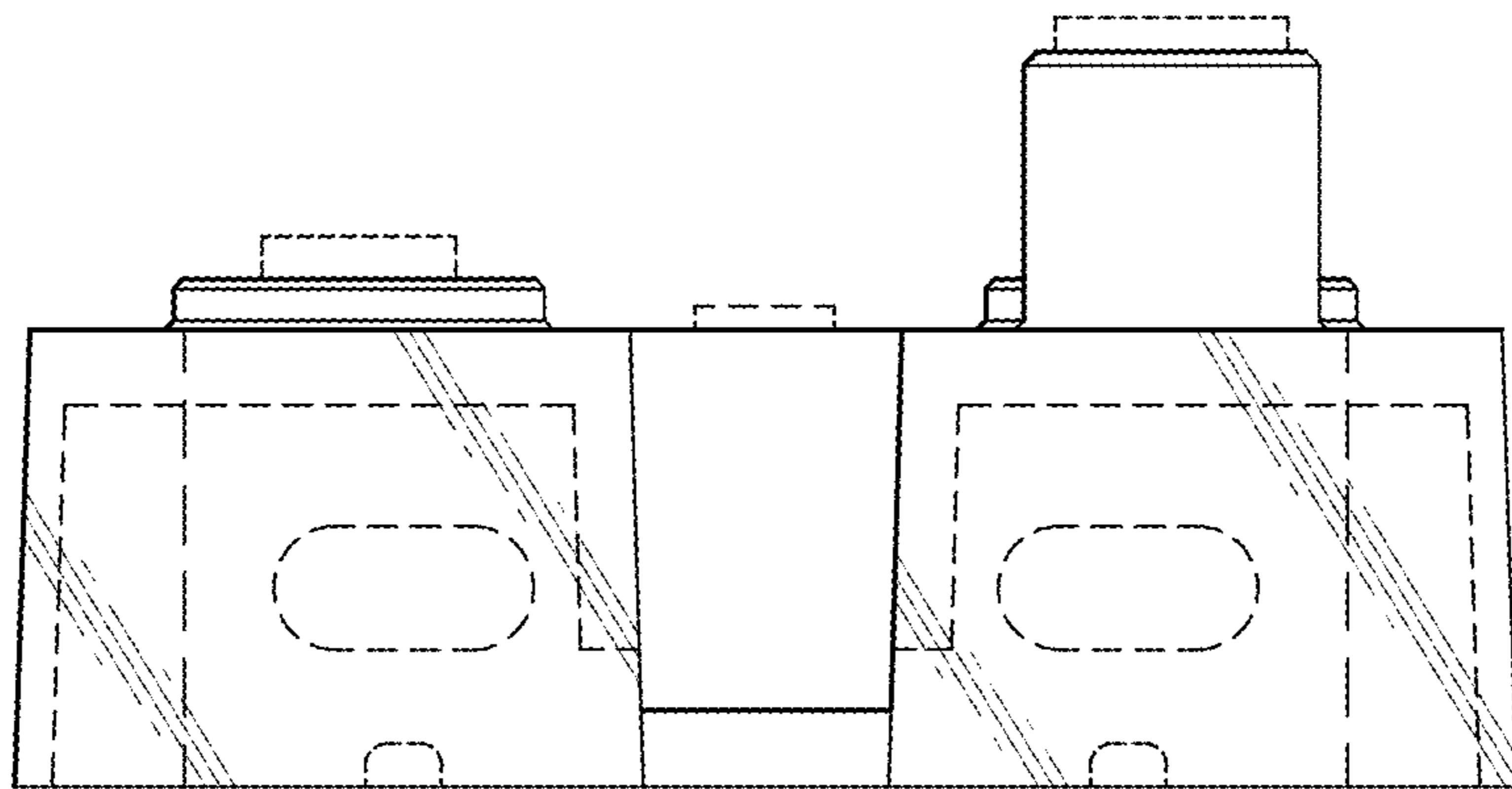


Fig-6

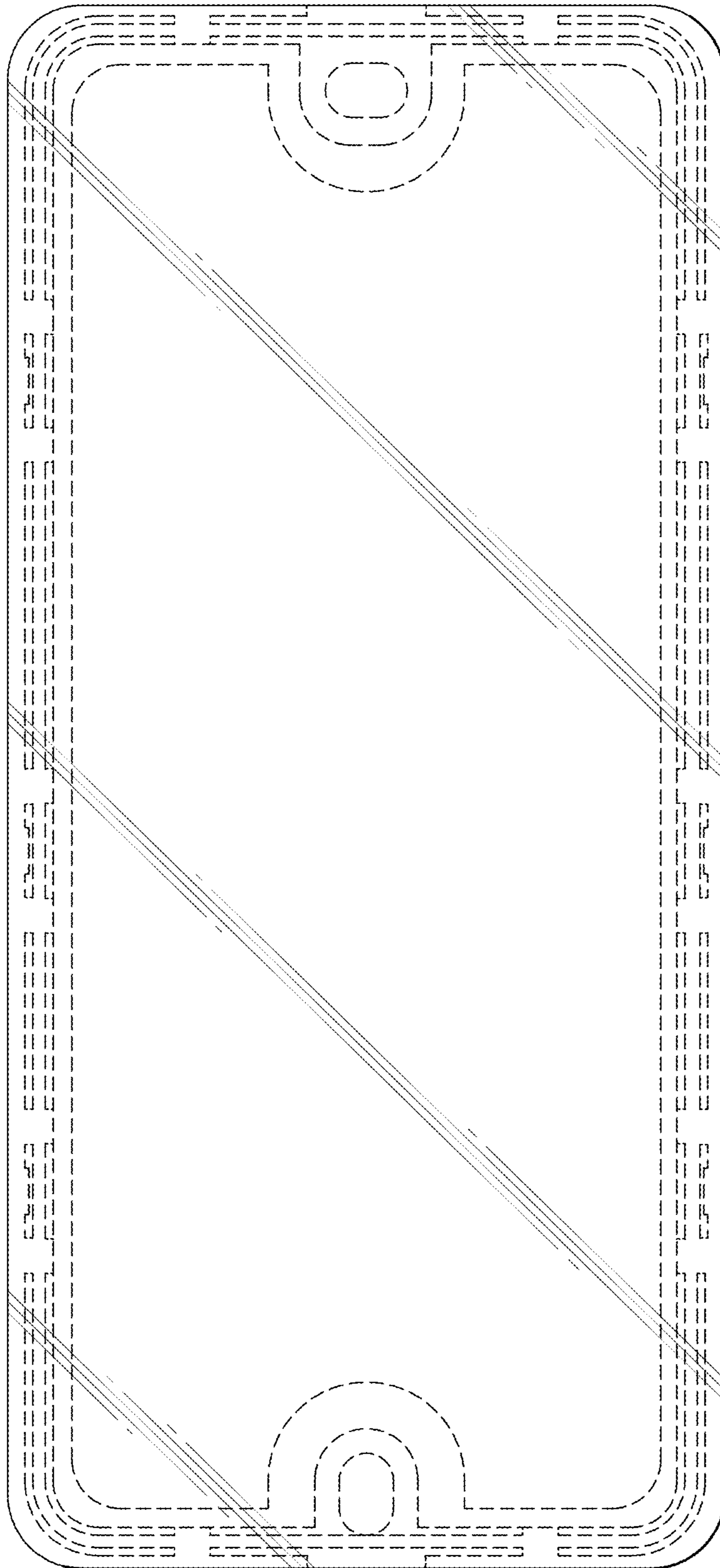


Fig-7