



US00D831086S

(12) **United States Design Patent**  
**Sakka et al.**

(10) **Patent No.:** **US D831,086 S**  
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(54) **SUBSTRATE PROCESSING UNIT**

FOREIGN PATENT DOCUMENTS

(71) Applicant: **Hitachi High-Technologies Corporation**, Minato-ku, Tokyo (JP)

JP D1456090 S 11/2015  
JP D1456479 S 11/2015

(72) Inventors: **Yusaku Sakka**, Tokyo (JP); **Hikomichi Kawasaki**, Tokyo (JP)

\* cited by examiner

(73) Assignee: **Hitachi High-Technologies Corporation**, Tokyo (JP)

*Primary Examiner* — Mitchell I. Siegel

*Assistant Examiner* — Khawaja Anwar

(74) *Attorney, Agent, or Firm* — Crowell & Moring LLP

(\*\*) Term: **15 Years**

(57) **CLAIM**

(21) Appl. No.: **29/606,724**

The ornamental design for a substrate processing unit, as shown and described.

(22) Filed: **Jun. 7, 2017**

(30) **Foreign Application Priority Data**

**DESCRIPTION**

Dec. 9, 2016 (JP) ..... 2016-026822

(51) **LOC (11) Cl.** ..... **15-03**

(52) **U.S. Cl.**  
USPC ..... **D15/199**

(58) **Field of Classification Search**  
USPC ..... D15/138, 199; D13/182, 184, 199  
CPC ..... H05H 1/2406; H05H 2001/2412; H05H 2001/2418

See application file for complete search history.

Application Ser. No. 29/606,718, filed herewith and entitled “Substrate Processing Unit”; and  
Application Ser. No. 29/606,730, filed herewith and entitled “Substrate Processing Unit”.

FIG. 1 is a front, top and right side perspective view of a substrate processing unit according to the design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a right side elevational view thereof;

FIG. 5 is a left side elevational view thereof;

FIG. 6 is a top plan view thereof;

FIG. 7 is a bottom plan view thereof;

FIG. 8 is an enlarged view of the portion shown in BOX 8 in FIG. 2 thereof; and,

FIG. 9 is another perspective view thereof, shown with an unclaimed cover.

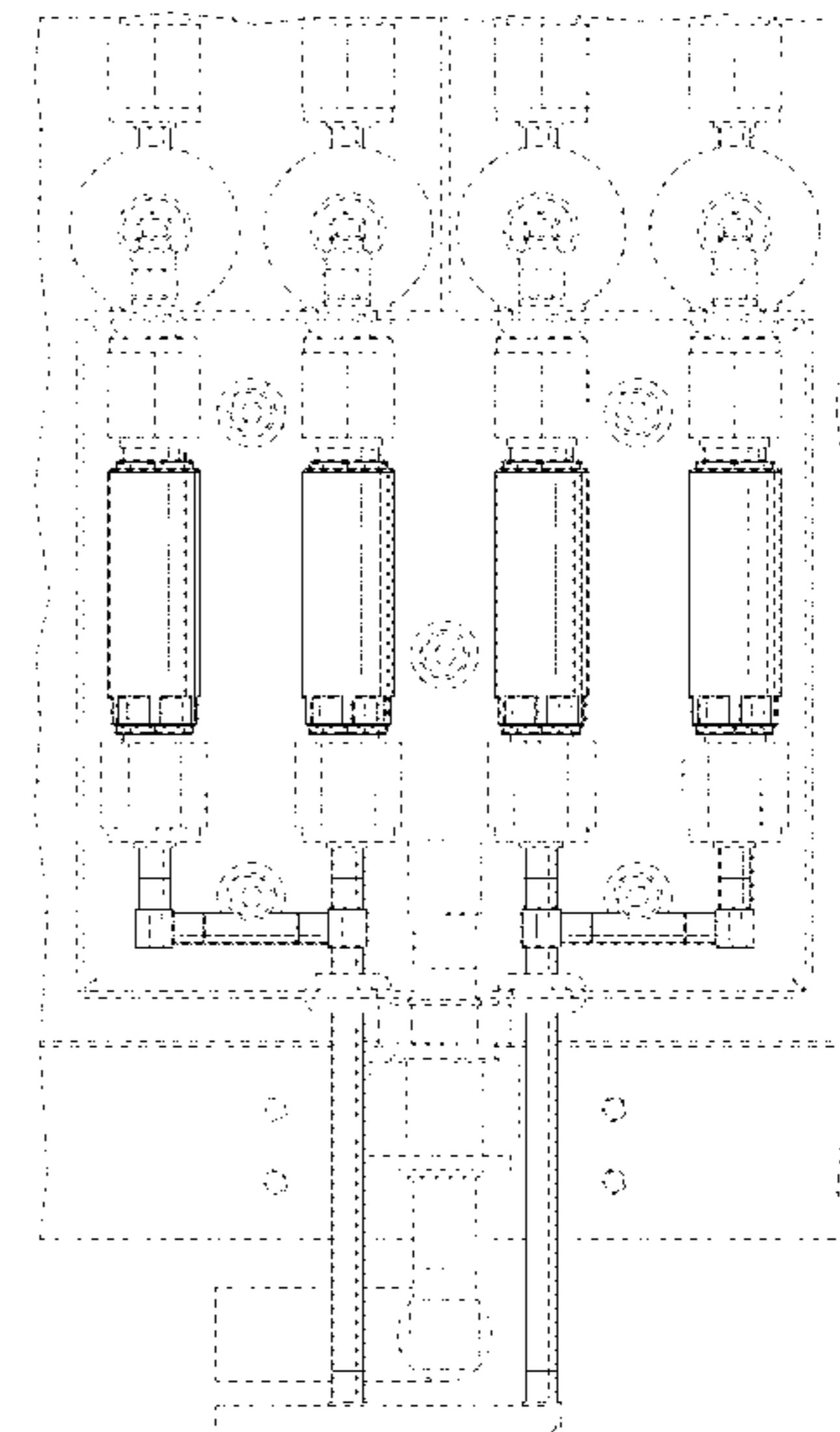
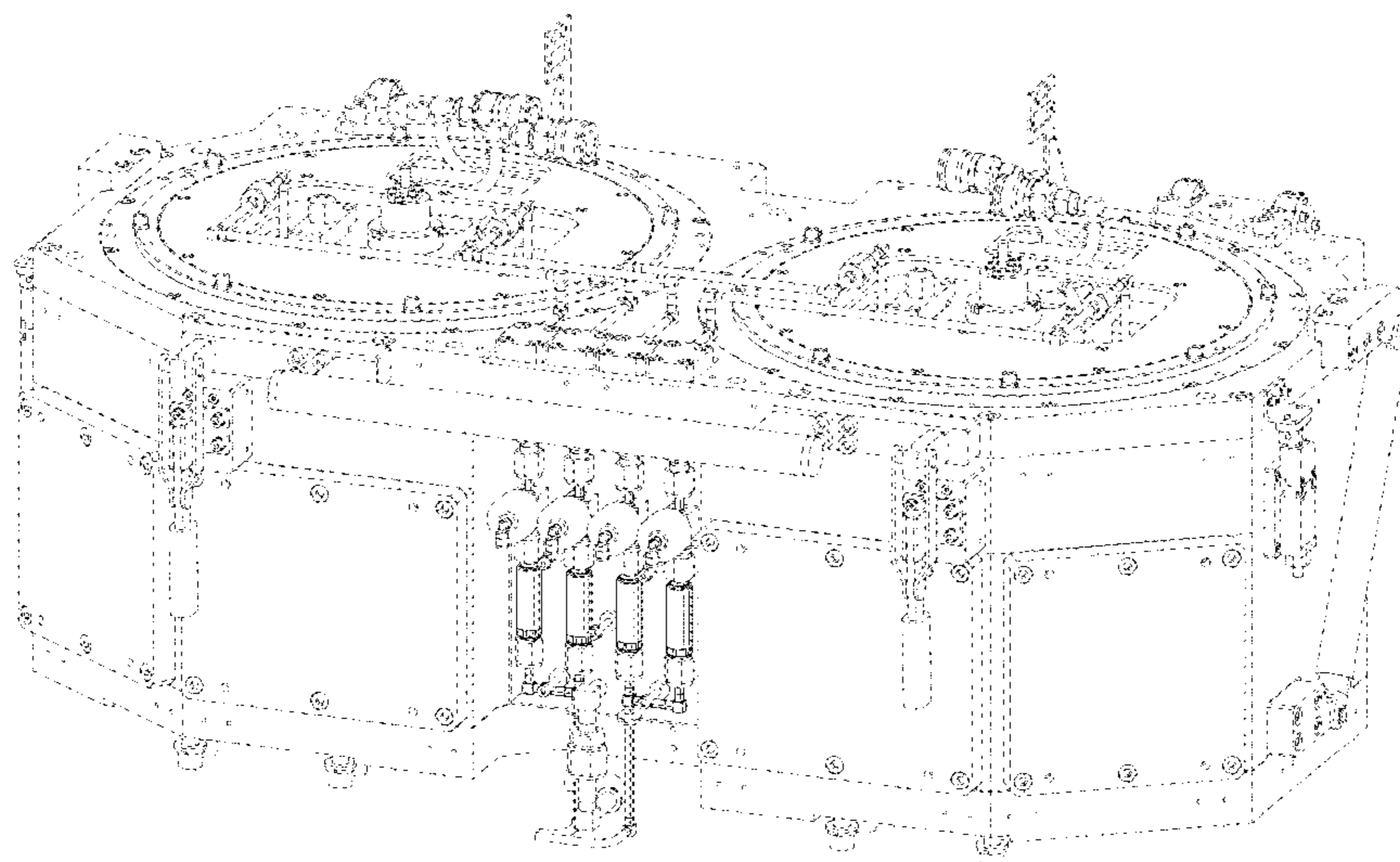
The broken lines in the figures depict unclaimed portions of the substrate processing unit and form no part of the claimed design.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D556,226 S	*	11/2007	Jennings	.....	D15/199
D581,438 S	*	11/2008	Liu	.....	D15/199
D623,673 S	*	9/2010	Duer	.....	D15/199
D675,172 S	*	1/2013	Tiner	.....	D13/199
D785,578 S	*	5/2017	Kim	.....	D13/182
D804,556 S	*	12/2017	Harada	.....	D15/144.1
D805,044 S	*	12/2017	Sakai	.....	D13/182
D808,350 S	*	1/2018	Hart	.....	D13/182
D811,457 S	*	2/2018	Sakai	.....	D15/199

**1 Claim, 9 Drawing Sheets**



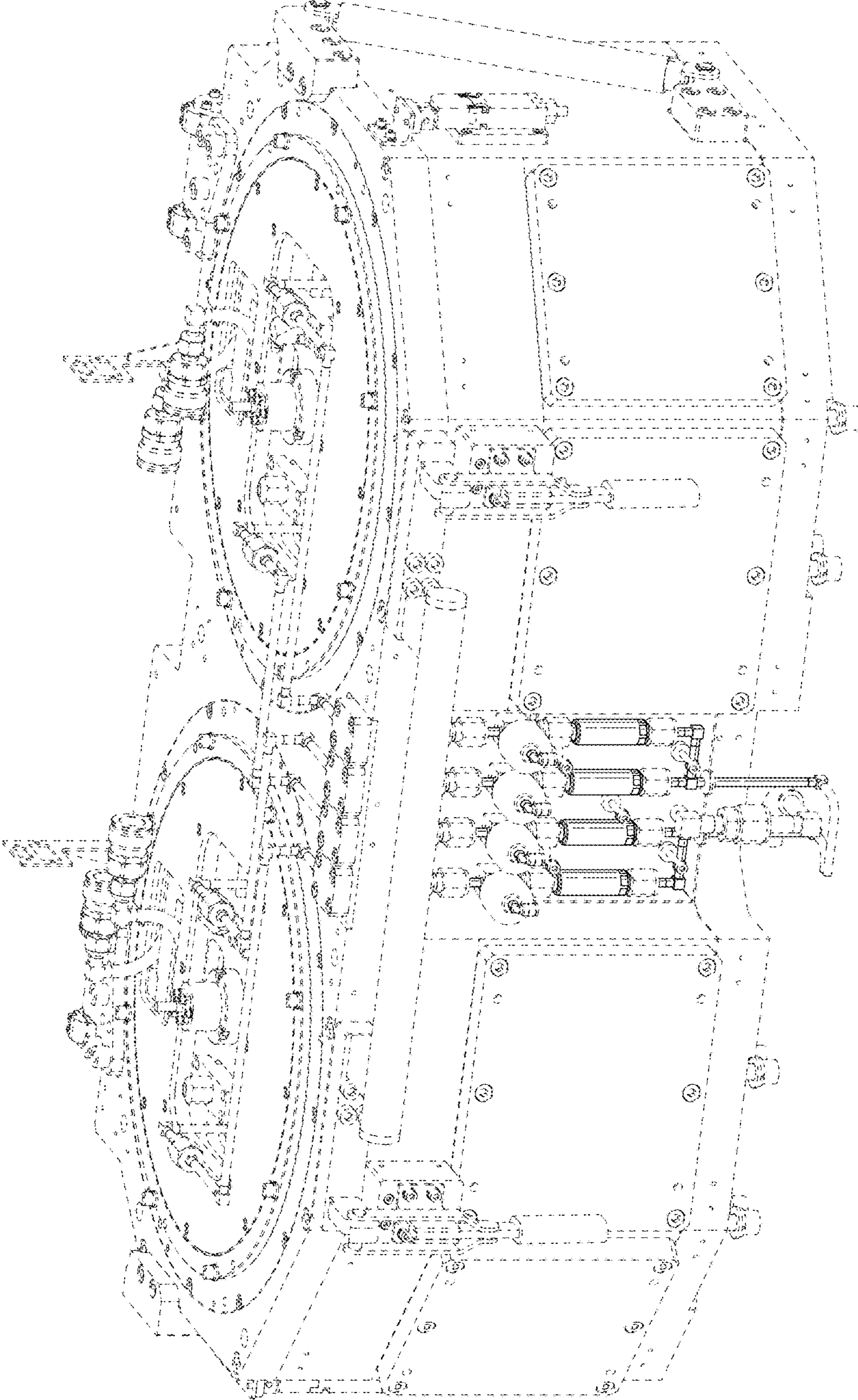


FIG. 1

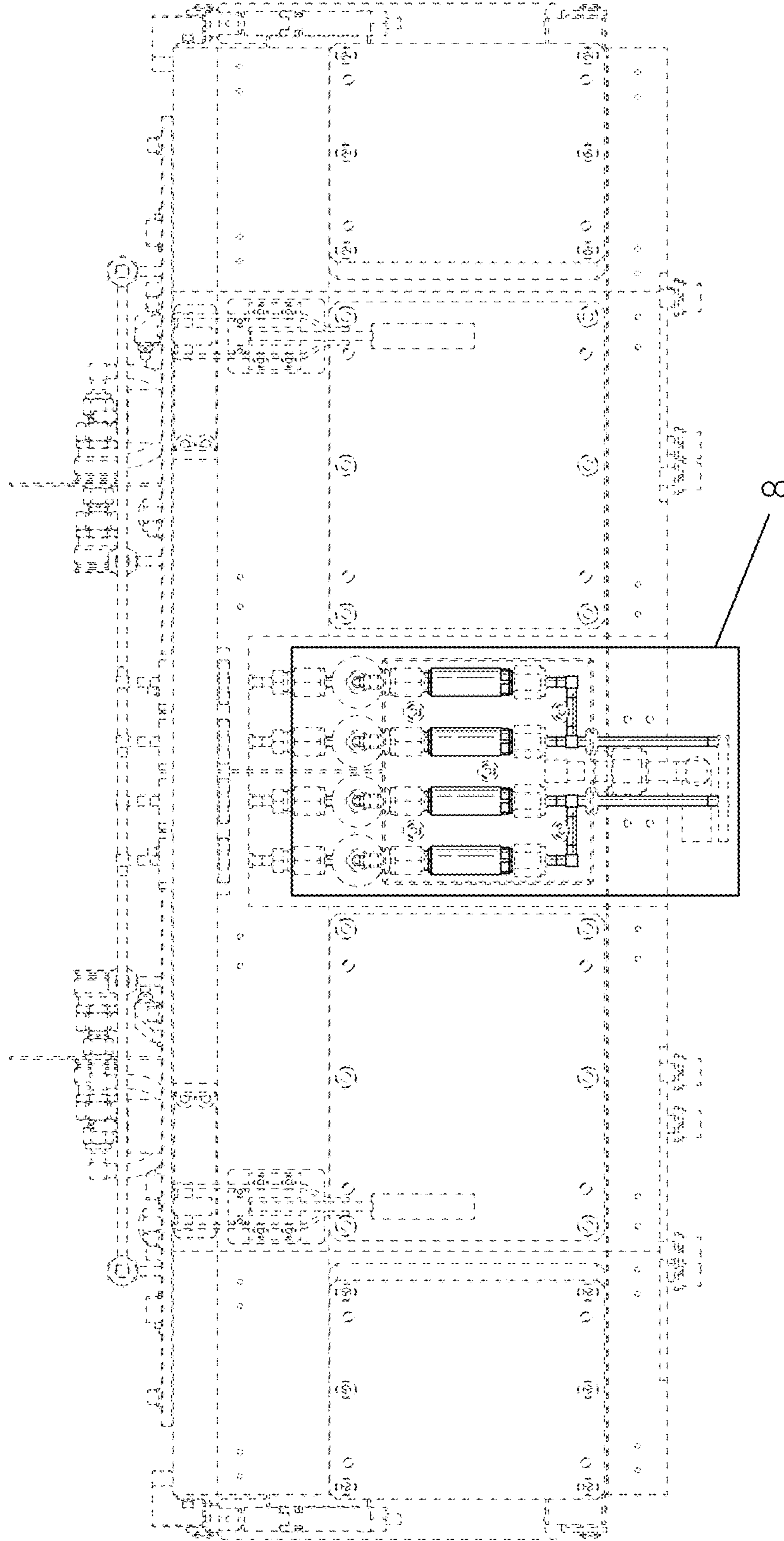


FIG. 2

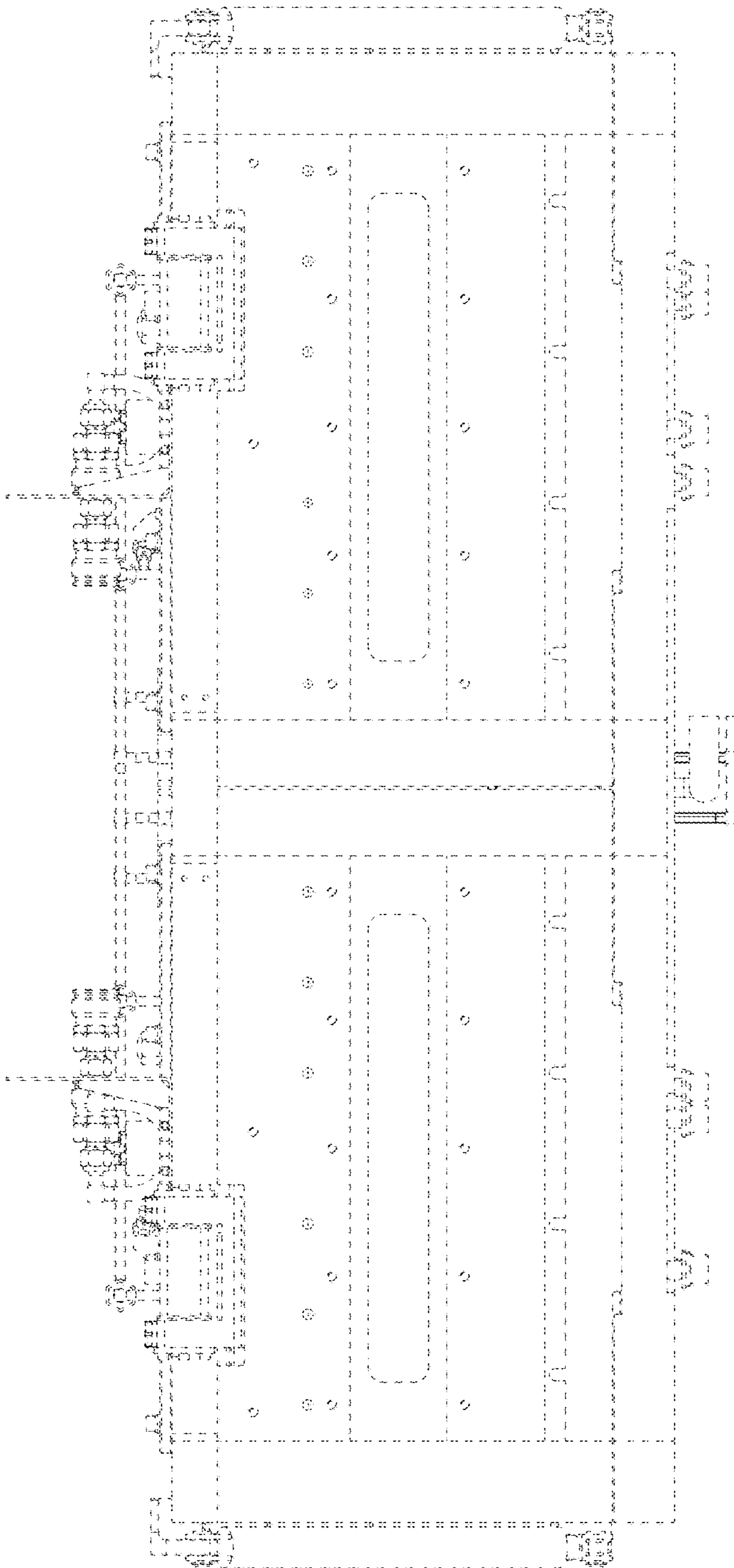


FIG. 3

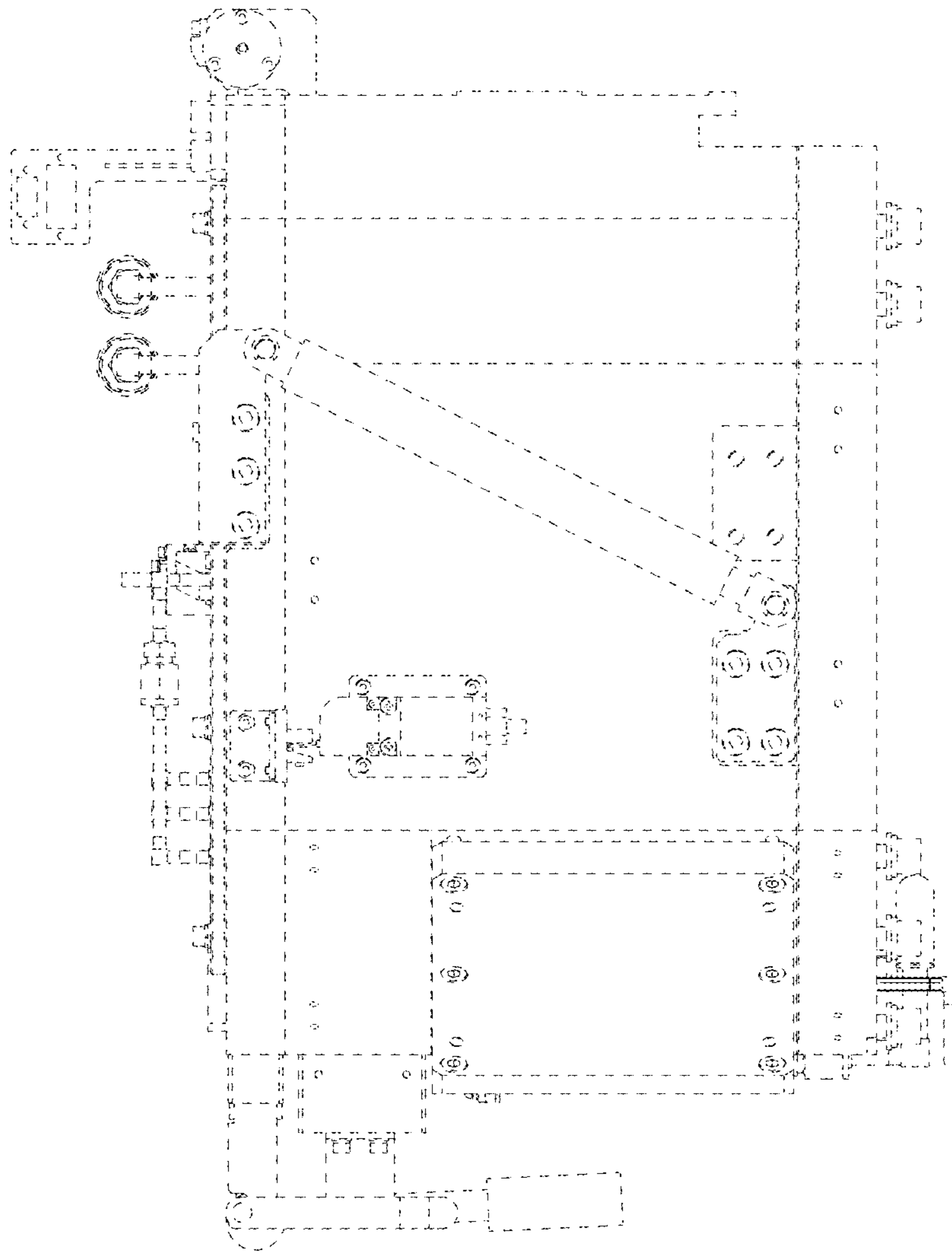


FIG. 4

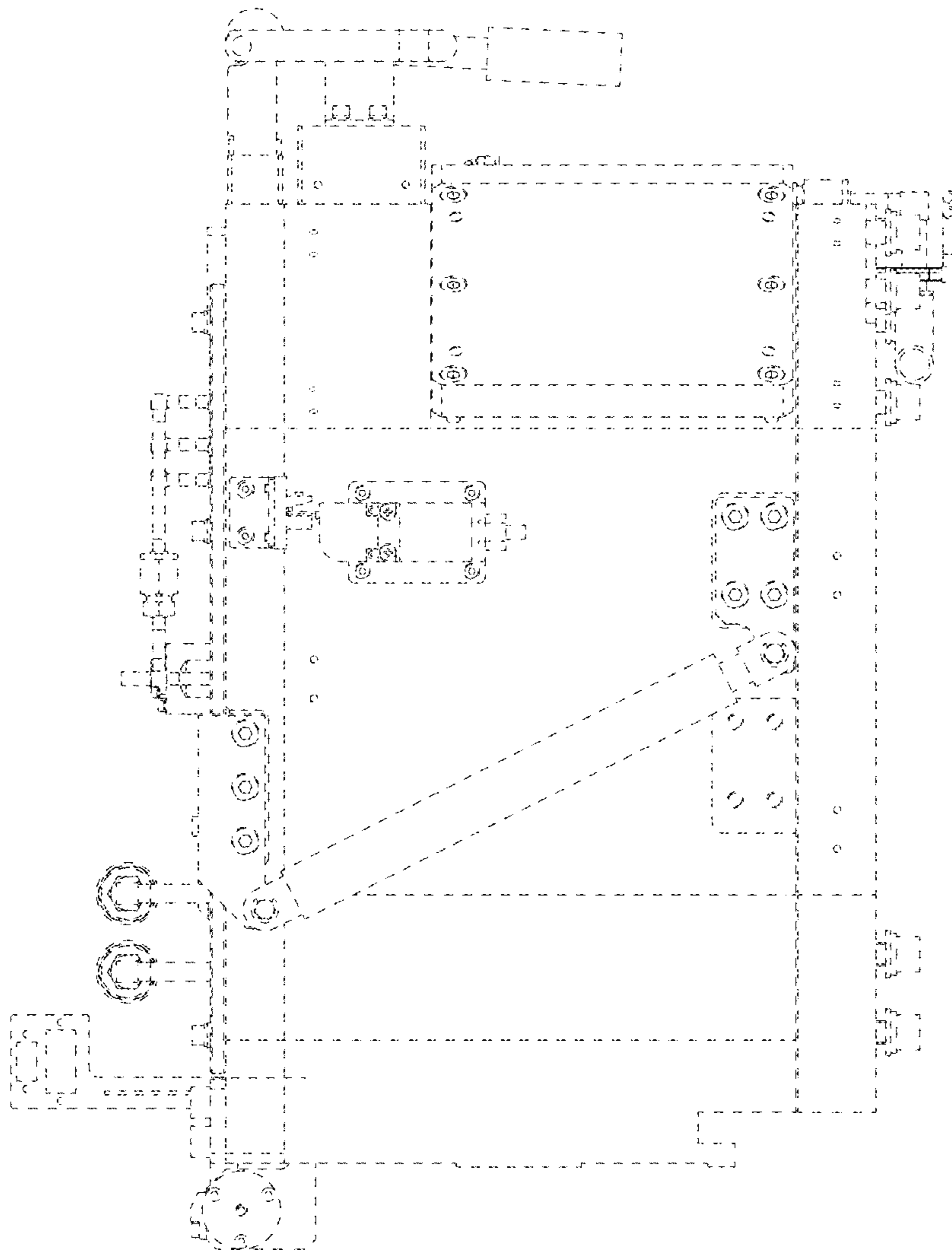


FIG. 5

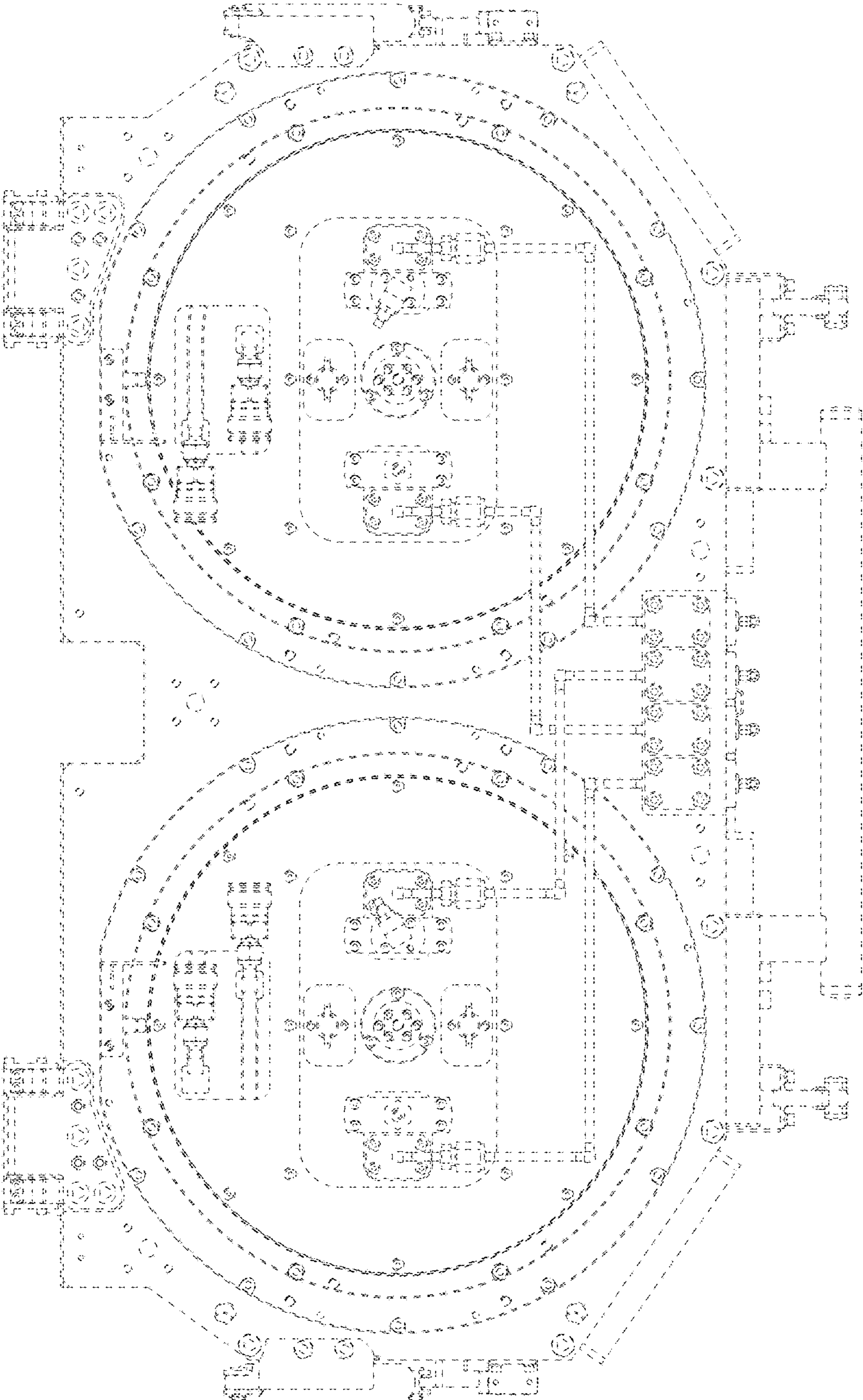


FIG. 6

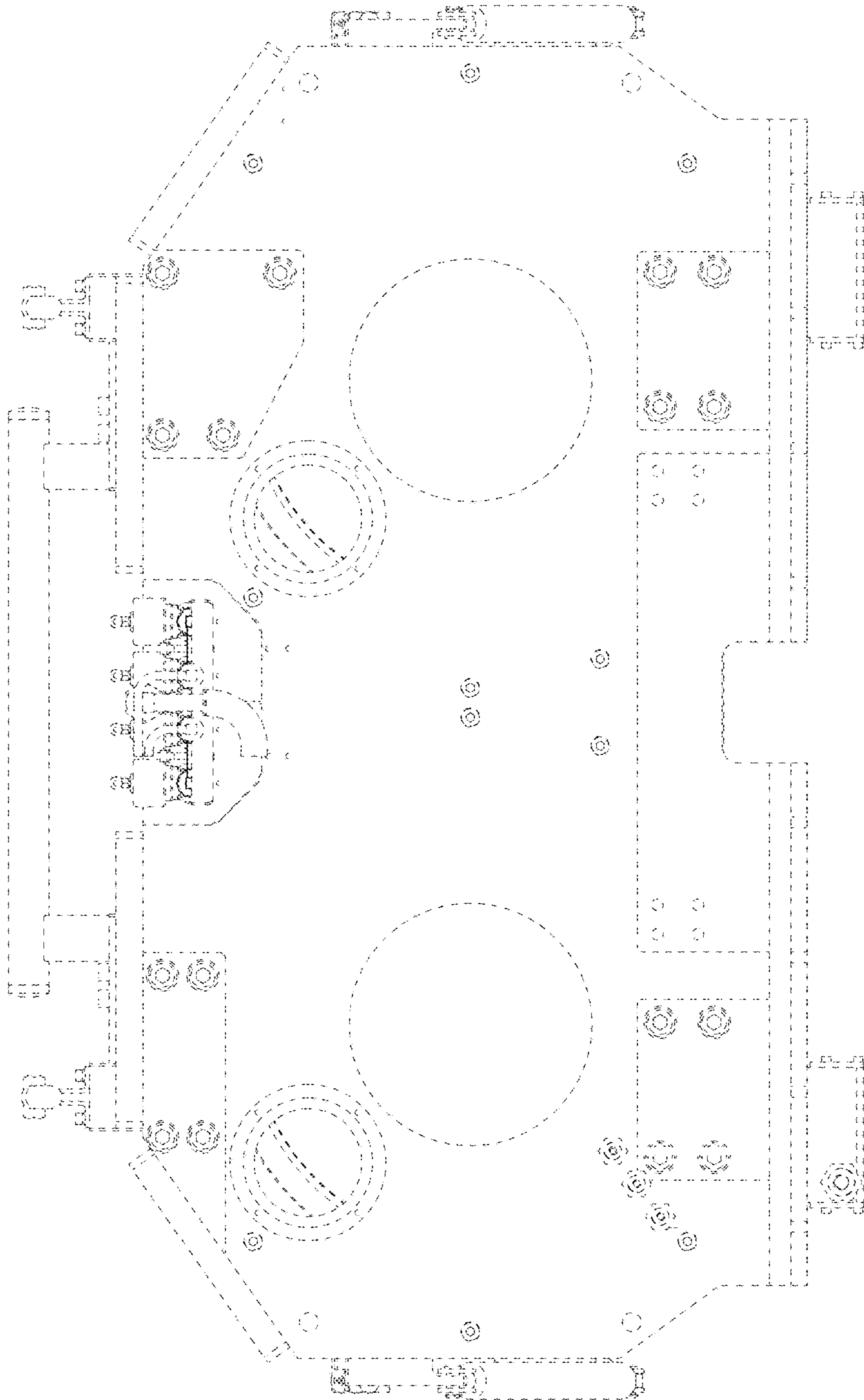


FIG. 7



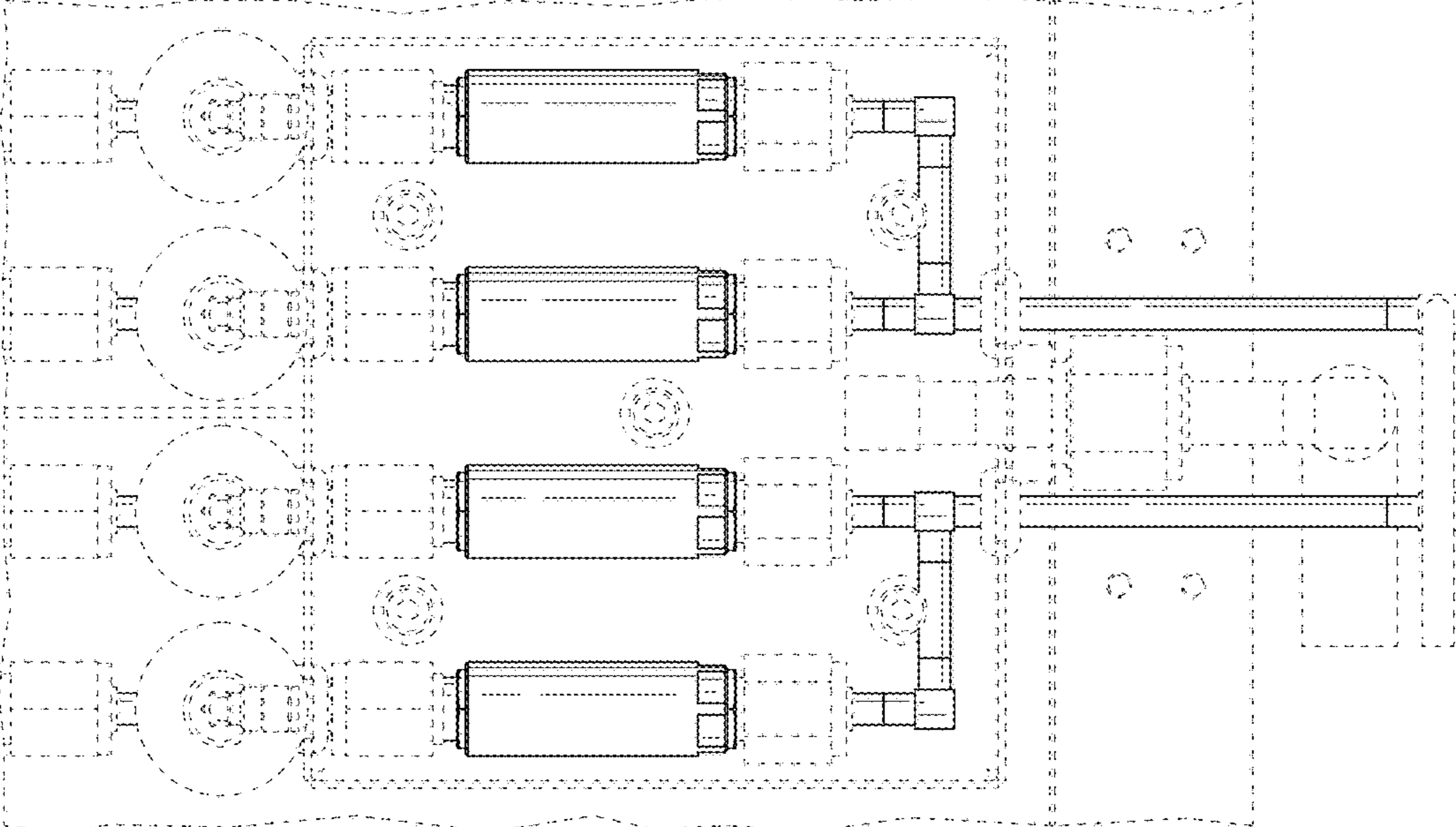


FIG. 8

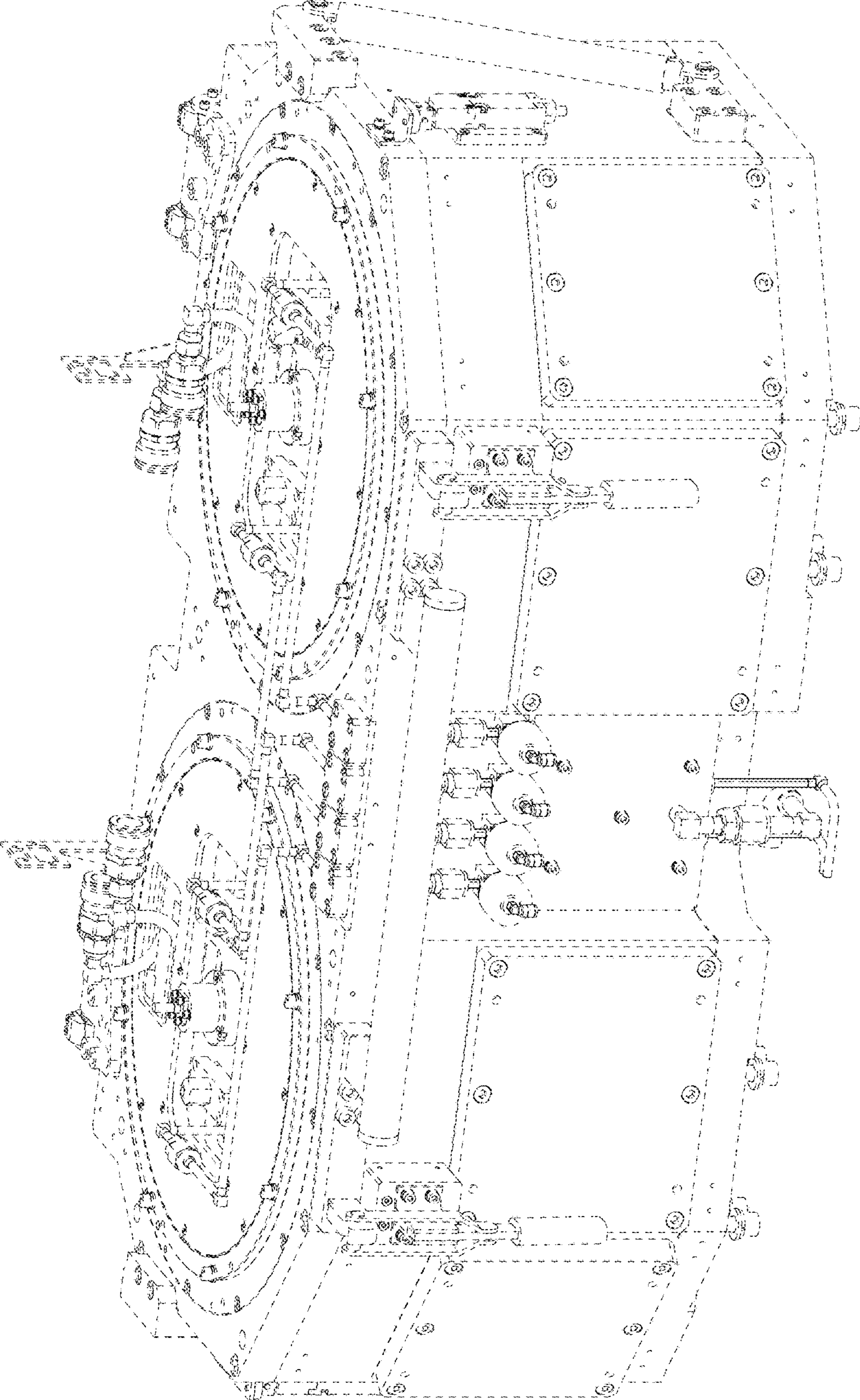


FIG. 9