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(54) **CAPACITOR**

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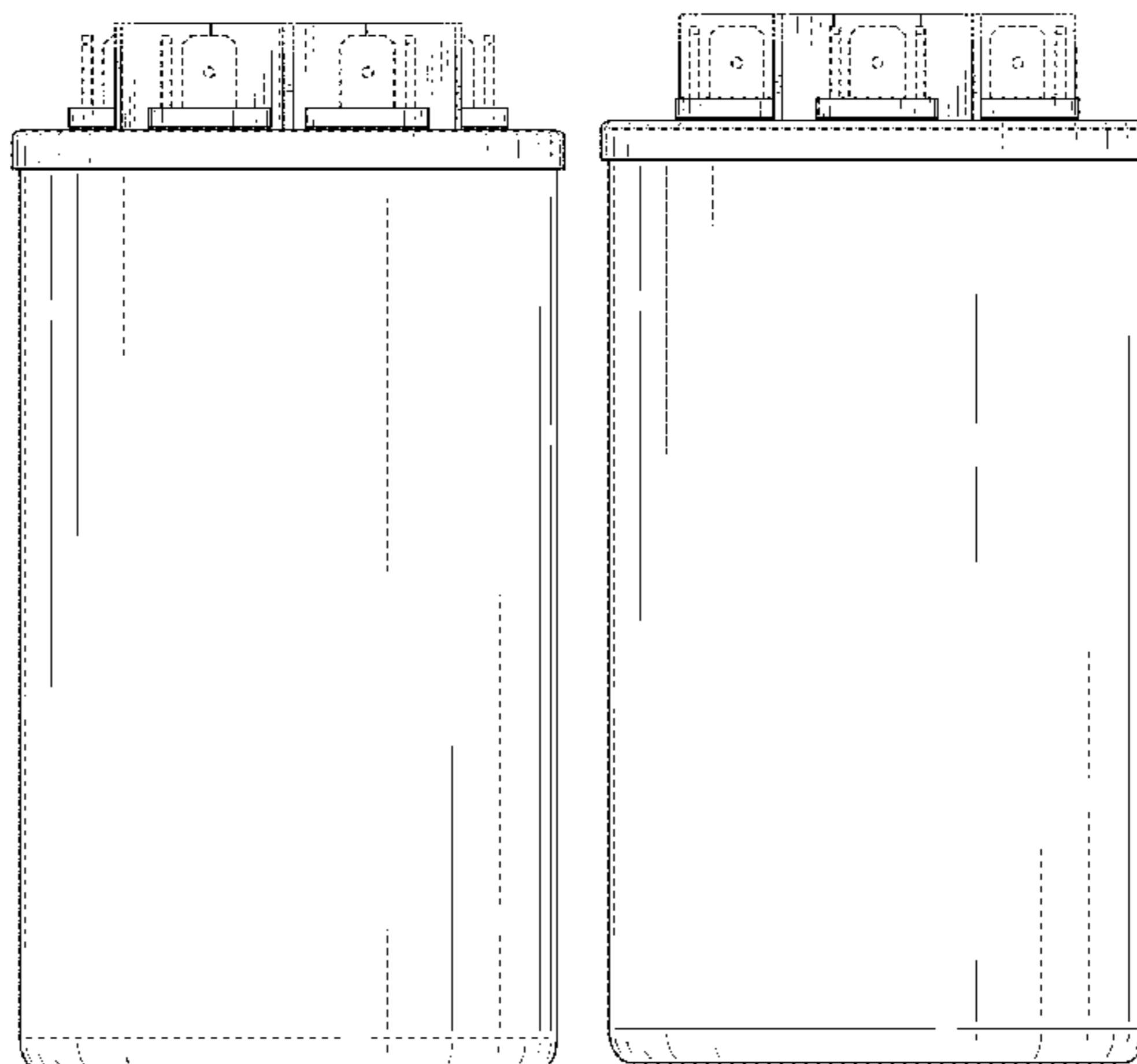
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Related U.S. Application Data

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- Photograph 2 from Defendants' First Supplemental Disclosure of Non-Infringement and Invalidity Contentions, undated (1 page).
- Photograph 3 from Defendants' First Supplemental Disclosure of Non-Infringement and Invalidity Contentions, undated (1 page).
- Photograph 4 from Defendants' First Supplemental Disclosure of Non-Infringement and Invalidity Contentions, undated (1 page).
- Photograph 5 from Defendants' First Supplemental Disclosure of Non-Infringement and Invalidity Contentions, undated (1 page).
- Photograph 6 from Defendants' First Supplemental Disclosure of Non-Infringement and Invalidity Contentions, undated (1 page).
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(57)

CLAIM

The ornamental design for a capacitor, as shown and described.

DESCRIPTION

FIG. 1 is a front top-left perspective view of a capacitor showing my new design, taken at line 1 in FIG. 6;

FIG. 2 is a front elevation view thereof, taken at line 2 in FIG. 1;

FIG. 3 is a rear elevation view thereof, taken at line 3 in FIG. 1;

FIG. 4 is a left side elevation view thereof, taken at line 4 in FIG. 1;

FIG. 5 is a right side elevation view thereof, taken at line 5 in FIG. 1;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

The short dashed broken lines represent unclaimed boundaries and the long dashed broken lines indicate environmental features and form no part of the claimed design; the dot-dash broken lines in FIGS. 1 and 6 are for reference purposes only.

1 Claim, 4 Drawing Sheets

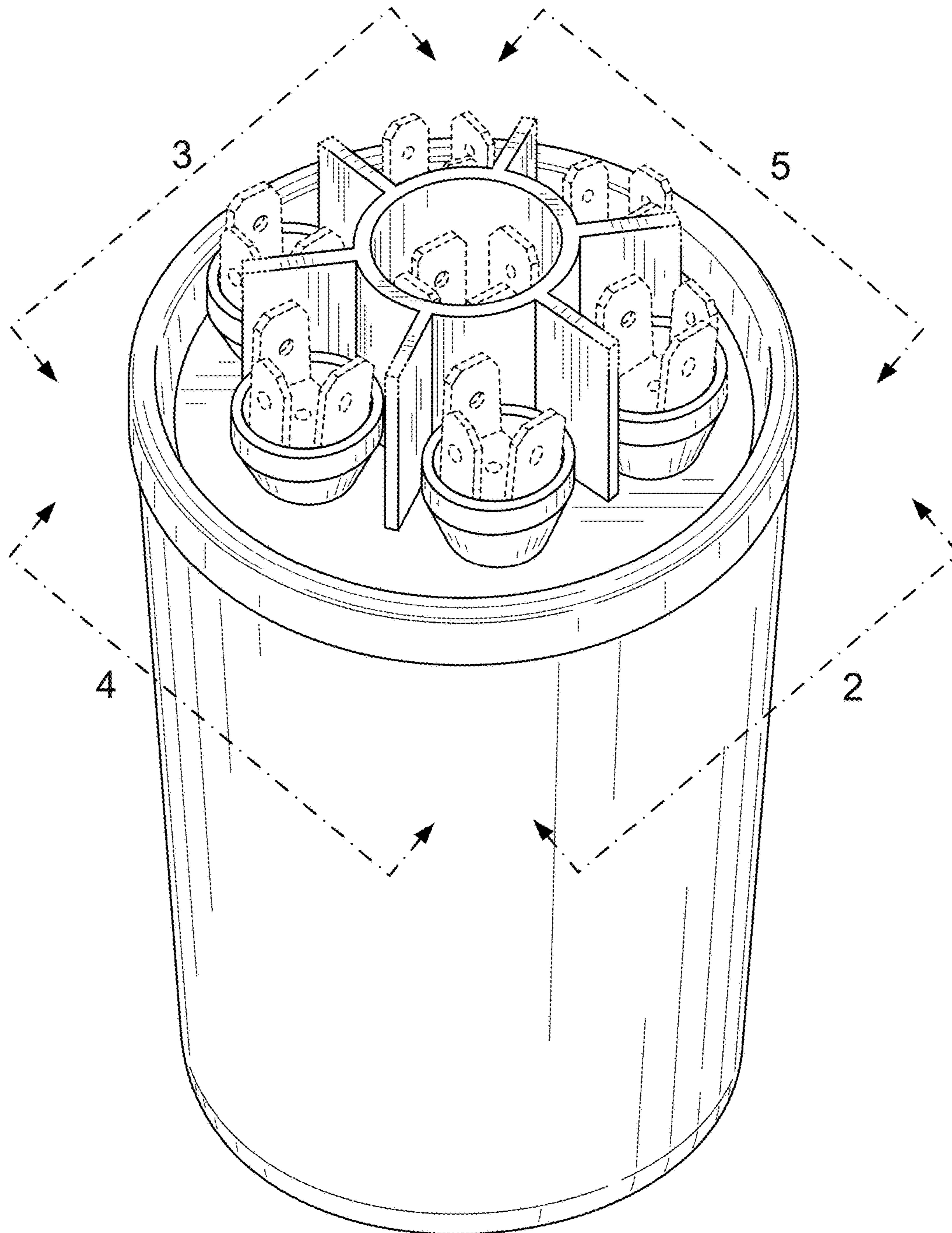


FIG. 1

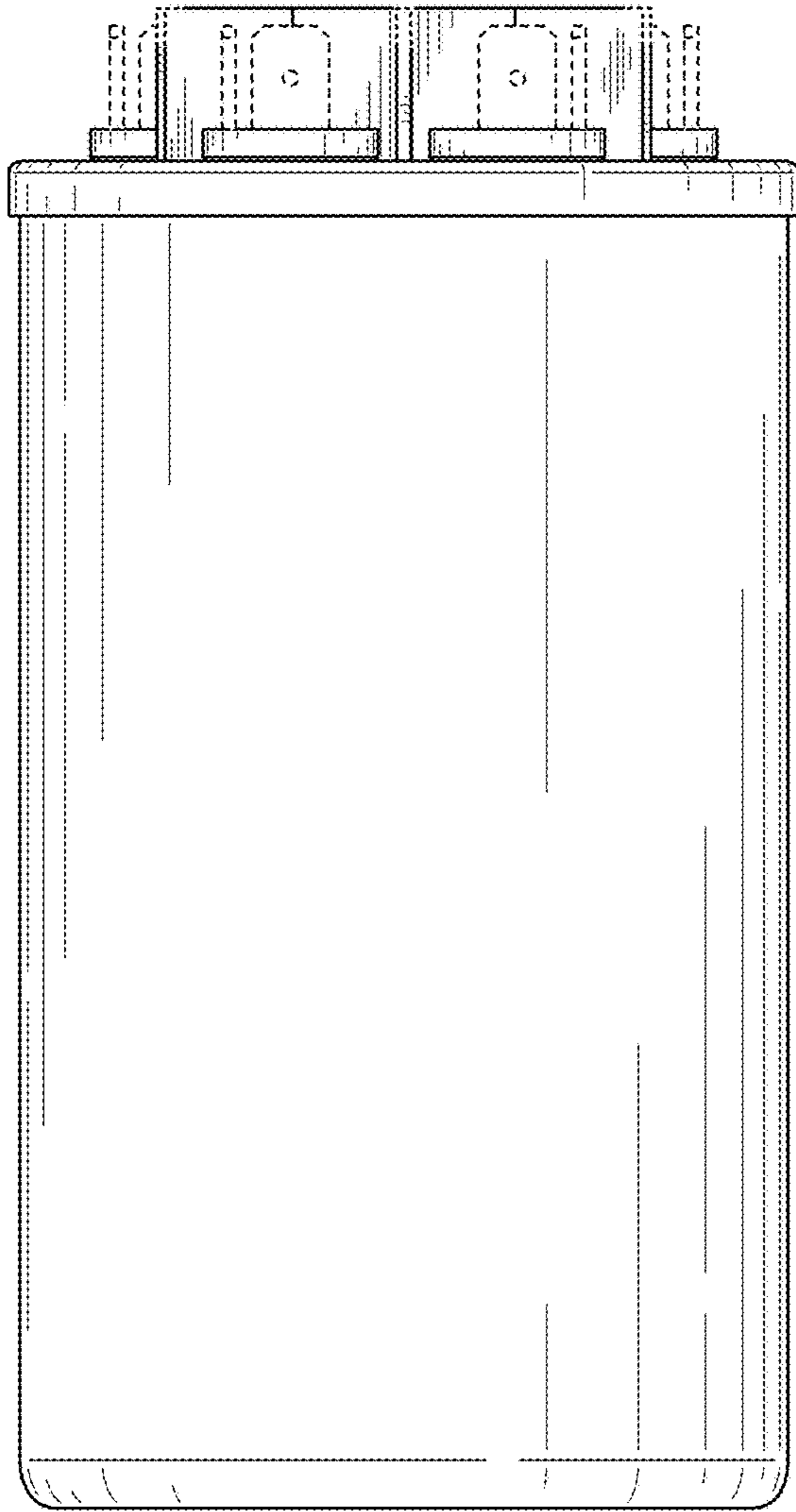


FIG. 2

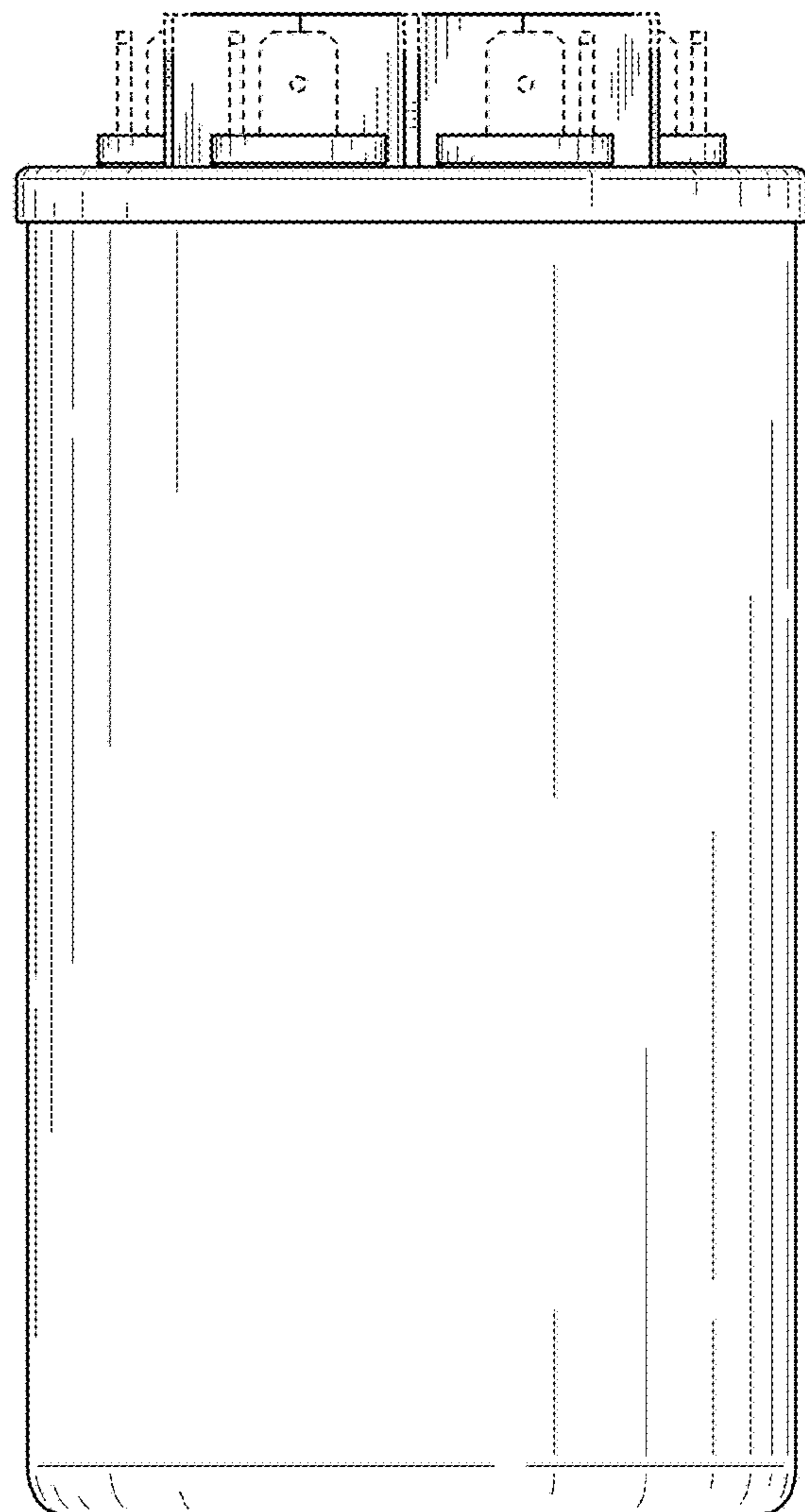


FIG. 3

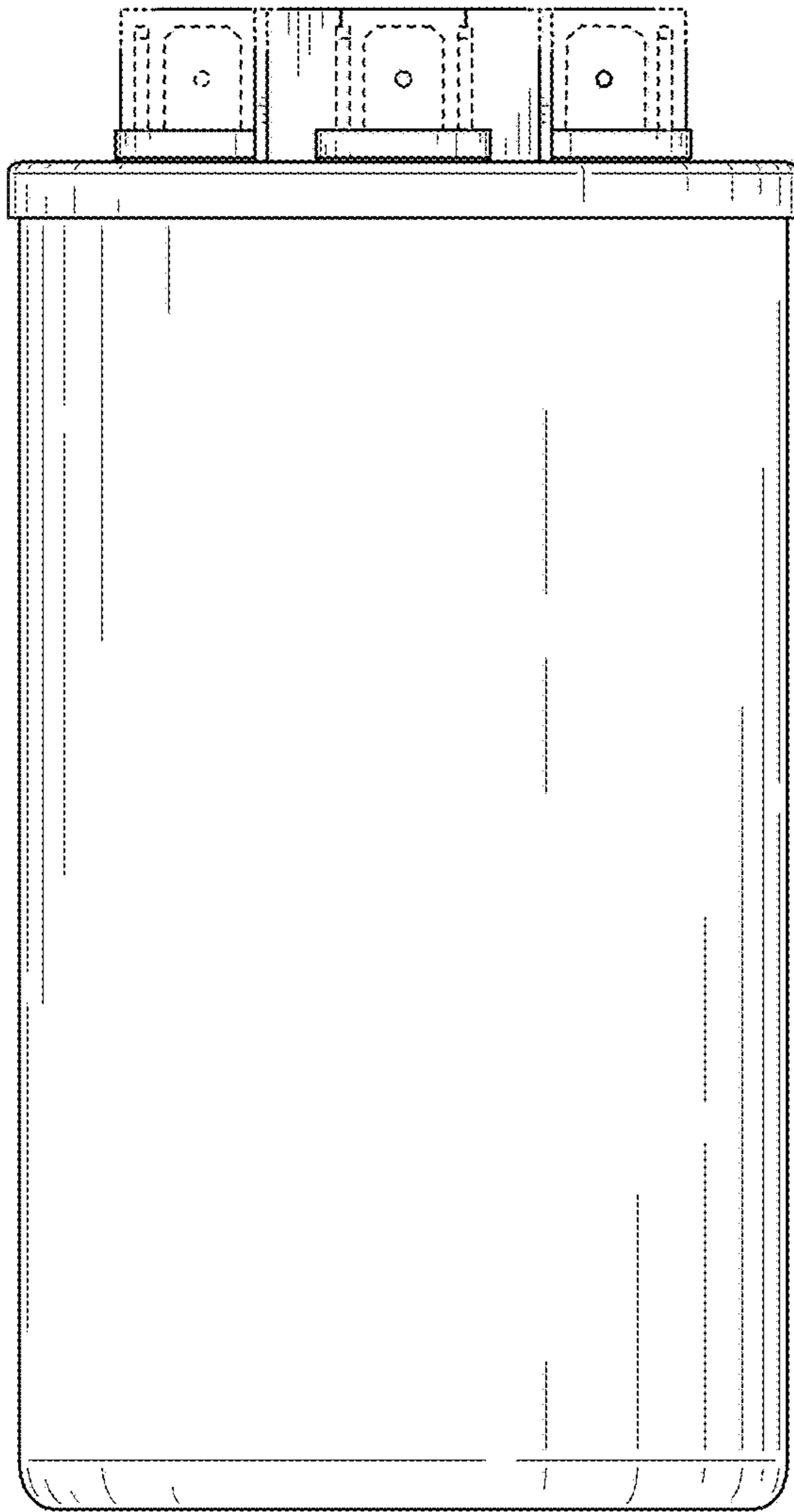


FIG. 4

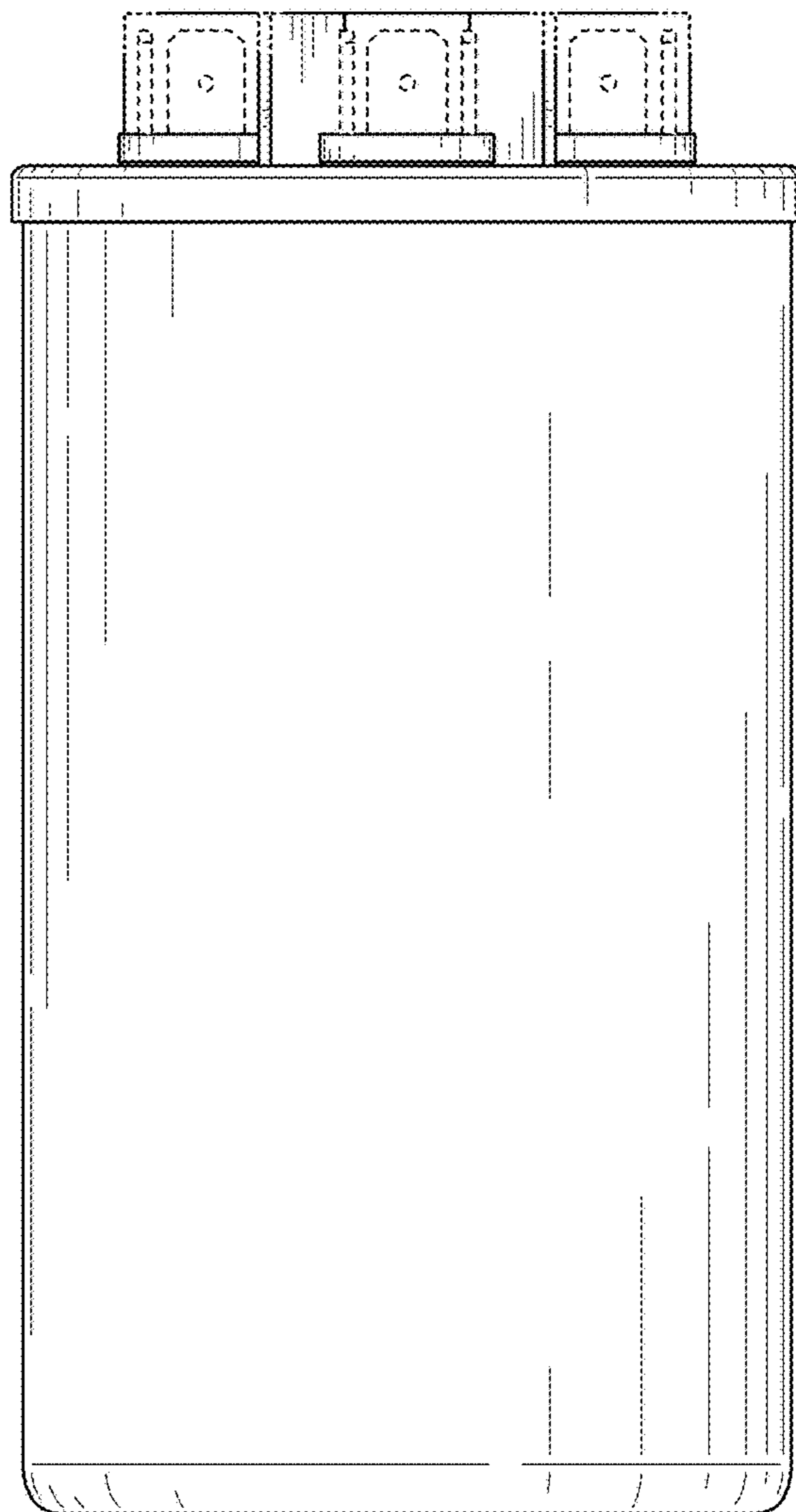


FIG. 5

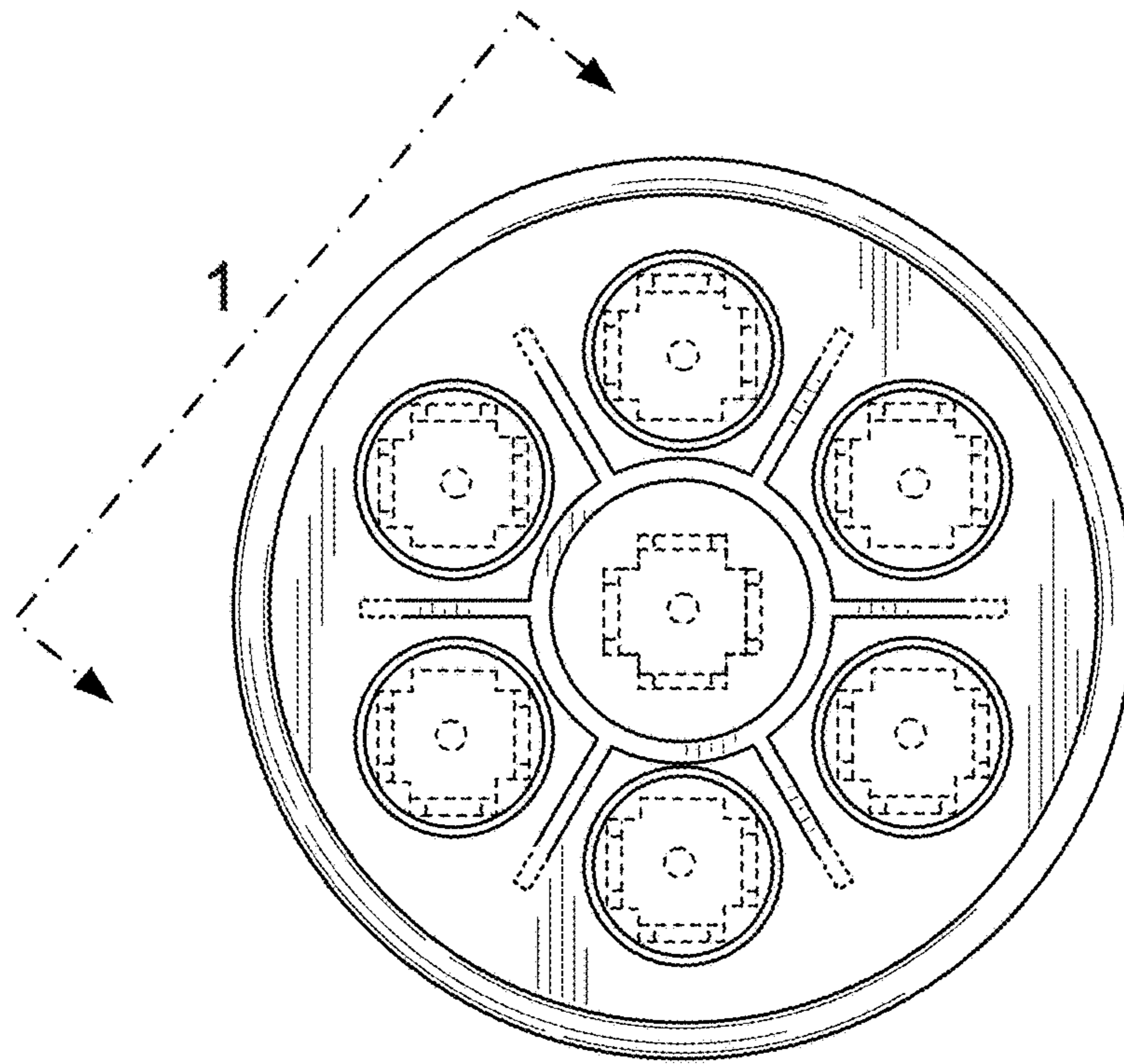


FIG. 6

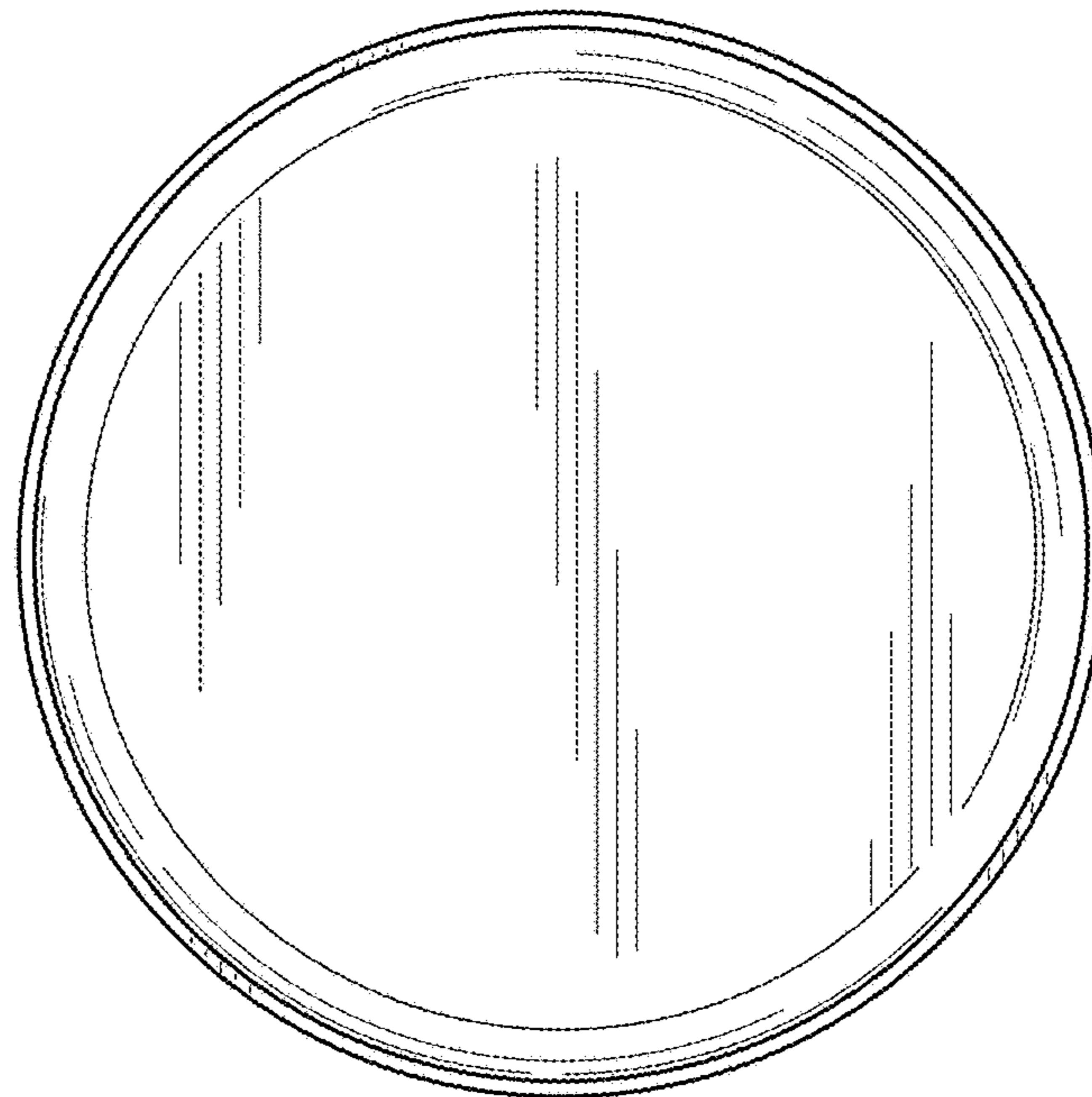


FIG. 7