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(12) **United States Design Patent** (10) **Patent No.:** **US D828,344 S**  
**Lee et al.** (45) **Date of Patent:** **\*\* Sep. 11, 2018**

(54) **ELECTRONIC DEVICE**

CPC . G06F 1/1626; H04M 1/0266; H04M 1/0264;  
H04M 1/0202

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See application file for complete search history.

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(73) Assignee: **Samsung Electronics Co., Ltd.**,  
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(\*\*) Term: **15 Years**

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**Related U.S. Application Data**

(63) Continuation of application No. 29/491,063, filed on May 16, 2014, now Pat. No. Des. 768,102.

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(74) *Attorney, Agent, or Firm* — NSIP Law

(30) **Foreign Application Priority Data**

(57) **CLAIM**

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We claim the ornamental design for an electronic device, as shown and described.

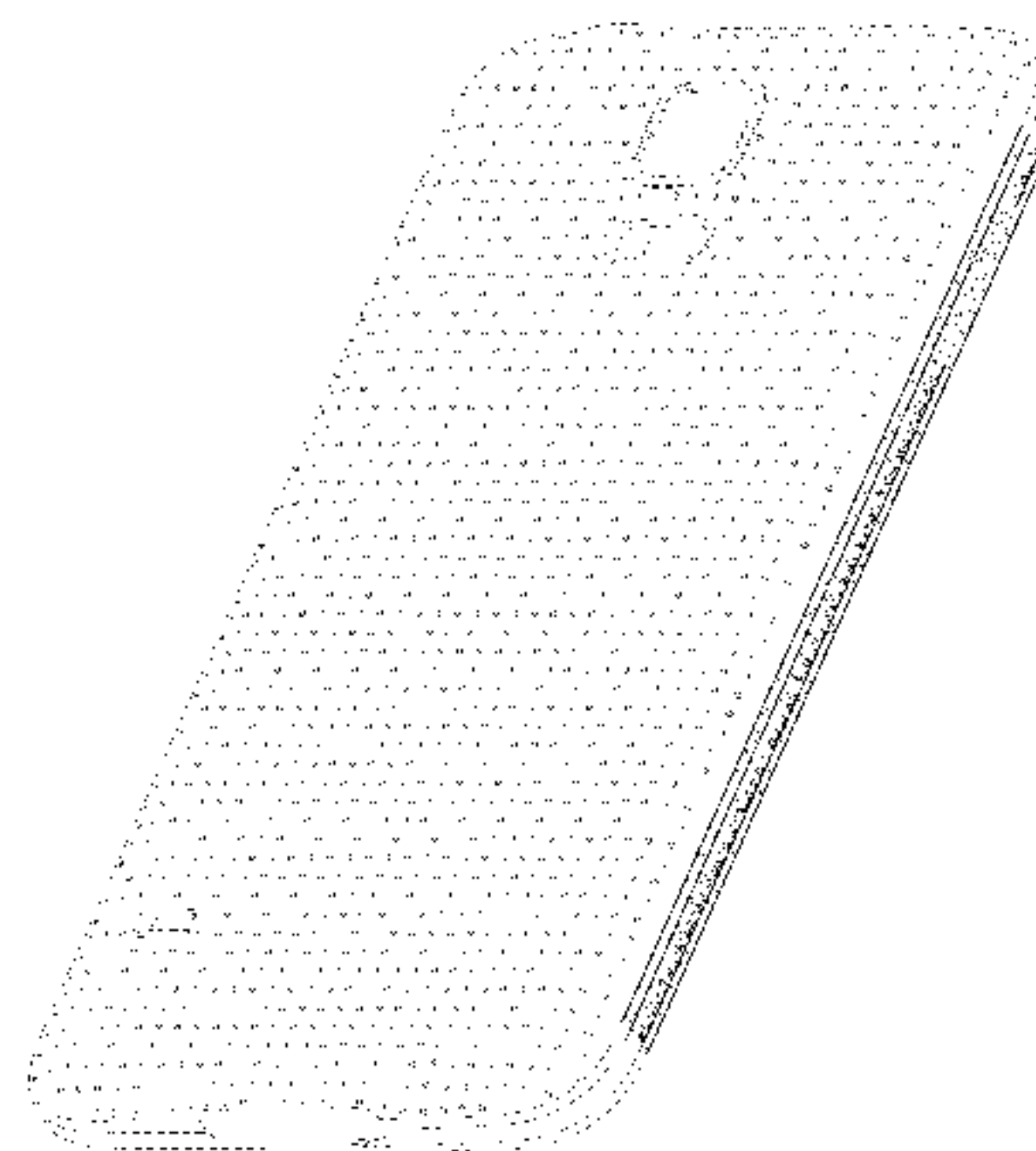
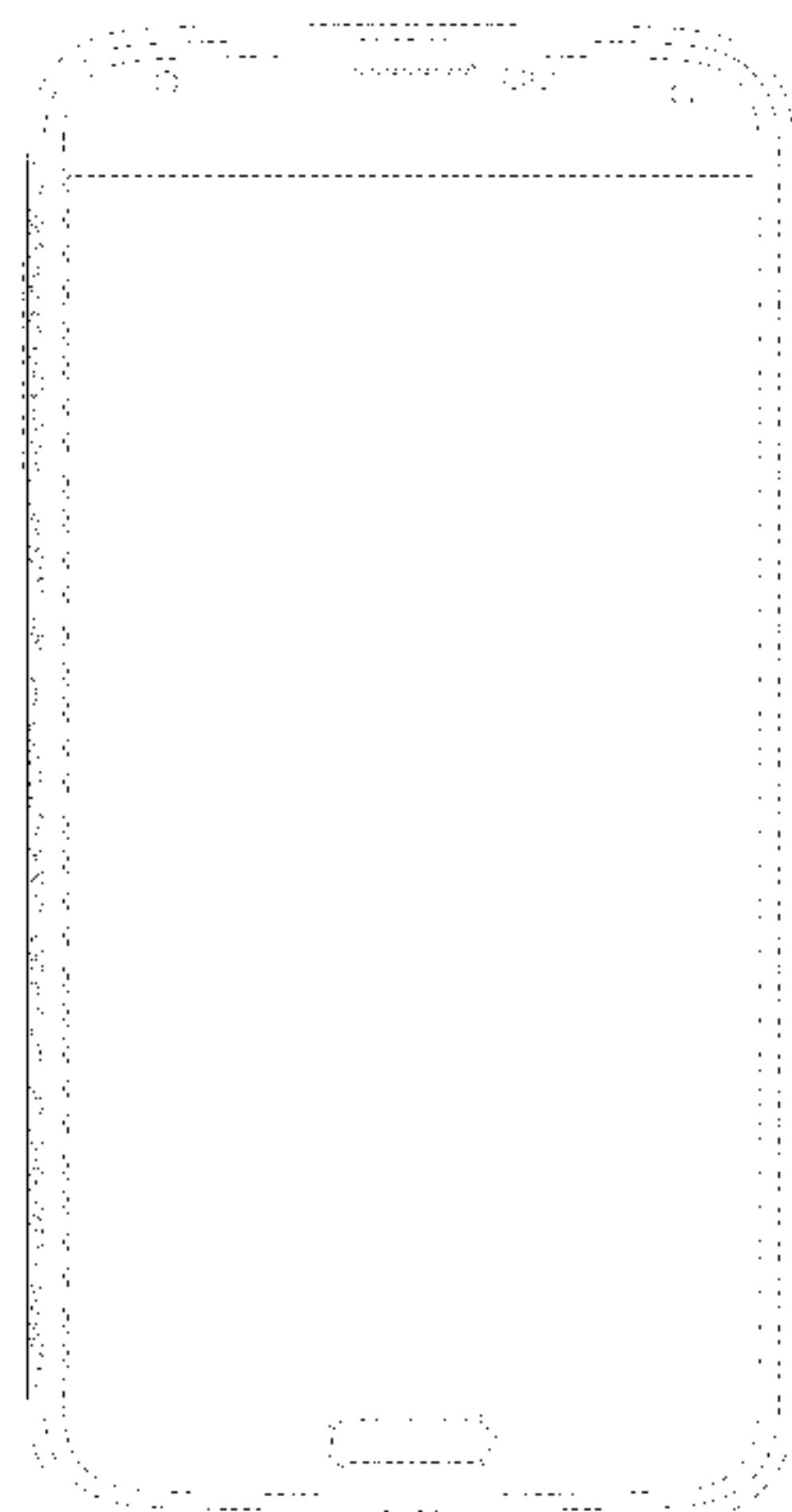
**DESCRIPTION**

(51) **LOC (11) Cl.** ..... **14-03**  
(52) **U.S. Cl.**  
USPC ..... **D14/248**

(58) **Field of Classification Search**  
USPC ..... D14/138 G, 138 AD, 341, 138 R, 496,  
D14/203.1, 203.3, 203.5, 203.7, 248;  
455/575.1, 556.2; D21/517, 329;  
361/679.3, 679.56; D25/126; D6/577

FIG. 1 is a front perspective view of an electronic device showing our new design;  
FIG. 2 is a front view thereof;  
FIG. 3 is a rear view thereof;  
FIG. 4 is a left-side view thereof;  
FIG. 5 is a right-side view thereof;  
FIG. 6 is a top view thereof;  
FIG. 7 is a bottom view thereof; and,  
FIG. 8 is a rear perspective view thereof.  
The broken lines in the figures depict environmental subject matter and form no part of the claimed design. The dense

(Continued)



stipple shading in the drawings represents the claimed design, and does not represent surface ornamentation.

**1 Claim, 6 Drawing Sheets**

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FIG.1

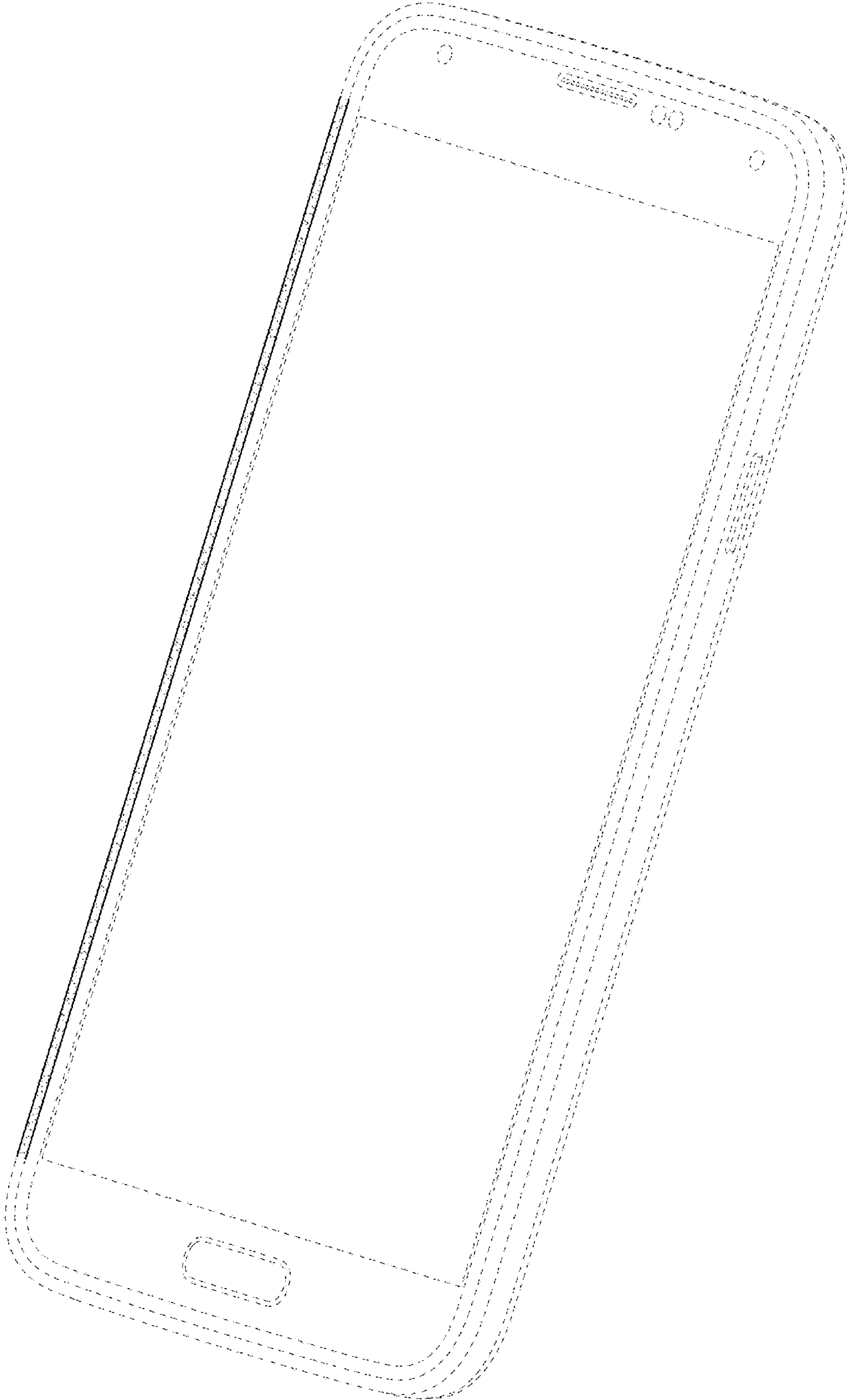


FIG.2

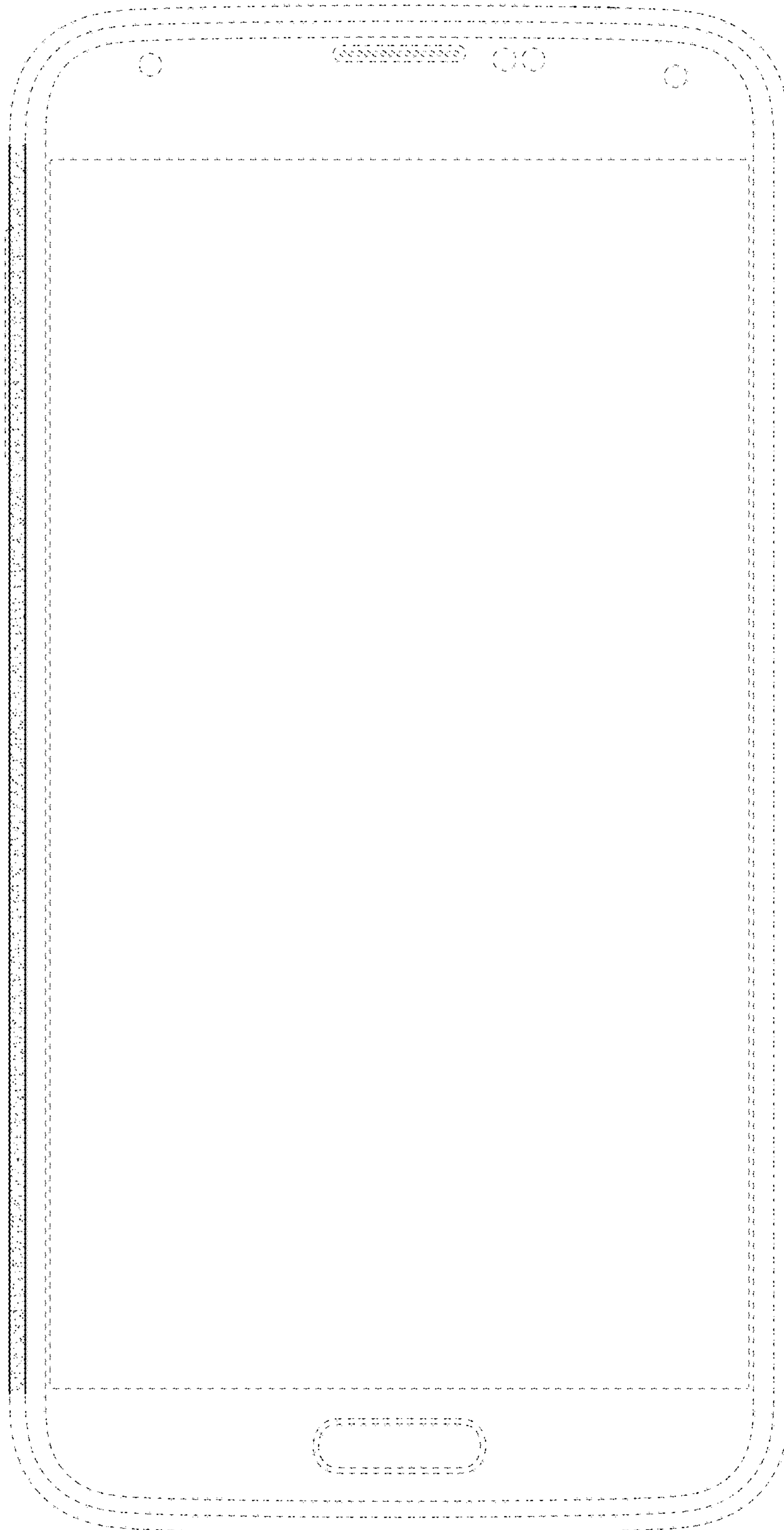




FIG.3

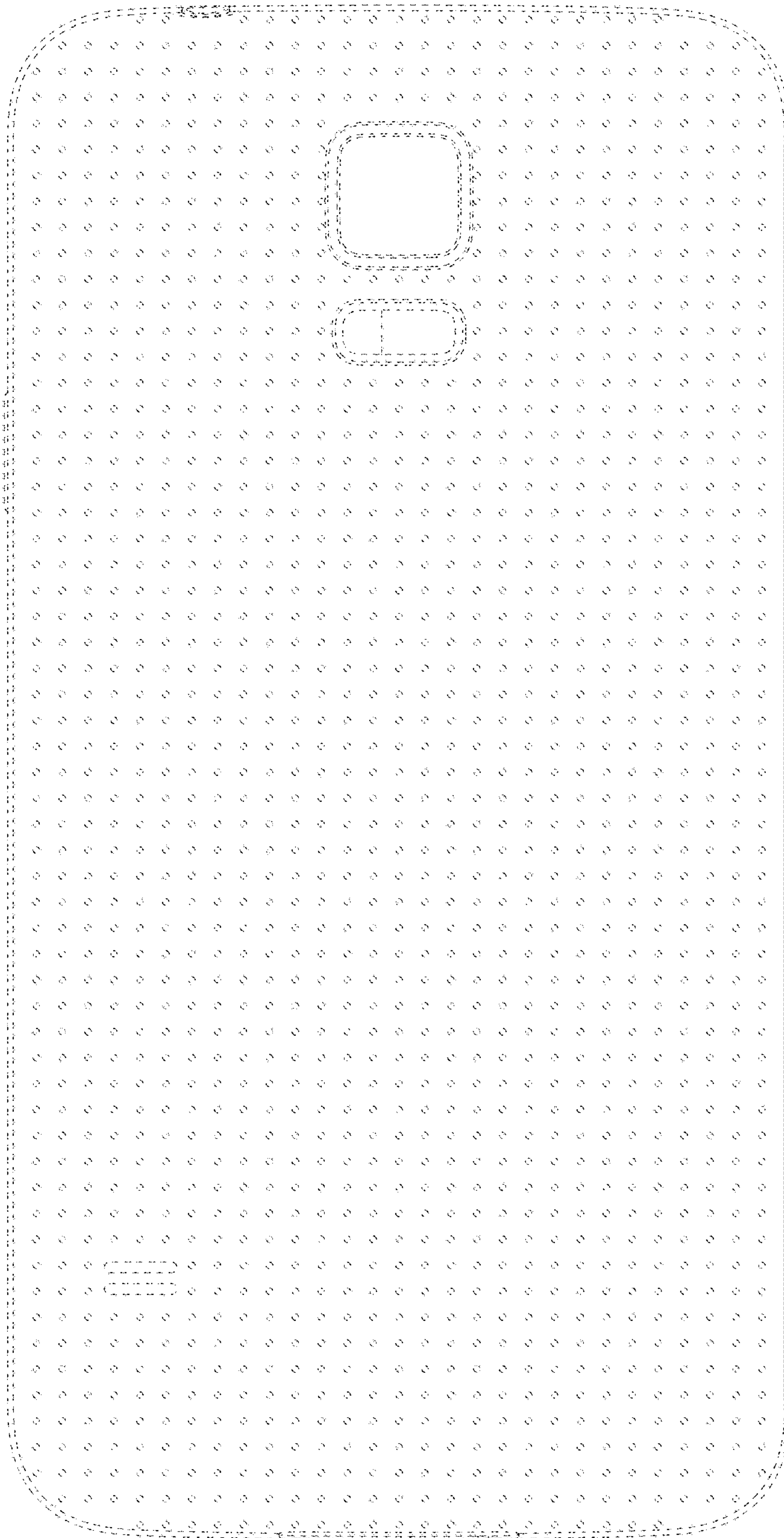


FIG.4

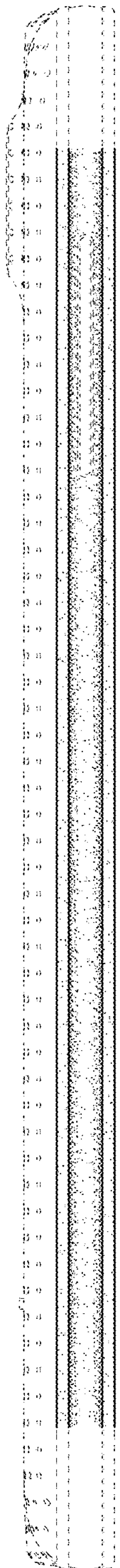


FIG.5

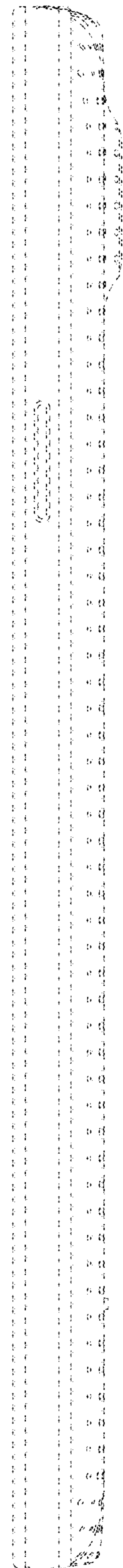


FIG.6

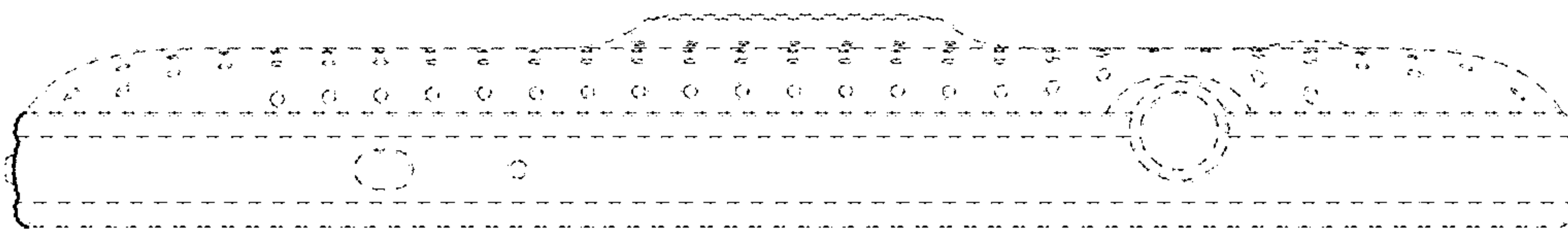


FIG.7

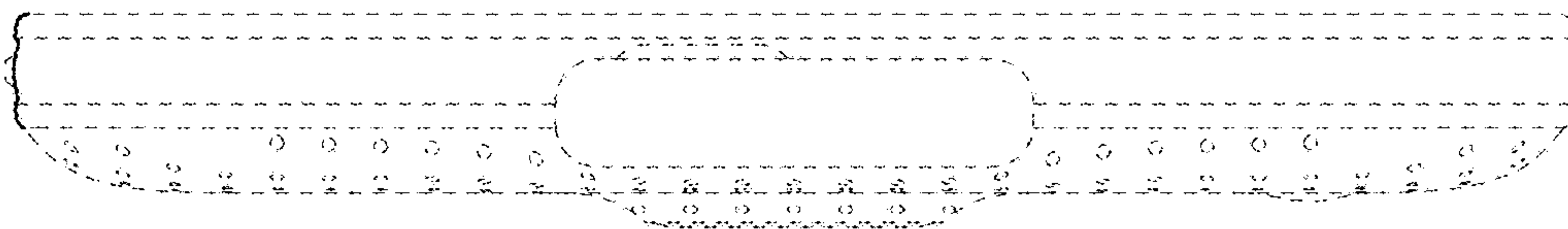


FIG.8

